

# Real-Time Systems

## Lecture 08: DC Implementables

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### Contents & Goals

- Last Lectures:**
  - (Un)decidability results for fragments of DC in discrete and continuous time.

#### This Lecture:

- Educational Objectives:** Capabilities for following tasks/questions:
  - What does this standard forms mean? Give a satisfying interpretation.
  - What are implementables? What is a control automaton?
  - Please specify (and prove correct) a controller which satisfies this requirement.

#### Content:

- DC Standard Forms
- Control Automata
- DC Implementables
- Example

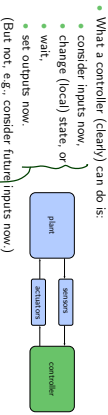
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### DC Implementables

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### Requirements vs. Implementations

- Problem:** in general, a DC requirement doesn't tell how to achieve it, how to build a controller/write a program which ensures it.



- What a controller (clearly) can do is:
  - consider inputs now,
  - change (local) state, or
  - wait,
  - set outputs now.
 (But not, e.g., consider future inputs now.)
- So, if we have
  - a DC requirement 'Req',
  - a description 'Impl' in DC, which 'uses' **just these operations**,
 then
  - proving correctness amounts to proving  $\models_{DC} \text{Impl} \implies \text{Req}$  (in DC)
  - and we (more or less) know how to program (the correct) 'Impl' in a PLC language or in C on a real-time OS, or or...

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### Approach: Control Automata and DC Implementables

#### Plan:

- Introduce **DC Standard Forms**
- Introduce **Control Automata**
- Introduce **DC Implementables** as subset of **DC Standard Forms**
- Example: a correct controller design for the notorious Gas Burner



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### DC Standard Forms: Followed-by

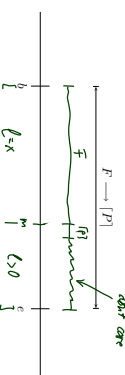
In the following,  $F$  is a DC formula,  $P$  a state assertion,  $\theta$  a rigid term.

- Followed-by:**

$$F \rightarrow [P] \iff \neg \langle F; [-P] \rangle \iff \Box \langle F; [-P] \rangle$$

In other symbols

$$\forall x \bullet \Box \langle (F \wedge \ell = x); \ell > 0 \rangle \implies (F \wedge \ell = x); [P]; \text{true}$$

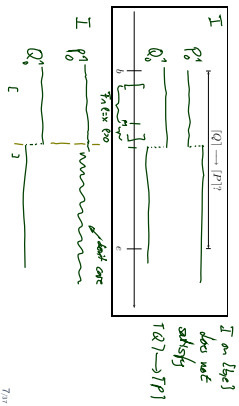


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DC Standard Forms: Followed-by Examples

$$\forall x \bullet \Box (F \wedge t = x) : t > 0 \implies (F \wedge t = x) : [P] : true$$

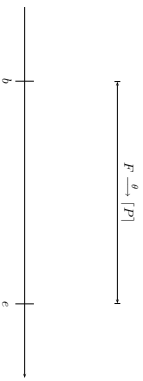


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DC Standard Forms: (Timed) leads-to

- (Timed) leads-to:  $\overset{Q \neq P!}{\implies}$

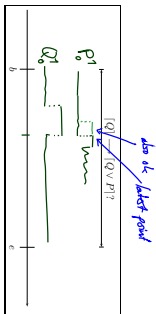
$$F \xrightarrow{\theta} [P] \iff (F \wedge t = \theta) \rightarrow [P]$$



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DC Standard Forms: Followed-by Examples

$$\forall x \bullet \Box (F \wedge t = x) : t > 0 \implies (F \wedge t = x) : [P] : true$$



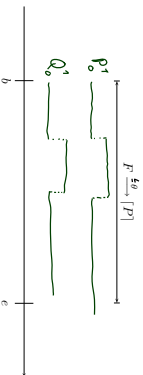
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DC Standard Forms: (Timed) up-to

- (Timed) up-to:

$$F \xrightarrow{\leq \theta} [P] \iff (F \wedge t \leq \theta) \rightarrow [P]$$

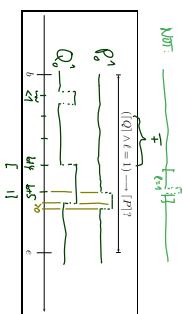


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DC Standard Forms: Followed-by Examples

$$\forall x \bullet \Box (F \wedge t = x) : t > 0 \implies (F \wedge t = x) : [P] : true$$



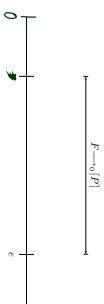
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DC Standard Forms: Initialisation

- Followed-by-initially:

$$F \rightarrow_0 [P] \iff \neg (F : \neg P)$$



- (Timed) up-to-initially:

$$F \xrightarrow{\leq \theta}_0 [P] \iff (F \wedge t \leq \theta) \rightarrow_0 [P]$$

- Initialisation:  $\bigcap \vee ([P] : true)$

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- Let  $X_1, \dots, X_k$  be  $k$  state variables ranging over finite domains  $\mathcal{D}(X_1), \dots, \mathcal{D}(X_k)$ .
- With a DC formula 'Imp' ranging over  $X_1, \dots, X_k$  we have a **system of  $k$  control automata**:
- 'Imp' is typically a conjunction of **DC implementables**.
- A state assertion of the form
 
$$X_i = d_i, \quad d_i \in \mathcal{D}(X_i),$$
 which constrains the values of  $X_i$ , is called **basic phase** of  $X_i$ .
- A **phase** of  $X_i$  is a Boolean combination of basic phases of  $X_i$ .
- Abbreviations:**
  - Write  $X_i$  instead of  $X_i = 1$ , if  $X_i$  is Boolean.
  - Write  $d_i$  instead of  $X_i = d_i$ , if  $\mathcal{D}(X_i)$  is disjoint from  $\mathcal{D}(X_j)$ ,  $i \neq j$ .

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Model of Gas Burner controller as a system of four control automata:

- $H$  Boolean, representing heat request, (input)
- $F$  Boolean, representing flame, (input)
- $C$  with  $\mathcal{D}(C) = \{\text{idle, purge, ignite, burn}\}$ , representing the (status of the) controller, (local)
- $G$  Boolean, representing gas valve, (output)

- Basic phase** of  $C$ :
 
$$C = \text{purge} \quad (\text{or only: } \text{purge})$$
- Phase** of  $C$ :
 
$$\text{purge} \vee \text{idle}$$

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- DC Implementables are special patterns of DC Standard Forms (due to A. P. Rayn)
- Within one pattern,  $\pi_1, \pi_2, \dots, \pi_n$ ,  $n \geq 0$ , denote **phases of the same state variable**  $X_i$ .
- $\varphi$  denotes a state assertion not depending on  $X_i$ .
- $\theta$  denotes a **rigid** term.

- Initialisation:**

$$\bigwedge \vee [\pi] : \text{true}$$
- Sequencing:**

$$[\pi] \longrightarrow [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$
- Progress:**

$$[\pi] \xrightarrow{\theta} [\neg\pi]$$
- Synchronisation:**

$$[\pi \wedge \varphi] \xrightarrow{\theta} [\neg\pi]$$

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- Bounded Stability:**

$$[\neg\pi] : \left[ \left[ \pi \wedge \varphi \right] \xrightarrow{\theta} [\pi \vee \pi_1 \vee \dots \vee \pi_n] \right]$$
- Unbounded Stability:**

$$[\neg\pi] : \left[ \left[ \pi \wedge \varphi \right] \longrightarrow [\pi \vee \pi_1 \vee \dots \vee \pi_n] \right]$$
- Bounded initial stability:**

$$[\pi \wedge \varphi] \xrightarrow{\theta} [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$
- Unbounded initial stability:**

$$[\pi \wedge \varphi] \longrightarrow [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$

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- Let  $X_1, \dots, X_k$  be a system of  $k$  control automata.
- Let 'Imp' be a conjunction of **DC implementables**.
- Then 'Imp' specifies all interpretations  $\mathcal{I}$  of  $X_1, \dots, X_k$  and all valuations  $\gamma$  such that
 
$$\mathcal{I} \gamma \models_0 \text{Imp}$$

- Hmm: And what does this have to do with controllers...?

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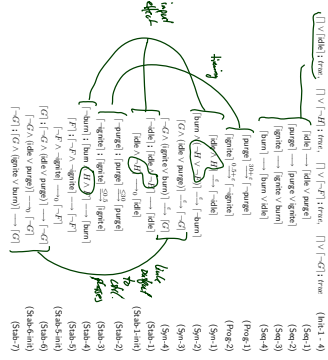
## Example: Gas Burner

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Model of Gas Burner controller as a system of four control automata:

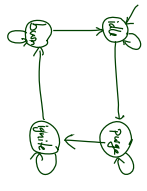
- $F$ : Boolean representing heat request.
- $F$ : Boolean representing flame.
- $C$  with  $D(C) = \{\text{idle, purge, ignite, burn}\}$ , representing the controller.
- $G$ : Boolean representing gas valve.

Gas Burner Controller Specification

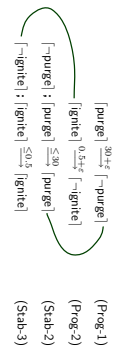


Gas Burner Controller Specification: Untimed

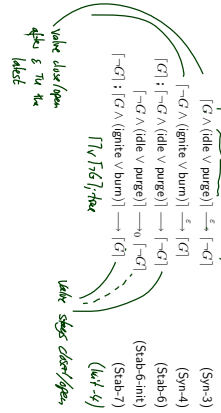
- (In1-1)  $\neg V \wedge \text{idle} : \text{true}$
- (Seq-1)  $\text{idle} \rightarrow \text{idle} \vee \text{purge}$
- (Seq-2)  $\text{purge} \rightarrow \text{purge} \vee \text{ignite}$
- (Seq-3)  $\text{ignite} \rightarrow \text{ignite} \vee \text{burn}$
- (Seq-4)  $\text{burn} \rightarrow \text{burn} \vee \text{idle}$



Gas Burner Controller Specification: Timing

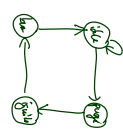


Gas Burner Controller Specification: Outputs



Gas Burner Controller Specification: Inputs

- (Syn-1)  $\text{idle} \wedge H : \text{true}$
- (Syn-2)  $\text{burn} \wedge (\neg H \vee \neg F) : \text{true}$
- (Stab-1)  $\text{idle} : \text{idle} \wedge \neg H \rightarrow \text{idle}$
- (Stab-1-init)  $\text{idle} \wedge \neg H \rightarrow \text{idle}$
- (Stab-4)  $\text{burn} : \text{burn} \wedge H \wedge F \rightarrow \text{burn}$



$$\begin{aligned}
 & \neg \vee \neg \text{Idle} : \text{true} & (\text{Init-2}) \\
 & \neg \vee \neg \text{F} : \text{true} & (\text{Init-3}) \\
 & \text{---} & \text{---} \\
 & \text{F} : \neg \text{F} \wedge \neg \text{ignite} \longrightarrow \neg \text{F} & (\text{Stab-5}) \\
 & \neg \text{F} \wedge \neg \text{ignite} \longrightarrow \neg \text{F} & (\text{Stab-5-init})
 \end{aligned}$$

*no spurious flows*

$$\text{GB-Ctrl} \equiv \text{Init-1} \wedge \dots \wedge \text{Stab-7} \wedge \epsilon > 0$$

**Recall:** Req-1  $\iff \square(\epsilon \geq 80) \implies 20 \cdot J_L \leq 0$   
 and (cf. [Olderog and Dierks, 2008]) Req-1  $\implies$  Req-2

$$\text{Req-1} := \square(\epsilon \leq 30) \implies J_L \leq 1.$$

Here we show

$$\models \text{GB-Ctrl} \wedge A(\epsilon) \implies \text{Req-1}.$$

$$\models \text{GB-Ctrl} \implies \square \left( \begin{array}{l} \text{Idle} \implies J_G \leq \epsilon \\ \text{Purge} \implies J_G \leq \epsilon \\ \text{Ignite} \implies \epsilon \leq 0.5 + \epsilon \\ \text{Burn} \implies J_{-F} \leq 2\epsilon \end{array} \right) \quad (4)$$

**Proof:** Let  $Z$  be an interpretation,  $\gamma$  a valuation, and  $[c, d]$  an interval with  $Z, \gamma, [c, d] \models \text{GB-Ctrl}$ . Let  $[b, d] \subseteq [c, d]$ .

- Case 1:  $Z, \gamma, [b, d] \models \text{Idle}$   

$$\begin{array}{l} \text{Idle} \implies J_G \leq \epsilon \\ \text{Idle} \implies \neg \text{Ignite} \implies \neg \text{F} \\ \text{Idle} \implies \neg \text{Burn} \implies \neg \text{F} \end{array}$$
- Case 2:  $Z, \gamma, [b, d] \models \text{Purge}$  Analogously to case 1.

$$\begin{array}{l} \text{Idle} \implies J_G \leq \epsilon \\ \text{Purge} \implies J_G \leq \epsilon \\ \text{Burn} \implies J_{-F} \leq 2\epsilon \end{array} \quad (6)$$

- Case 3:  $Z, \gamma, [b, d] \models \text{Ignite}$   

$$\text{Ignite} \stackrel{0.5\epsilon}{\implies} \neg \text{Ignite} \quad (\text{Prog-2})$$

$$Z, \gamma, [b, d] \models \epsilon \leq 0.5 + \epsilon$$
- Case 4:  $Z, \gamma, [b, d] \models \text{Burn}$   

$$\text{Burn} \wedge (\neg \text{Idle} \vee \neg \text{F}) \stackrel{\epsilon}{\implies} \neg \text{Burn} \quad (\text{Syn-2})$$

$$\text{F} : \neg \text{F} \wedge \neg \text{ignite} \longrightarrow \neg \text{F} \quad (\text{Stab-5})$$

$$\begin{array}{l} Z, \gamma, [b, d] \models \square(\neg \text{F}) \implies \epsilon \leq \epsilon \wedge \neg \square(\text{F} : \neg \text{F}) : \neg \text{F} \\ \text{---} \\ Z, \gamma, [b, d] \models \square(\neg \text{F}) \implies \epsilon \leq \epsilon \wedge \neg \square(\text{F} : \neg \text{F}) : \neg \text{F} \end{array}$$

$$\models \exists \epsilon \bullet \text{GB-Ctrl} \implies \square(\epsilon \leq 30) \stackrel{\text{Req-1}}{\implies} J_L \leq 1$$

**Proof Sketch**

Assume  $Z, \gamma, [b, d] \models \text{GB-Ctrl} \wedge \epsilon \leq 30$

- Distinguish 5 cases:
- $Z, \gamma, [b, d] \models \neg \text{Ignite}$   

$$\forall \text{Idle} : \text{true} \wedge \epsilon \leq 30 \quad (1)$$
  - $Z, \gamma, [b, d] \models \text{Ignite}$   

$$\forall \text{Ignite} : \text{true} \wedge \epsilon \leq 30 \quad (2)$$
  - $Z, \gamma, [b, d] \models \text{Purge}$   

$$\forall \text{Purge} : \text{true} \wedge \epsilon \leq 30 \quad (3)$$
  - $Z, \gamma, [b, d] \models \text{Burn}$   

$$\forall \text{Burn} : \text{true} \wedge \epsilon \leq 30 \quad (4)$$

- Case 0:  $Z, \gamma, [b, d] \models \neg \text{Ignite}$  ✓
- Case 1:  $Z, \gamma, [b, d] \models \text{Idle} : \text{true} \wedge \epsilon \leq 30$

$$\begin{array}{l} \text{Idle} \implies \text{Idle} \vee \text{Purge} \\ \neg \text{Purge} : \text{true} \implies \text{Purge} \stackrel{\leq 30}{\implies} \text{Purge} \end{array} \quad (\text{Seq-1})$$

$$\begin{array}{l} \text{Idle} \implies \text{Idle} \vee \text{Purge} \\ \text{Ignite} \implies \text{Ignite} \vee \text{Purge} \\ \text{Burn} \implies \text{Burn} \vee \text{Purge} \end{array} \quad (\text{Stab-2})$$

Thus  $\epsilon \leq 0.5$  is sufficient for Req-1 in this case.



## References

Oleberg and Dierks, 2008] Oleberg, E.-R. and Dierks, H. (2008). *Real-Time Systems - Formal Specification and Automatic Verification*. Cambridge University Press.