

Real-Time Systems

Lecture 15: The Universality Problem for TBA

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Contents & Goals

- Last Lecture:
 - Extended Timed Automata

This Lecture:

- Educational Objectives: Capabilities for following tasks/questions
 - What's a TBA and what's the difference to (extended) TA?
 - What's undecidable for timed (Büchi) automata?
 - What's the idea of the proof?

Content:

- Uppaal Query Language
- Timed Büchi Automata and timed regular languages [Alur and Dill, 1994].
- The Universality Problem is undecidable for TBA [Alur and Dill, 1994]
- Why this is unfortunate.
- Timed regular languages are not everything.

The Logic of Uppaal

The Uppaal Fragment of Timed Computation Tree Logic

Consider $N = C(A_1, \dots, A_n)$ over data variables V .

- basic formulae:
 - $atom ::= A_i.f \mid \varphi$
 - where $f \in A_i$ is a location and φ a constraint over X_i and V .
- configuration formulae:
 - $term ::= atom \mid \neg term \mid term_1 \wedge term_2$
 - $c\text{-formula} ::= \exists \exists term \mid \exists \exists term$
 - (“exists finally”, “exists globally”)
- existential path formulae:
 - $term ::= atom \mid \neg term \mid term_1 \wedge term_2$
 - $c\text{-formula} ::= \exists \exists term \mid \exists \exists term$
 - (“always finally”, “always globally”, “leads to”)
- universal path formulae:
 - $a\text{-formula} ::= \forall \forall term \mid \forall \forall term \mid term_1 \text{ --- } term_2$
 - $F ::= c\text{-formula} \mid a\text{-formula}$

Configurations at Time t

- Recall: computation path (cp/path) starting in (\bar{c}_0, v_0, t_0)
 - $\xi = \langle (\bar{c}_0, v_0), t_0 \rangle \xrightarrow{\Delta_1} \langle (\bar{c}_1, v_1), t_1 \rangle \xrightarrow{\Delta_2} \langle (\bar{c}_2, v_2), t_2 \rangle \xrightarrow{\Delta_3} \dots$
 - which is infinite or maximally finite.

- Given ξ and $t \in \text{Time}$, we use $\xi(t)$ to denote the set
 - $\{(\bar{c}, v) \mid \exists t' \in \mathbb{N}_0 : t' \leq t \leq t_{i+1} \wedge \bar{c}_i \wedge v = \bar{c}_i \wedge v_i + t - t_i\}$

of configurations at time t .

- Why is it a set?
 - Can it be empty?
 - $\xi(0) = \{(\bar{c}_0, v_0)\}$
 - $\xi(0.2) = \{(\bar{c}_0, v_0 + 0.2)\}$
 - $\xi(3.0) = \{(\bar{c}_2, v_2)\}$

Satisfaction of Uppaal-Logic by Configurations

- We define a satisfaction relation
 - $\langle \bar{c}, v \rangle, t_0 \models F$
 - between time stamped configurations

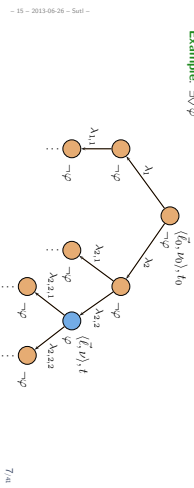
of a network $C(A_1, \dots, A_n)$ and formulae F of the Uppaal logic.

- It is defined inductively as follows:
 - $\langle \bar{c}, v \rangle, t_0 \models A_i.f$ iff $\bar{c}_i = f$
 - $\langle \bar{c}, v \rangle, t_0 \models \varphi$ iff $\bar{c}_i = \varphi$
 - $\langle \bar{c}, v \rangle, t_0 \models \neg term$ iff $\langle \bar{c}, v \rangle, t_0 \not\models term$
 - $\langle \bar{c}, v \rangle, t_0 \models term_1 \wedge term_2$ iff $\langle \bar{c}, v \rangle, t_0 \models term_1 \wedge \langle \bar{c}, v \rangle, t_0 \models term_2$

Satisfaction of Uppal-Logic by Configurations

Exists finally:

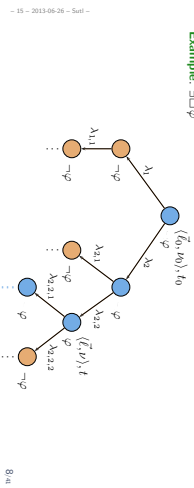
- $\langle \vec{r}_0, v_0 \rangle, t_0 \models \exists \square \text{ term}$ iff $\exists \text{ path } \xi \text{ of } N \text{ starting in } \langle \vec{r}_0, v_0 \rangle, t_0$
 $\exists t \in \text{Time}, \langle \vec{r}, v \rangle \in \text{Conf} : t_0 \leq t \wedge (\langle \vec{r}, v \rangle \in \exists \square \wedge (\langle \vec{r}, v \rangle, t) \models \text{term})$



Satisfaction of Uppal-Logic by Configurations

Exists globally:

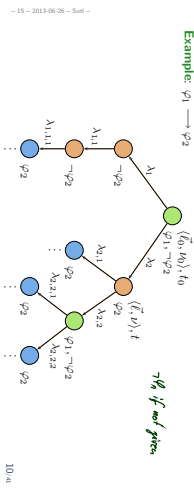
- $\langle \vec{r}_0, v_0 \rangle, t_0 \models \exists \square \text{ term}$ iff $\exists \text{ path } \xi \text{ of } N \text{ starting in } \langle \vec{r}_0, v_0 \rangle, t_0$
 $\forall t \in \text{Time}, \langle \vec{r}, v \rangle \in \text{Conf} : t_0 \leq t \wedge (\langle \vec{r}, v \rangle \in \exists \square \implies \langle \vec{r}, v \rangle, t \models \text{term})$



Satisfaction of Uppal-Logic by Configurations

Leads to:

- $\langle \vec{r}_0, v_0 \rangle, t_0 \models \text{term}_2$ iff term_2 iff $\text{path } \xi \text{ of } N \text{ starting in } \langle \vec{r}_0, v_0 \rangle, t_0$
 $\forall t \in \text{Time}, \langle \vec{r}, v \rangle \in \text{Conf} : t_0 \leq t \wedge (\langle \vec{r}, v \rangle \in \xi(t))$
 $\wedge (\langle \vec{r}, v \rangle, t) \models \text{term}_1$
 $\implies (\langle \vec{r}, v \rangle, t) \models \forall \square \text{ term}_2$



Satisfaction of Uppal-Logic by Networks

We write

- $N \models a\text{-formula}$
if and only if
for some $\langle \vec{r}_0, v_0 \rangle \in C_{\text{init}}, \langle \vec{r}_0, v_0 \rangle, 0 \models a\text{-formula}$,
and
 $N \models a\text{-formula}$

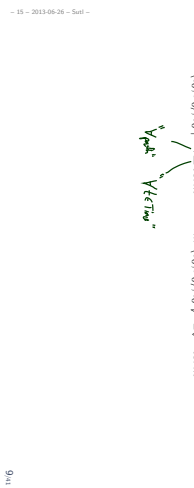
if and only if
for all $\langle \vec{r}_0, v_0 \rangle \in C_{\text{init}}, \langle \vec{r}_0, v_0 \rangle, 0 \models a\text{-formula}$,
where C_{init} are the initial configurations of $\mathcal{T}_c(N)$.

- If $C_{\text{init}} = \emptyset$, (1) is a contradiction and (2) is a tautology.
- If $C_{\text{init}} \neq \emptyset$, then
 $N \models F$ if and only if $\langle \vec{r}_{\text{init}}, v_{\text{init}} \rangle, 0 \models F$.

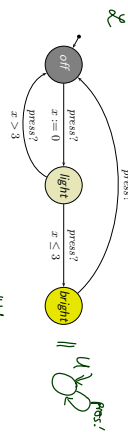
Satisfaction of Uppal-Logic by Configurations

Always finally:

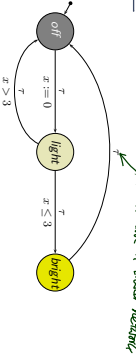
- $\langle \vec{r}_0, v_0 \rangle, t_0 \models \forall \square \text{ term}$ iff $\forall \text{ path } \xi \text{ of } N \text{ starting in } \langle \vec{r}_0, v_0 \rangle, t_0$
 $\exists t \in \text{Time}, \langle \vec{r}, v \rangle \in \text{Conf} : t_0 \leq t \wedge (\langle \vec{r}, v \rangle \in \forall \square \wedge (\langle \vec{r}, v \rangle, t) \models \text{term})$



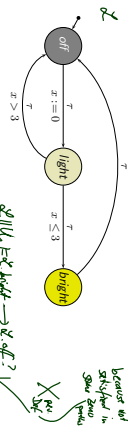
Example



Example

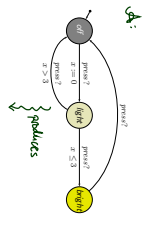


Example

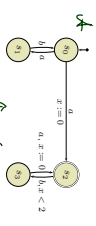


- $N \models \exists x \text{ C.bright} \checkmark$
- $N \models \exists x \text{ C.bright} X$ (we must be in light before bright)
- $N \models \exists x \text{ C.off} \checkmark$ (A1)
- $N \models \forall x \text{ C.light} X$ (because (A))
- $N \models \forall x (\text{C.bright} \Rightarrow x \geq 3) X$ (we have 2 steps and $x < 3$)
- $N \models \text{C.bright} \rightarrow \text{C.off} X$

... vs. Timed Automata



$\xi = (\text{off}, 0), 0 \perp, (\text{off}, 1), 1$
 $\xrightarrow{\text{progress}} (\text{light}, 0), 1 \perp, (\text{light}, 3), 4$
 $\xrightarrow{\text{progress}} (\text{bright}, 3), 4 \perp, \dots$
 ξ is a computation path and run of \mathcal{A} .



New: Given a timed word $(a, 1), (b, 2), (a, 3), (b, 4), (a, 5), (b, 6), \dots$ does \mathcal{A} accept it?
 New: acceptance criterion is visiting accepting state infinitely often.

Timed Languages

Definition. A time sequence $\tau = \tau_1, \tau_2, \dots$ is an infinite sequence of time values $\tau_i \in \mathbb{R}_{>0}$, satisfying the following constraints:
 (i) Monotonicity: τ increases strictly monotonically, i.e. $\tau_i < \tau_{i+1}$ for all $i \geq 1$.
 (ii) Progress: For every $t \in \mathbb{R}_{>0}$, there is some $i \geq 1$ such that $\tau_i > t$.

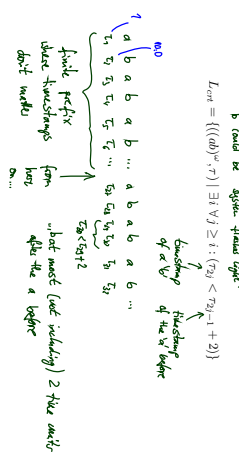
Definition. A timed word over an alphabet Σ is a pair (σ, τ) where
 $\sigma = \sigma_1, \sigma_2, \dots \in \Sigma^*$ is an infinite word over Σ , and
 τ is a time sequence.

Definition. A timed language over an alphabet Σ is a set of timed words over Σ .

Timed Buchi Automata

Alur and Dill, 1994

Timed word over alphabet Σ is a pair (σ, τ) where
 $\sigma = \sigma_1, \sigma_2, \dots$ is an infinite word over Σ , and
 τ is a time sequence (strictly (1) monotonic, non-zero)



Timed Buchi Automata

Definition. The set $\Phi(X)$ of clock constraints over X is defined inductively by

$$\delta ::= x \leq c \mid c \leq x \mid \neg \delta \mid \delta_1 \wedge \delta_2$$

where $x \in X$ and $c \in \mathbb{Q}$ is a rational constant.

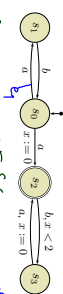
not empty! (neg, and, but, implies)

Definition. A timed Buchi automaton (TBA) \mathcal{A} is a tuple $(\Sigma, S, S_0, X, E, F)$, where

- Σ is an alphabet,
 - S is a finite set of states, $S_0 \subseteq S$ is a set of start states,
 - X is a finite set of clocks, and
 - $E \subseteq S \times S \times \Sigma \times 2^X \times \Phi(X)$ gives the set of transitions.
- An edge $(s, s', a, \lambda, \delta)$ represents a transition from state s to state s' on input symbol a . The set $\lambda \subseteq X$ gives the clocks to be reset with this transition, and δ is a clock constraint over X .
- $F \subseteq S$ is a set of **accepting states**.

Example: TBA

$\mathcal{A} = (\Sigma, S, S_0, X, E, F)$
 $(a, s', a, \lambda, \delta) \in E$



- $\Sigma^* = \{a, b\}$
- $S = \{s_1, s_0, s_2, s_3\}$
- $S_0 = \{s_1\}$
- $K = \{s_0\}$
- $F = \{s_0, s_2\}$
- $\Theta = \{ (s_0, s_0, a, \emptyset, x=0), (s_0, s_2, a, \emptyset, x \leq 2), (s_2, s_3, a, \emptyset, x=0), (s_3, s_1, b, \emptyset, x \leq 2) \}$

References

[Alur and Dill, 1994] Alur, R. and Dill, D. L. (1994). A theory of timed automata. *Theoretical Computer Science*, 120(2):183-235.

[Olderog and Dierks, 2008] Olderog, E.-R. and Dierks, H. (2008). *Real-Time Systems - Formal Specification and Automatic Verification*. Cambridge University Press.

References