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Real-Time Systems

Lecture 05: Duration Calculus III

- DC Syntax and Semantics: Terms, Formulae
- Educational Objectives: Capabilities for following tasks/questions.
- Real (and at best also write) Duration Calculus Formulae – including abbreviations.
- What is Validity/Satisfiability/Realisability for DC formulae?
- How can we prove a design correct?

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Formulate: Remarks

We will introduce three (or five) syntactical "levels":

- (i) **Symbols:**
 $f, g, \text{ true}, \text{false}, =, <, >, \leq, \geq, \quad x, y, z, \quad X, Y, Z, \quad d$
- (ii) **State Assertions:**
 $P ::= 0 \mid 1 \mid X = d \mid \neg P_1 \mid P_1 \wedge P_2$
- (iii) **Terms:**
 $\theta ::= x \mid \ell \mid f P \mid f(\theta_1, \dots, \theta_n)$
- (iv) **Formulae:**
 $F ::= p(\theta_1, \dots, \theta_n) \mid \neg F_1 \mid F_1 \wedge F_2 \mid \forall x \bullet F_1 \mid F_1 ; F_2$
- (v) **Abbreviations:**
 $\sqcap, \quad [P]_i, \quad [P]^t, \quad [P]^{\leq t}, \quad \diamond F, \quad \square F$

A **careless proof is a bad design**

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Substitution Lemma

Lemma 2.11. [Substitution]
Consider a formula F , a global variable x , and a term θ such that F is **drop-free** or θ is **rigid**.

Then for all interpretations \mathcal{I} , valuations V , and intervals $[b, e]$:

- If F is **rigid**, then $\mathcal{I}[F][V, [b, e]] = \mathcal{I}[F][V, [b', e']]$.
- If F is **drop-free** or θ is **rigid**, then in the calculation of the semantics of F , every occurrence of ℓ denotes the same value.

3.9

Validity, Satisfiability, Realisability

Let \mathcal{I}, \mathcal{V} be an interpretation, \mathcal{V} a valuation, $[b, c]$ an interval and F a DC formula.

- $\mathcal{I}, \mathcal{V}, [b, c] \models F$ (" F holds in $\mathcal{I}, \mathcal{V}, [b, c]$ ") iff $\mathcal{I}[\mathcal{T}]^{\mathcal{V}}(\mathcal{V}, [b, c]) = \text{tt}$.
- F is called **satisfiable** iff it holds in some $\mathcal{I}, \mathcal{V}, [b, c]$.
- F is called **realisable** iff it holds in some $\mathcal{I}, \mathcal{V}, [b, c]$.
- $\mathcal{I}, \mathcal{V} \models F$ (" F and \mathcal{V} realise F ") iff $\forall [b, c] \in \text{Inv} : \mathcal{I}, \mathcal{V}, [b, c] \models F$.
- F is called **realisable** iff some \mathcal{I} and \mathcal{V} realise F .
- If F is realisable then F is satisfiable, but not vice versa.
- If F is satisfiable then F is realisable, but not vice versa.
- If F is valid then F is realisable, but not vice versa.
- If F is realisable then F is valid, but not vice versa.
- $\mathcal{I} \models F$ (" F realises F ") iff $\forall \mathcal{V} \in \text{Val} : \mathcal{I}, \mathcal{V} \models F$.
- $\models F$ (" F is valid") iff $\forall \mathcal{V} \in \text{Val} : \mathcal{I}, \mathcal{V} \models F$.

13.s

Validity vs. Satisfiability vs. Realisability

Examples: Valid? Realisable? Satisfiable?

$\mathcal{I}, \mathcal{V}, [b, c] \models F$ (" F holds in $\mathcal{I}, \mathcal{V}, [b, c]$ ") iff	$\mathcal{I}[F]_{\mathcal{V}}([b, c]) = \text{tt}$
F is called satisfiable iff it holds in some $\mathcal{I}, \mathcal{V}, [b, c]$	$\mathcal{I}, \mathcal{V} \models F$ (" F and \mathcal{V} realise F ") iff $\forall [b, c] \in \text{Inv} : \mathcal{I}, \mathcal{V}, [b, c] \models F$
F is called realisable iff some \mathcal{I} and \mathcal{V} realise F	$\mathcal{I} \models F$ (" F realises F ") iff $\forall \mathcal{V} \in \text{Val} : \mathcal{I}, \mathcal{V} \models F$
$\models F$ (" F is valid") iff	$\forall \mathcal{V} \in \text{Val} : \mathcal{I}, \mathcal{V} \models F$

13.s

Initial Values

Initial Values

- $\mathcal{I}, \mathcal{V} \models_0 F$ (" \mathcal{I} and \mathcal{V} realise F from 0") iff

$\forall t \in \text{Time} : \mathcal{I}, \mathcal{V}, [0, t] \models F$.

- F is called **realisable from 0** iff some \mathcal{I} and \mathcal{V} realise F from 0.

- Intervals of the form $[0, t]$ are called **initial intervals**.

- $\mathcal{I} \models_0 F$ (" \mathcal{I} realises F from 0") iff $\forall \mathcal{V} \in \text{Val} : \mathcal{I}, \mathcal{V} \models_0 F$.

- $\models_0 F$ (" F is valid from 0") iff \forall interpretation $\mathcal{I} : \mathcal{I} \models_0 F$.

15.s

Initial or not Initial...

Initial or not Initial...

For all interpretations \mathcal{I} , valuations \mathcal{V} and DC formulae F ,

- (i) $\mathcal{I}, \mathcal{V} \models F$ implies $\mathcal{I}, \mathcal{V} \models_0 F$, but not vice versa.
- (ii) if F is realisable then F is realisable from 0, but not vice versa.
- (iii) F is valid iff F is valid from 0.

Specification and Semantics-based Correctness Proofs of
Real-Time Systems with DC

13.s

14.s

Satisfiable? Realisable? Valid?

$\ell \geq 0$	$\ell = 1$	$\ell = 2$	$\ell < 0$

15.s

Methodology: Ideal World...

- (i) Choose a collection of **observables** 'Obs'.
- (ii) Provide the **requirement/specification** 'Spec' as a conjunction of DC formulae (over 'Obs').
- (iii) Provide a description 'Ctrl' of the **controller** in form of a DC formula (over 'Obs').
- (iv) We say 'Ctrl' is **correct** (wrt. 'Spec') iff $\models_0 \text{Ctrl} \implies \text{Spec}$.

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- (iii) Provide a description 'Ctrl' of the **controller** in form of a DC formula (over 'Obs').
- Here, firstly consider a **design**:

- Des-1 : $\iff \square([L] \implies \ell \leq 1)$

- Des-2 : $\iff \square([L]; [\neg L]; [L] \implies \ell > 30)$
- Prove **correctness**:

- We want (or do we want) $\models_0 \dots \eta$;

$$\models (\text{Des-1} \wedge \text{Des-2} \implies \text{Req}) \quad (\text{Thm. 2.16})$$

- We do show

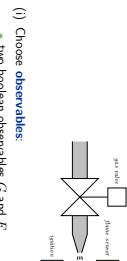
$$\models \text{Req-1} \implies \text{Req} \quad (\text{Thm. 2.17})$$

- and we show

$$\models (\text{Des-1} \wedge \text{Des-2}) \implies \text{Req-1}. \quad (\text{Thm. 2.19})$$

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- (ii) Provide the **requirement**:

$$\text{Req} : \iff \square(\ell \geq 60 \implies \text{flow}; L \leq \frac{\ell}{20})$$

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- (iii) Provide a description 'Ctrl' of the **controller** in form of a DC formula (over 'Obs'). Here, firstly consider a **design**:

- Des-1 : $\iff \square([L] \implies \ell \leq 1)$
- Des-2 : $\iff \square([L]; [\neg L]; [L] \implies \ell > 30)$
- Prove **correctness**:

- We want (or do we want) $\models_0 \dots \eta$;

$$\models (\text{Des-1} \wedge \text{Des-2} \implies \text{Req}) \quad (\text{Thm. 2.16})$$

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References

- [Oldeog and Dierks, 2008] Oldeog, E.-R. and Dierks, H. (2008): *Real-Time Systems – Formal Specification and Automatic Verification*, Cambridge University Press.

References

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