

Real-Time Systems

Lecture 07: DC Implementables

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Contents & Goals

Last Lectures:

- Semantical Correctness Proof

This Lecture:

- **Educational Objectives:** Capabilities for following tasks/questions.
 - What does this standard forms mean? Give a satisfying interpretation.
 - What are implementables? What is a control automaton?
 - Please specify (and prove correct) a controller which satisfies this requirement.
- **Content:**
 - DC Standard Forms
 - Control Automata
 - DC Implementables
 - Example

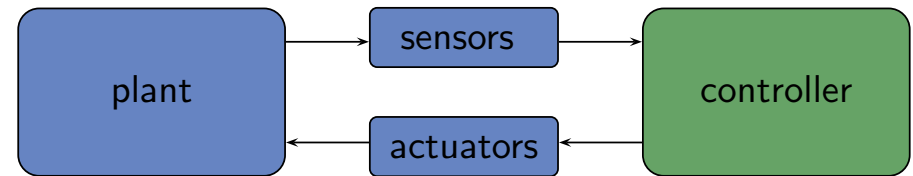
DC Implementables

Requirements vs. Implementations

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 - wait,
 - set outputs now.



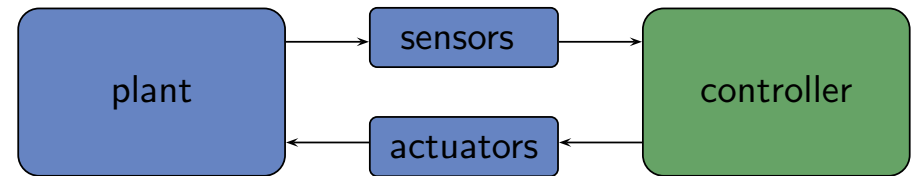
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 - a description '**Impl**' in DC, which "uses" **just these** operations,

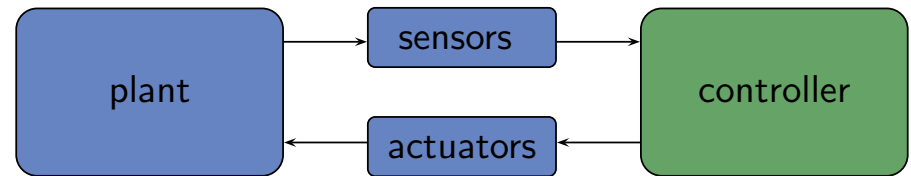
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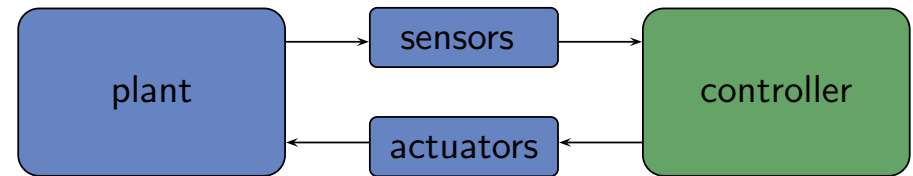
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- So, if we have

- a DC requirement '**Req**',
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then

- proving correctness amounts to proving $\models_0 \text{Impl} \implies \text{Req}$ (**in DC**)
- and we (more or less) know how to program (the correct) '**Impl**' in a PLC language, or in C on a real-time OS, or or or...

Approach: Control Automata and DC Impl'bles

Plan:

- Introduce **DC Standard Forms**
- Introduce **Control Automata**
- Introduce **DC Implementables** as subset of **DC Standard Forms**
- Example: a correct controller design for the notorious Gas Burner

DC Standard Forms: Followed-by

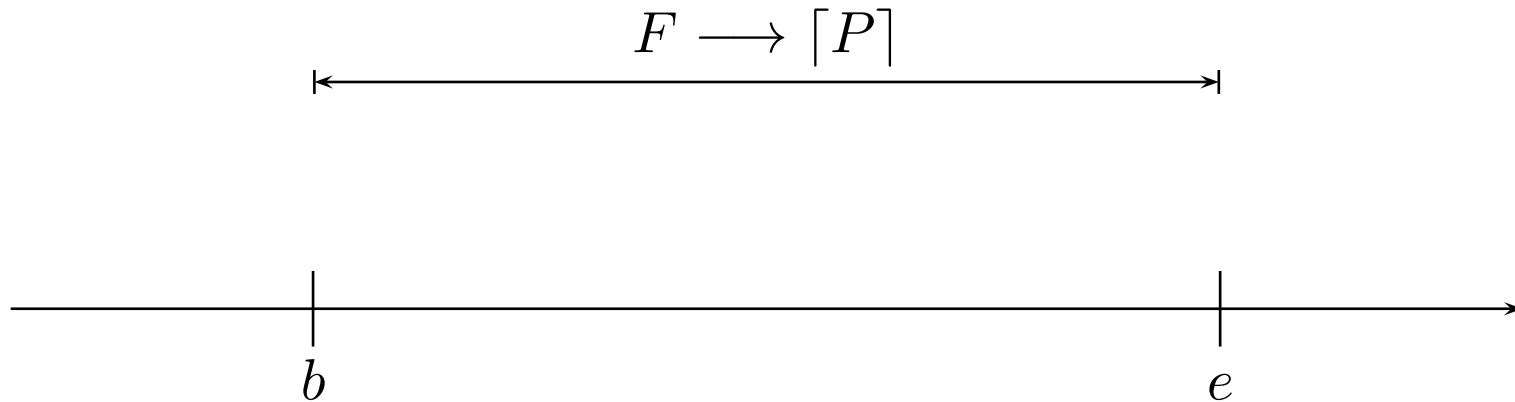
In the following: F is a DC **formula**, P a **state assertion**, θ a **rigid term**.

- **Followed-by:**

$$F \longrightarrow [P] :\iff \neg\Diamond(F ; [\neg P]) \iff \Box\neg(F ; [\neg P])$$

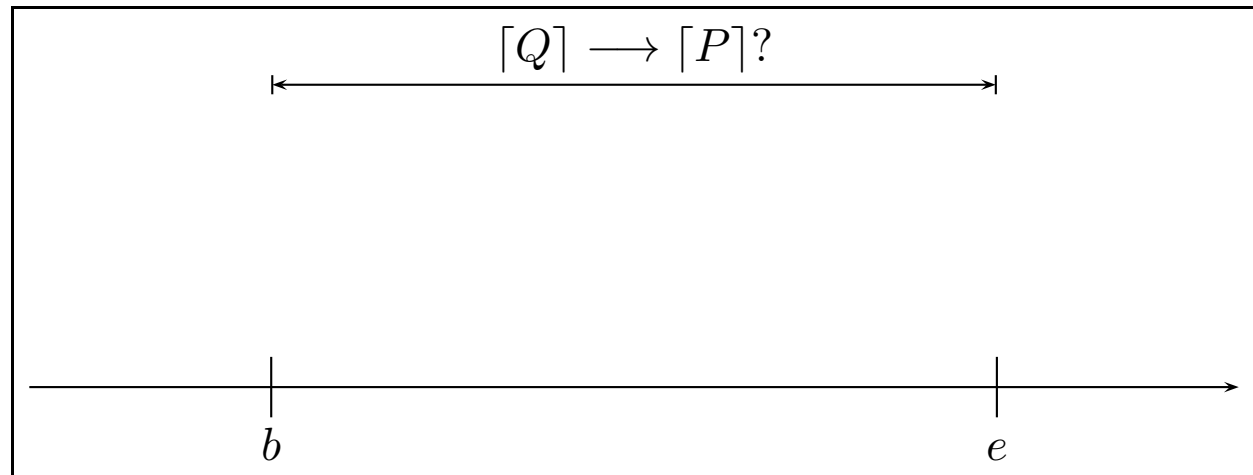
in other symbols

$$\forall x \bullet \Box((F \wedge \ell = x) ; \ell > 0 \implies (F \wedge \ell = x) ; [P] ; true)$$



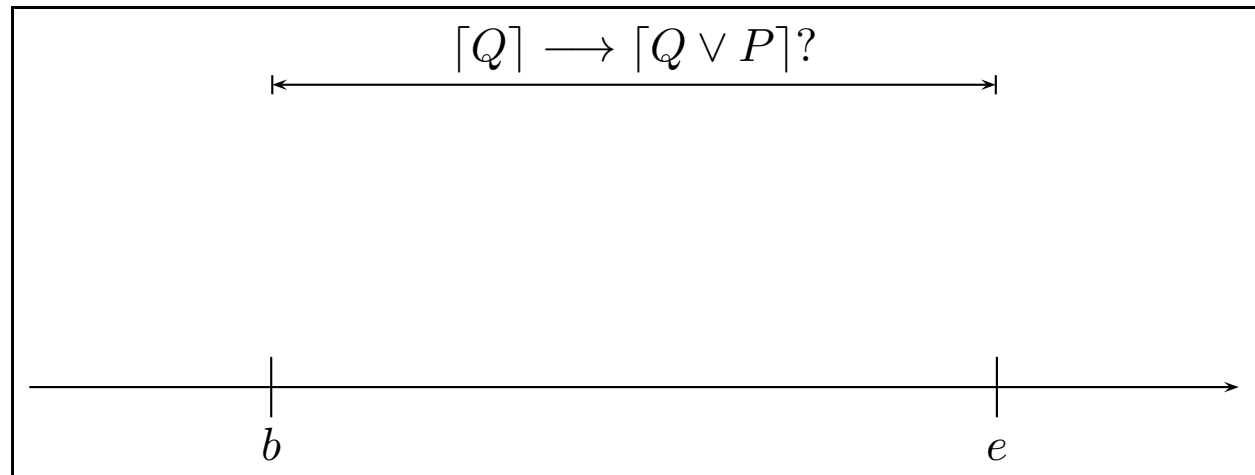
DC Standard Forms: Followed-by Examples

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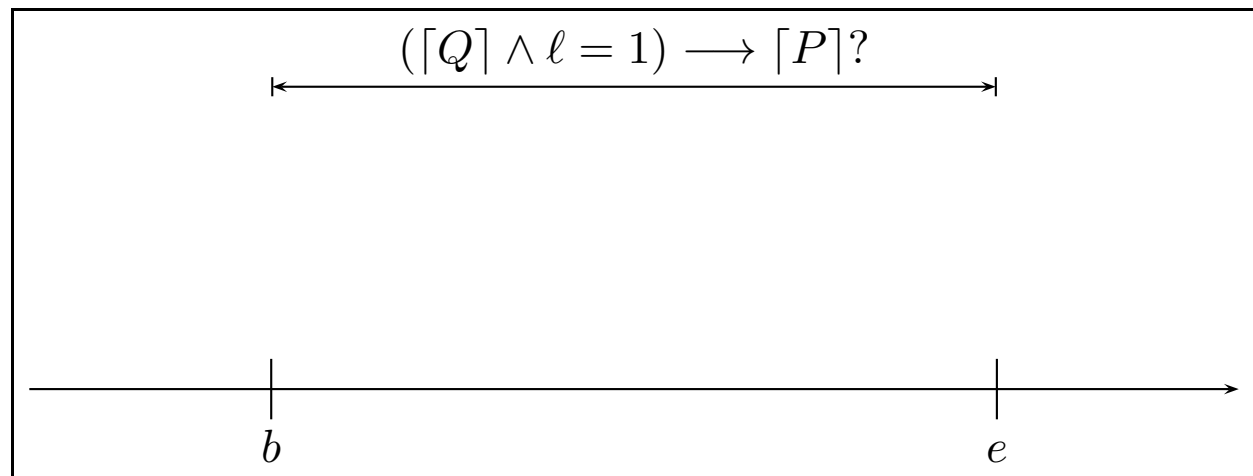
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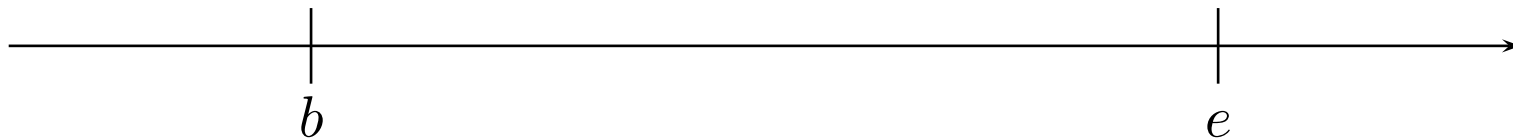


DC Standard Forms: (Timed) leads-to

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$$F \xrightarrow{\theta} [P] :\iff (F \wedge \ell = \theta) \longrightarrow [P]$$

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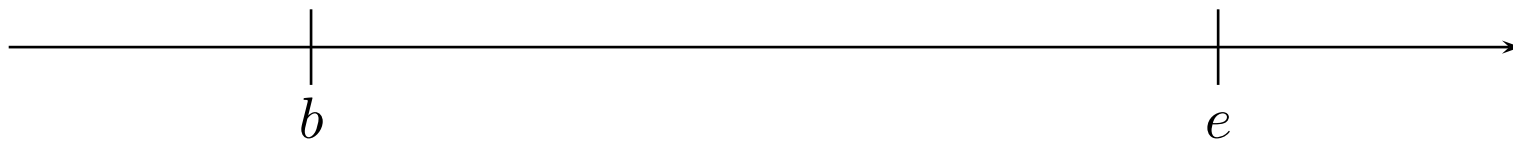


DC Standard Forms: (Timed) up-to

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$$F \xrightarrow{\leq \theta} [P] :\iff (F \wedge \ell \leq \theta) \longrightarrow [P]$$

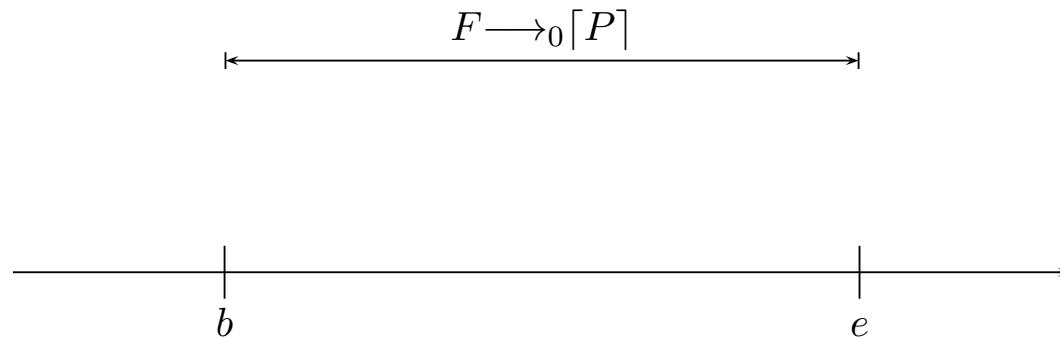
$$\xleftarrow{F \xrightarrow{\theta} [P]} \xrightarrow{\hspace{10em}}$$



DC Standard Forms: Initialisation

- **Followed-by-initially:**

$$F \longrightarrow_0 [P] :\iff \neg(F ; [\neg P])$$



- **(Timed) up-to-initially:**

$$F \xrightarrow{\leq \theta}_0 [P] :\iff (F \wedge \ell \leq \theta) \longrightarrow_0 [P]$$

- **Initialisation:**

$$[] \vee [P] ; true$$

Control Automata

- Let X_1, \dots, X_k be k state variables ranging over **finite** domains $\mathcal{D}(X_1), \dots, \mathcal{D}(X_k)$.
- With a DC formula 'Impl' ranging over X_1, \dots, X_k we have a **system of k control automata**.
- 'Impl' is typically a conjunction of **DC implementables**.

Control Automata

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- With a DC formula ‘Impl’ ranging over X_1, \dots, X_k we have a **system of k control automata**.
- ‘Impl’ is typically a conjunction of **DC implementables**.
- A state assertion of the form

$$X_i = d_i, \quad d_i \in \mathcal{D}(X_i),$$

which constrains the values of X_i , is called **basic phase** of X_i .

- A **phase** of X_i is a Boolean combination of basic phases of X_i .
- **Abbreviations:**
 - Write X_i instead of $X_i = 1$, if X_i is Boolean.
 - Write d_i instead of $X_i = d_i$, if $\mathcal{D}(X_i)$ is disjoint from $\mathcal{D}(X_j)$, $i \neq j$.

Control Automata: Example

Model of Gas Burner controller as a system of four control automata:

- H Boolean,
representing **heat request**, (input)
- F Boolean,
representing **flame**, (input)
- C with $\mathcal{D}(C) = \{\text{idle, purge, ignite, burn}\}$,
representing the (status of the) **controller**, (local)
- G Boolean,
representing **gas valve**. (output)

- **Basic phase** of C :

$C = \text{purge}$ (or only: purge)

- **Phase** of C :

$\text{purge} \vee \text{idle}$

DC Implementables

- DC Implementables are special patterns of DC Standard Forms (due to A.P. Ravn).
- Within one pattern,
 - $\pi, \pi_1, \dots, \pi_n, n \geq 0$, denote **phases** of **the same** state variable X_i ,
 - φ denotes a state assertion not depending on X_i .
- θ denotes a **rigid** term.

- **Initialisation:**

$$[\] \vee [\pi] ; true$$

- **Sequencing:**

$$[\pi] \longrightarrow [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$

- **Progress:**

$$[\pi] \xrightarrow{\theta} [\neg\pi]$$

DC Implementables Cont'd

- **Bounded Stability:**

$$[\neg\pi] ; [\pi \wedge \varphi] \xrightarrow{\leq\theta} [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$

- **Unbounded Stability:**

$$[\neg\pi] ; [\pi \wedge \varphi] \longrightarrow [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$

- **Bounded initial stability:**

$$[\pi \wedge \varphi] \xrightarrow{\leq\theta}_0 [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$

- **Unbounded initial stability:**

$$[\pi \wedge \varphi] \longrightarrow_0 [\pi \vee \pi_1 \vee \dots \vee \pi_n]$$

Specification by DC Implementables

- Let X_1, \dots, X_k be a system of k control automata.
- Let 'Impl' be a conjunction of **DC implementables**.
- Then 'Impl' **specifies** all interpretations \mathcal{I} of X_1, \dots, X_k and all valuations \mathcal{V} such that

$$\mathcal{I}, \mathcal{V} \models_0 \text{Impl}$$

- Hmm: And what does this have to do with controllers...?

Example: Gas Burner

Recall: Control Automata

Model of Gas Burner controller as a system of four control automata:

- H : Boolean,
representing **heat request**, (input)
- F : Boolean,
representing **flame**, (input)
- C with $\mathcal{D}(C) = \{\text{idle, purge, ignite, burn}\}$,
representing the **controller**, (local)
- G : Boolean,
representing **gas valve**. (output)

Gas Burner Controller Specification

$\lceil \rceil \vee \lceil \text{idle} \rceil ; \text{true},$	$\lceil \rceil \vee \lceil \neg H \rceil ; \text{true},$	$\lceil \rceil \vee \lceil \neg F \rceil ; \text{true},$	$\lceil \rceil \vee \lceil \neg G \rceil ; \text{true}$	(Init-1 - 4)
	$\lceil \text{idle} \rceil \longrightarrow \lceil \text{idle} \vee \text{purge} \rceil$			(Seq-1)
	$\lceil \text{purge} \rceil \longrightarrow \lceil \text{purge} \vee \text{ignite} \rceil$			(Seq-2)
	$\lceil \text{ignite} \rceil \longrightarrow \lceil \text{ignite} \vee \text{burn} \rceil$			(Seq-3)
	$\lceil \text{burn} \rceil \longrightarrow \lceil \text{burn} \vee \text{idle} \rceil$			(Seq-4)
	$\lceil \text{purge} \rceil \xrightarrow{30+\varepsilon} \lceil \neg \text{purge} \rceil$			(Prog-1)
	$\lceil \text{ignite} \rceil \xrightarrow{0.5+\varepsilon} \lceil \neg \text{ignite} \rceil$			(Prog-2)
	$\lceil \text{idle} \wedge H \rceil \xrightarrow{\varepsilon} \lceil \neg \text{idle} \rceil$			(Syn-1)
	$\lceil \text{burn} \wedge (\neg H \vee \neg F) \rceil \xrightarrow{\varepsilon} \lceil \neg \text{burn} \rceil$			(Syn-2)
	$\lceil G \wedge (\text{idle} \vee \text{purge}) \rceil \xrightarrow{\varepsilon} \lceil \neg G \rceil$			(Syn-3)
	$\lceil \neg G \wedge (\text{ignite} \vee \text{burn}) \rceil \xrightarrow{\varepsilon} \lceil G \rceil$			(Syn-4)
	$\lceil \neg \text{idle} \rceil ; \lceil \text{idle} \wedge \neg H \rceil \longrightarrow \lceil \text{idle} \rceil$			(Stab-1)
	$\lceil \text{idle} \wedge \neg H \rceil \longrightarrow_0 \lceil \text{idle} \rceil$			(Stab-1-init)
	$\lceil \neg \text{purge} \rceil ; \lceil \text{purge} \rceil \xrightarrow{\leq 30} \lceil \text{purge} \rceil$			(Stab-2)
	$\lceil \neg \text{ignite} \rceil ; \lceil \text{ignite} \rceil \xrightarrow{\leq 0.5} \lceil \text{ignite} \rceil$			(Stab-3)
	$\lceil \neg \text{burn} \rceil ; \lceil \text{burn} \wedge H \wedge F \rceil \longrightarrow \lceil \text{burn} \rceil$			(Stab-4)
	$\lceil F \rceil ; \lceil \neg F \wedge \neg \text{ignite} \rceil \longrightarrow \lceil \neg F \rceil$			(Stab-5)
	$\lceil \neg F \wedge \neg \text{ignite} \rceil \longrightarrow_0 \lceil \neg F \rceil$			(Stab-5-init)
	$\lceil G \rceil ; \lceil \neg G \wedge (\text{idle} \vee \text{purge}) \rceil \longrightarrow \lceil \neg G \rceil$			(Stab-6)
	$\lceil \neg G \wedge (\text{idle} \vee \text{purge}) \rceil \longrightarrow_0 \lceil \neg G \rceil$			(Stab-6-init)
	$\lceil \neg G \rceil ; \lceil G \wedge (\text{ignite} \vee \text{burn}) \rceil \longrightarrow \lceil G \rceil$			(Stab-7)

Gas Burner Controller Specification: Untimed

$\lceil \rceil \vee \lceil \text{idle} \rceil ; \text{true}$	(Init-1)
$\lceil \text{idle} \rceil \longrightarrow \lceil \text{idle} \vee \text{purge} \rceil$	(Seq-1)
$\lceil \text{purge} \rceil \longrightarrow \lceil \text{purge} \vee \text{ignite} \rceil$	(Seq-2)
$\lceil \text{ignite} \rceil \longrightarrow \lceil \text{ignite} \vee \text{burn} \rceil$	(Seq-3)
$\lceil \text{burn} \rceil \longrightarrow \lceil \text{burn} \vee \text{idle} \rceil$	(Seq-4)

Gas Burner Controller Specification: Timing

$$[\text{purge}] \xrightarrow{30+\varepsilon} [\neg\text{purge}] \quad (\text{Prog-1})$$

$$[\text{ignite}] \xrightarrow{0.5+\varepsilon} [\neg\text{ignite}] \quad (\text{Prog-2})$$

$$[\neg\text{purge}] ; [\text{purge}] \xrightarrow{\leq 30} [\text{purge}] \quad (\text{Stab-2})$$

$$[\neg\text{ignite}] ; [\text{ignite}] \xrightarrow{\leq 0.5} [\text{ignite}] \quad (\text{Stab-3})$$

Gas Burner Controller Specification: Outputs

$$\lceil G \wedge (\text{idle} \vee \text{purge}) \rceil \xrightarrow{\varepsilon} \lceil \neg G \rceil \quad (\text{Syn-3})$$

$$\lceil \neg G \wedge (\text{ignite} \vee \text{burn}) \rceil \xrightarrow{\varepsilon} \lceil G \rceil \quad (\text{Syn-4})$$

$$\lceil G \rceil ; \lceil \neg G \wedge (\text{idle} \vee \text{purge}) \rceil \longrightarrow \lceil \neg G \rceil \quad (\text{Stab-6})$$

$$\lceil \neg G \wedge (\text{idle} \vee \text{purge}) \rceil \longrightarrow_0 \lceil \neg G \rceil \quad (\text{Stab-6-init})$$

$$\lceil \neg G \rceil ; \lceil G \wedge (\text{ignite} \vee \text{burn}) \rceil \longrightarrow \lceil G \rceil \quad (\text{Stab-7})$$

Gas Burner Controller Specification: Inputs

$$[\text{idle} \wedge H] \xrightarrow{\varepsilon} [\neg \text{idle}] \quad (\text{Syn-1})$$

$$[\text{burn} \wedge (\neg H \vee \neg F)] \xrightarrow{\varepsilon} [\neg \text{burn}] \quad (\text{Syn-2})$$

$$[\neg \text{idle}] ; [\text{idle} \wedge \neg H] \longrightarrow [\text{idle}] \quad (\text{Stab-1})$$

$$[\text{idle} \wedge \neg H] \longrightarrow_0 [\text{idle}] \quad (\text{Stab-1-init})$$

$$[\neg \text{burn}] ; [\text{burn} \wedge H \wedge F] \longrightarrow [\text{burn}] \quad (\text{Stab-4})$$

Gas Burner Controller Specification: Assumptions

$\Box \vee [\neg H] ; true$ (Init-2)

$\Box \vee [\neg F] ; true$ (Init-3)

$\Box \vee [\neg G] ; true$ (Init-4)

$[F] ; [\neg F \wedge \neg \text{ignite}] \longrightarrow [\neg F]$ (Stab-5)

$[\neg F \wedge \neg \text{ignite}] \longrightarrow_0 [\neg F]$ (Stab-5-init)

Gas Burner Controller Correctness Proof

$$\text{GB-Ctrl} := \text{Init-1} \wedge \dots \wedge \text{Stab-7} \wedge \varepsilon > 0$$

Recall:

$$\text{Req} := \iff \square(\ell \geq 60 \implies 20 \cdot f L \leq \ell)$$

and (cf. [Olderog and Dierks, 2008])

$$\models \text{Req-1} \implies \text{Req}$$

for the **simplified**

$$\text{Req-1} := \square(\ell \leq 30 \implies f L \leq 1).$$

Here we show

$$\models \text{GB-Ctrl} \wedge A(\varepsilon) \implies \text{Req-1}.$$

Lemma 3.15

$$\models \text{GB-Ctrl} \implies \square \left(\begin{array}{l} ([\text{idle}] \implies \int G \leq \varepsilon) \\ \wedge ([\text{purge}] \implies \int G \leq \varepsilon) \\ \wedge ([\text{ignite}] \implies \ell \leq 0.5 + \varepsilon) \\ \wedge ([\text{burn}] \implies \int \neg F \leq 2\varepsilon) \end{array} \right)$$

Proof: Let \mathcal{I} be an interpretation, \mathcal{V} a valuation, and $[c, d]$ an interval with $\mathcal{I}, \mathcal{V}, [c, d] \models \text{GB-Ctrl}$. Let $[b, e] \subseteq [c, d]$.

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- Case 1: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{idle}]$

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$$[G \wedge (\text{idle} \vee \text{purge})] \xrightarrow{\varepsilon} [\neg G] \quad (\text{Syn-3})$$

$$[G] ; [\neg G \wedge (\text{idle} \vee \text{purge})] \longrightarrow [\neg G] \quad (\text{Stab-6})$$

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$$[G] ; [\neg G \wedge (\text{idle} \vee \text{purge})] \longrightarrow [\neg G] \quad (\text{Stab-6})$$

$$\mathcal{I}, \mathcal{V}, [b, e] \models \Box([\neg G] \implies \ell \leq \varepsilon) \wedge \neg \Diamond([G] ; [\neg G] ; [G])$$

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$$[G] ; [\neg G \wedge (\text{idle} \vee \text{purge})] \longrightarrow [\neg G] \quad (\text{Stab-6})$$

$$\mathcal{I}, \mathcal{V}, [b, e] \models \Box([G] \implies \ell \leq \varepsilon) \wedge \neg \Diamond([G] ; [\neg G] ; [G])$$

- Case 2: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{purge}]$ Analogously to case 1.

Lemma 3.15 Cont'd

$$(\lceil \text{idle} \rceil \implies \int G \leq \varepsilon)$$

$$(\lceil \text{purge} \rceil \implies \int G \leq \varepsilon)$$

$$(\lceil \text{ignite} \rceil \implies \ell \leq 0.5 + \varepsilon)$$

$$(\lceil \text{burn} \rceil \implies \int \neg F \leq 2\varepsilon)$$

Lemma 3.15 Cont'd

- Case 3: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{ignite}]$

$$\begin{aligned}([\text{idle}] &\implies f G \leq \varepsilon) \\([\text{purge}] &\implies f G \leq \varepsilon) \\([\text{ignite}] &\implies \ell \leq 0.5 + \varepsilon) \\([\text{burn}] &\implies f \neg F \leq 2\varepsilon)\end{aligned}$$

Lemma 3.15 Cont'd

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$$[\text{ignite}] \xrightarrow{0.5+\varepsilon} [\neg\text{ignite}] \quad (\text{Prog-2})$$

Lemma 3.15 Cont'd

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$$[\text{ignite}] \xrightarrow{0.5+\varepsilon} [\neg\text{ignite}] \quad (\text{Prog-2})$$

$$\mathcal{I}, \mathcal{V}, [b, e] \models \ell \leq 0.5 + \varepsilon$$

- Case 4: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{burn}]$

Lemma 3.15 Cont'd

$$\begin{aligned} ([\text{idle}] &\implies f G \leq \varepsilon) \\ ([\text{purge}] &\implies f G \leq \varepsilon) \\ ([\text{ignite}] &\implies \ell \leq 0.5 + \varepsilon) \\ ([\text{burn}] &\implies f \neg F \leq 2\varepsilon) \end{aligned}$$

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$$[\text{ignite}] \xrightarrow{0.5+\varepsilon} [\neg \text{ignite}] \quad (\text{Prog-2})$$

$$\mathcal{I}, \mathcal{V}, [b, e] \models \ell \leq 0.5 + \varepsilon$$

- Case 4: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{burn}]$

$$[\text{burn} \wedge (\neg H \vee \neg F)] \xrightarrow{\varepsilon} [\neg \text{burn}] \quad (\text{Syn-2})$$

$$[F] ; [\neg F \wedge \neg \text{ignite}] \longrightarrow [\neg F] \quad (\text{Stab-5})$$

Lemma 3.15 Cont'd

$$\begin{aligned}
 ([\text{idle}] &\Longrightarrow f G \leq \varepsilon) \\
 ([\text{purge}] &\Longrightarrow f G \leq \varepsilon) \\
 ([\text{ignite}] &\Longrightarrow \ell \leq 0.5 + \varepsilon) \\
 ([\text{burn}] &\Longrightarrow f \neg F \leq 2\varepsilon)
 \end{aligned}$$

- Case 3: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{ignite}]$

$$[\text{ignite}] \xrightarrow{0.5+\varepsilon} [\neg \text{ignite}] \quad (\text{Prog-2})$$

$$\mathcal{I}, \mathcal{V}, [b, e] \models \ell \leq 0.5 + \varepsilon$$

- Case 4: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{burn}]$

$$[\text{burn} \wedge (\neg H \vee \neg F)] \xrightarrow{\varepsilon} [\neg \text{burn}] \quad (\text{Syn-2})$$

$$[F] ; [\neg F \wedge \neg \text{ignite}] \longrightarrow [\neg F] \quad (\text{Stab-5})$$

$$\mathcal{I}, \mathcal{V}, [b, e] \models \square([\neg F] \Longrightarrow \ell \leq \varepsilon) \wedge \neg \diamond([F] ; [\neg F] ; [F])$$

Lemma 3.16

$$\models \exists \varepsilon \bullet \text{GB-Ctrl} \implies \underbrace{\square(\ell \leq 30 \implies f L \leq 1)}_{\text{Req-1}}$$

Lemma 3.16 Cont'd

- Case 0: $\mathcal{I}, \mathcal{V}, [b, e] \models \square$
- Case 1: $\mathcal{I}, \mathcal{V}, [b, e] \models \text{[idle]} ; \text{true} \wedge \ell \leq 30$

$$\text{[idle]} \longrightarrow \text{[idle} \vee \text{purge]} \quad (\text{Seq-1})$$

$$\text{[}\neg\text{purge]} ; \text{[purge]} \xrightarrow{\leq 30} \text{[purge]} \quad (\text{Stab-2})$$

Lemma 3.16 Cont'd

- Case 2: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{burn}] ; \text{true} \wedge \ell \leq 30$

$$[\text{burn}] \longrightarrow [\text{burn} \vee \text{idle}] \quad (\text{Seq-4})$$

Lemma 3.16 Cont'd

- Case 3: $\mathcal{I}, \mathcal{V}, [b, e] \models \lceil \text{ignite} \rceil ; \text{true} \wedge \ell \leq 30$

$$\lceil \text{ignite} \rceil \longrightarrow \lceil \text{ignite} \vee \text{burn} \rceil \quad (\text{Seq-3})$$

Lemma 3.16 Cont'd

- Case 4: $\mathcal{I}, \mathcal{V}, [b, e] \models [\text{purge}] ; \text{true} \wedge \ell \leq 30$

$$[\text{purge}] \longrightarrow [\text{purge} \vee \text{ignite}] \quad (\text{Seq-2})$$

Correctness Result

Theorem 3.17.

$$\models \left(\text{GB-Ctrl} \wedge \varepsilon \leq \frac{1}{12} \right) \implies \text{Req}$$

Discussion

- We used only

'Seq-1', 'Seq-2', 'Seq-3', 'Seq-4',
'Prog-2', 'Syn-2', 'Syn-3',
'Stab-2', 'Stab-5', 'Stab-6'.

What about

$$\text{Prog-1} = [\text{purge}] \xrightarrow{30+\varepsilon} [\neg\text{purge}]$$

for instance?

References

[Olderog and Dierks, 2008] Olderog, E.-R. and Dierks, H. (2008). *Real-Time Systems - Formal Specification and Automatic Verification*. Cambridge University Press.