

Softwaretechnik / Software-Engineering

Lecture 17: Software Engineering Research

2015-07-16

Prof. Dr. Andreas Podelski, **Dr. Bernd Westphal**

Albert-Ludwigs-Universität Freiburg, Germany

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Schedule of the Block “Invited Talks”

- **12:15 - 12:17:39** — Introduction
- **12:17:53 - 12:55**
 - “The Wireless Fire Alarm System: Ensuring Conformance to Industrial Standards through Formal Verification”
Sergio Feo Arenis
- **12:55 - 13:05** — Break
- **13:05 - 13:30**
 - “Towards Successful Subcontracting for Software in Small to Medium-Sized Enterprises”
Daniel Dietsch
- **13:30 - 13:55**
 - “Traces, Interpolants, and Automata: a New Approach to Automatic Software Verification.”
Dr. Jochen Hoenicke

Introduction	L 1:	20.4., Mo
	T 1:	23.4., Do
Development Process, Metrics	L 2:	27.4., Mo
	L 3:	30.4., Do
	L 4:	4.5., Mo
	T 2:	7.5., Do
Requirements Engineering	L 5:	11.5., Mo
	-	14.5., Do
	L 6:	18.5., Mo
	L 7:	21.5., Do
	-	25.5., Mo
	-	28.5., Do
	T 3:	1.6., Mo
	-	4.6., Do
	L 8:	8.6., Mo
	L 9:	11.6., Do
Architecture & Design, Software Modelling	L 10:	15.6., Mo
	T 4:	18.6., Do
	L 11:	22.6., Mo
	L 12:	25.6., Do
Quality Assurance	L 13:	29.6., Mo
	L 14:	2.7., Do
Invited Talks	T 5:	6.7., Mo
	L 15:	9.7., Do
Wrap-Up	L 16:	13.7., Mo
	L 17:	16.7., Do
	T 6:	20.7., Mo
	L 18:	23.7., Do

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The Wireless Fire Alarm System: Ensuring Conformance to Industrial Standards through Formal Verification

Sergio Feo-Arenís Bernd Westphal Daniel Dietsch
Marco Muñoz Syar Andisha



July 16th – 2015

Context



- Develop a wireless fire alarm system (safety critical).
- Requires certification to international standards.
- Small company with little to no experience with formal methods, but an acute need for product safety and quality.
- Project duration: ca. 2 years.

- Can formal methods handle development projects in the context of a small company (SME)? at which cost?
- How to tackle requirements from industrial standards using formal methods?
- What research ideas emerged from the project?



- Develop a Standard-compliant Fire Alarm System**
- Use a wireless protocol that supports range extenders (repeaters).
 - Maximize energy efficiency.
 - **Ensure compliance with the norm DIN EN-54 (Part 25).**



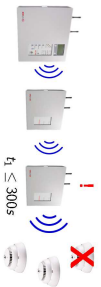
EN54 Requirements

- Detect and display communication failures in at most 300+100 seconds.
- Display alarms timely:
 - In at most 10 seconds for single alarms.
 - The first in 10 seconds and the last in 100 seconds for 10 simultaneous.
- Fulfill even when there are other users of the frequency.

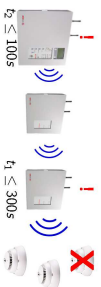


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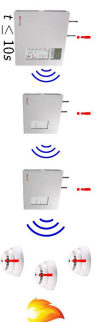
Scenario



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Challenges



Testing a design is difficult:

- There is a very large number of possible system configurations.
- Requires a prototype implementation.
- Controlling timing and radio communication environments requires costly procedures.
- The requirements assume an inherent nondeterminism.

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Thus:

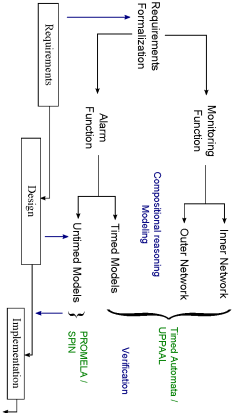
Verification could help.

General Risks

- Development in a small company:
 - Development team of 3 people: 1 computer scientist, 1 programmer, 1 electrical engineer.
- Underspecified standard requirements.
- High cost of certification.
 - A failed certification attempt threatens the very existence of the company.
- Market introduction deadlines have high priority.
- Lack of structure in the software development process.
 - Weak documentation practices.
- No familiarity with model-based development.

Overview

We accompanied the conventional development process as consultants.



What to Verify: Requirements Formalization

EN-54 provides:

- High-level real-time requirements (hard to formalize)
- **Test Procedures**

Effort required: Months. It was necessary to negotiate ambiguities with the certification authority.

What to Verify: Requirements Formalization

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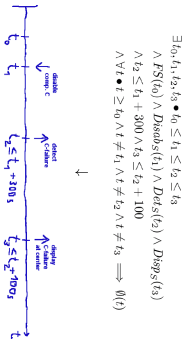
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Chose duration calculus (DC) as formalism to generalize and capture the standard requirements based on test procedures.

- The formalism was not familiar to developers or the certificate authority.
- Required developing a graphical means of communication between the stakeholders. [Visual Narratives](#)

What to Verify: Requirements Formalization



What to Verify: Requirements Formalization

Result of the DC formalization:

- Captured test procedures.
- Captured environment assumptions during tests (frequency jamming, simplifying assumptions).
- Generalized to cover all components in arbitrary system topologies.

What to Verify: Requirements Formalization

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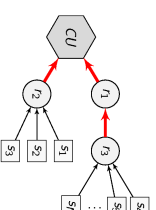
- Captured test procedures.
- Captured environment assumptions during tests (frequency jamming, simplifying assumptions)
- Generalized to cover all components in arbitrary system topologies.

In total:

- 6 (quantified) observables
- 7 (quantified) testable DC formulae

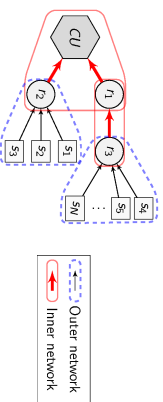
Modeling: Monitoring Function

Topologies can be decomposed:



Modeling: Monitoring Function

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We modelled each "network" separately using networks of timed automata (UPPAAL).

Modeling: Monitoring Function

Decomposition gives way to additional proof obligations:

- No interference between networks (by design).
- No collisions (TDMA). [\[Guard time analysis\]](#)
- Topology subassumption: Verifying a maximal subnetwork is enough.

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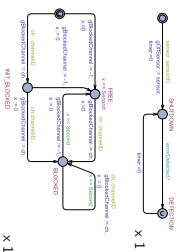
- No interference between networks (by design)
- No collisions (TDMA). [Guard time analysis]
- Topology subsumption: Verifying a maximal subnetwork is enough.

To make models tractable, we require optimization:

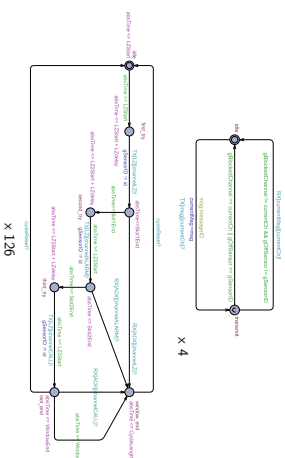
- Each component has an individual clock. [Quasi-equal clock reduction]
- Support plug-in models: Separate environment and design.

Modeling: Sensor Failures

Modeled as timed automata networks with UPPAAL:



Modeling: Sensor Failures



Verification: Monitoring Function

Other model components:

- Auxiliary automata: Master, Central clock, Monitor
- Inner network: 10 Repeaters

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Found 2 flaws:

- Timing was off by 1 tic
- Frequency / intrusion

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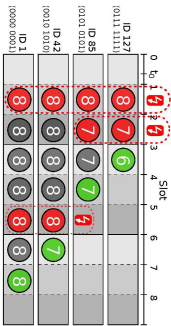
A revised design was successfully verified:

Query	Sensors as slaves			Repeaters as slaves		
	seconds	MB	States	seconds	MB	States
Detection	36,070.78	3,419.00	1,004	231.64	230.59	6M
No Spurious	97.44	44.29	0.04	3.94	10.14	0.15M
No ID Collision	13,895.17	2,543.20	604	368.59	250.91	9.9M
Detection Feasible	10,893.13	597.08	604	39.21	59.07	1.2M

Verification is scalable for real world problems (!) But additional effort is required.

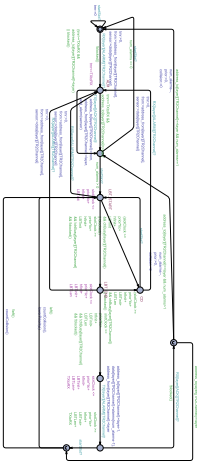
Modeling: Alarm Function

Alarms are transmitted (semi-)asynchronously using CSMA-CD / Collision resolution using tree splitting.



Each component ID induces a unique timing pattern for retrying transmissions.

Modeling: Alarm Function



Verification: Alarm Function

For single, explicit topologies: Timed automata / UPPAAL.

Full collision				
Query	MR	seq	MB	State
Observed	seq	3.9 ± 1	43.1 ± 1	385 ± 114
Timeouts	seq	4.7	6.2	110.207
TotalTime	seq	44.5 ± 11	311.4 ± 102	641x ± 159x
	opt	41.8 ± 10	305.6 ± 80	600x ± 140x

Checking one topology is feasible, but the procedure does not scale for full verification (more than 10^{20} possible topologies). [\[Parameterized Verification of Aggregation Protocols\]](#)

Models are still useful for simulation: extracted expected alarm times for different scenarios.

Verification: Alarm Function

For single, explicit topologies: Timed automata / UPPAAL.

Limited Collision				
Query	MR	seq	MB	State
Observed	seq	1.4 ± 1	38.2 ± 1	385 ± 114
Timeouts	seq	0.5	24.1	19.528
TotalTime	seq	17.3 ± 6	179.1 ± 61	419x ± 124x
	opt	17.1 ± 6	182.2 ± 64	412x ± 124x

Checking one topology is feasible, but the procedure does not scale for full verification (more than 10^{20} possible topologies). [\[Parameterized Verification of Aggregation Protocols\]](#)

Models are still useful for simulation: extracted expected alarm times for different scenarios.

Verification: Alarm Function



For increased confidence: Does the collision resolution algorithm guarantee non-starvation?

Verification: Alarm Function



For increased confidence: Does the collision resolution algorithm guarantee non-starvation? Created an untimed model in PROMELA / SPIN.

- N : number of colliding components.
- I : set of IDs that may participate in the collision.
- Check all possible N -collision scenarios: vary IDs and timing.

Verification: Alarm Function

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- Check all possible N -collision scenarios: vary IDs and timing.

Results:

- Reproduced the hidden terminal problem.
- For $N = 2$: found a problem with IDs 0 and 128
- For $N = \{3..10\}$: still not scaling to all IDs, used sampling (31744).

$ I $	N	sec.	MB	States
265	2	49	1.610	1.238.970
H	10	3.393	6.309	6.342.610
L	10	4.271	10.685	10.439.545
Rnd	10	4.465	11.534	11.268.368
average		4.138	9.394	9.765.809

Lessons Learned

Generalized test procedures are useful for verification:

- Developers are already used to producing test specifications.
- Thus: are cost-effective for increasing confidence.

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Models are useful:

- For validation.
- As documentation.
- But still not very accessible for developers.

Formal verification shows potential to relieve the effort of testing.

Conclusions

- Formal methods are able to handle typical industrial scenarios (but require expert knowledge).
- The customers are confident early in the process that certification tests will be passed.
- Implementation is easier when based on a verified design.
- Other requirements can be simply tested.
- Still expensive: Almost as expensive as the certification test itself.
- Additional value: Formal methods not only improve confidence but helps structure development processes.
- Difficult technology transfer: SMEs prefer to scale out instead of up.

Outlook



- Check whether the source code of the implementation corresponds to the design models. Interrupt based implementations are hard to verify.
- Use the models to perform model-based testing.
- Investigate reuse strategies (new features, product lines).

*Towards
Successful Subcontracting for Software
in Small to Medium-Sized Enterprises*

REL Workshop 2012-09-25

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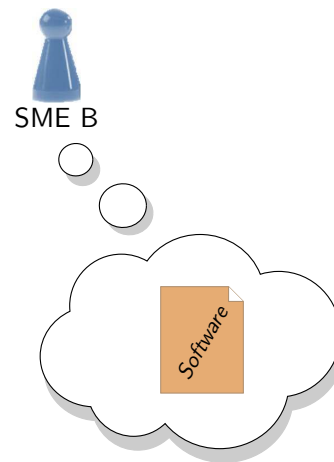
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outline

- ▶ Introduction
 - What is sub-contracting for software?
 - When is it successful?
 - Why is it often not successful?
- The Salomo Approach:
 - Overview
 - Checkable Requirements, Checking Tool
 - Regulations in the Contract
- Related Work
- Conclusion and Further Work

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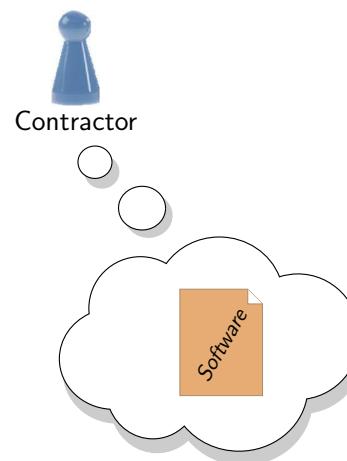
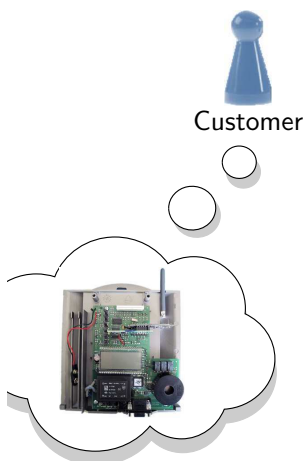
Successful Software contracting for Software in Systems



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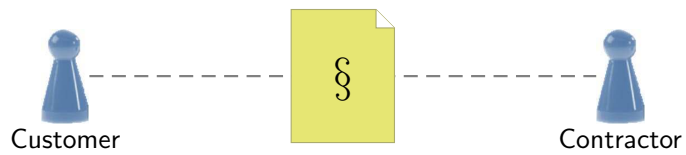
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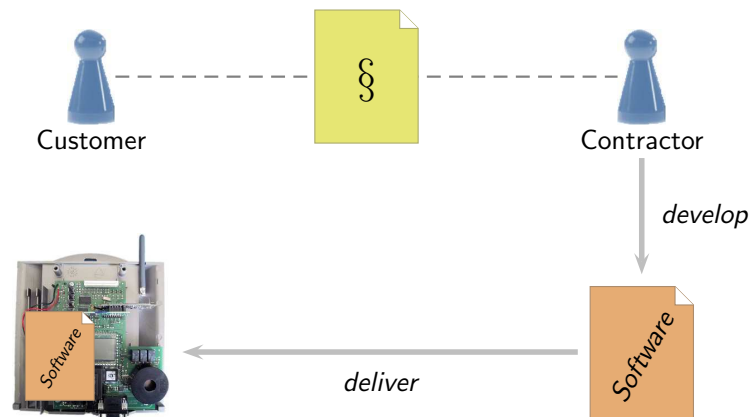
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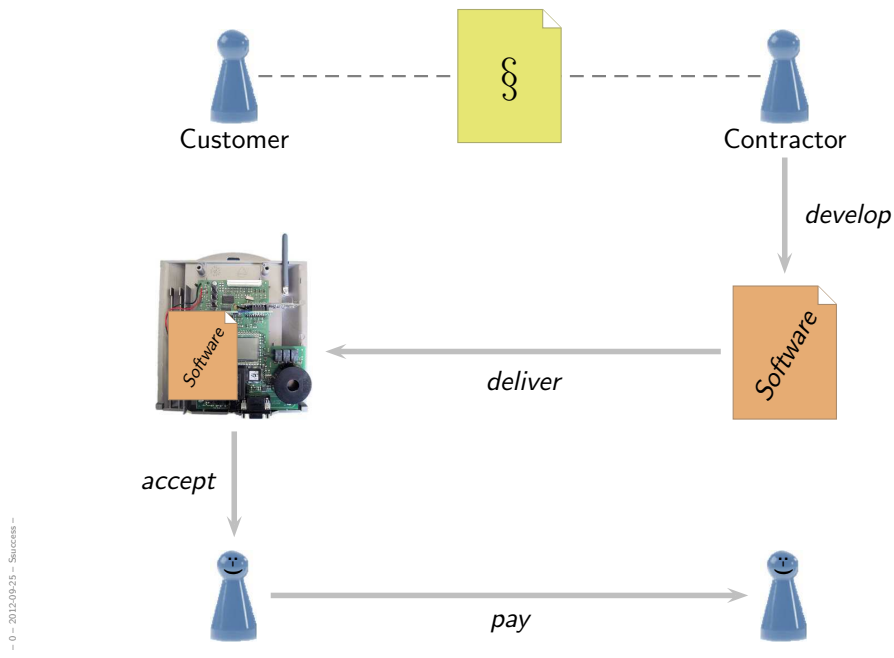
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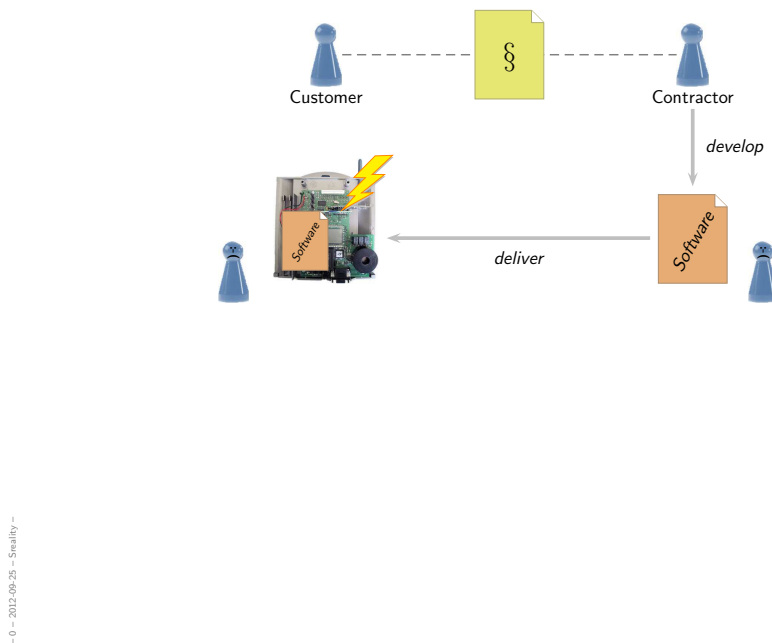


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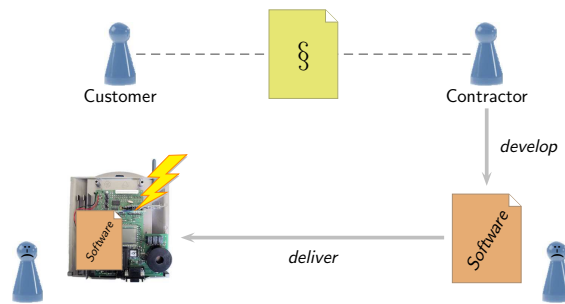


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Software contracting for Software in Systems in Reality



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- **misunderstandings** in the **requirements**,

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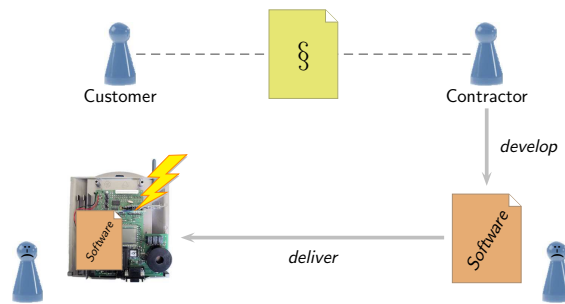
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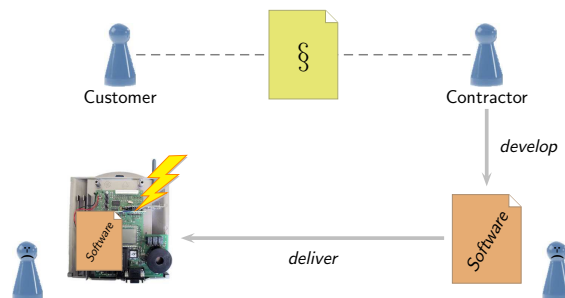
In addition, there is a **high uncertainty** about the outcome:

- given **unclear** requirements,
an appointed **expert witness** may **confirm either interpretation**.



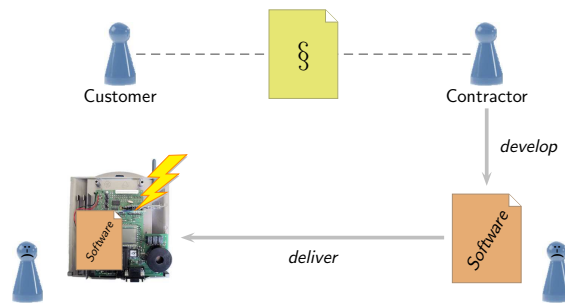
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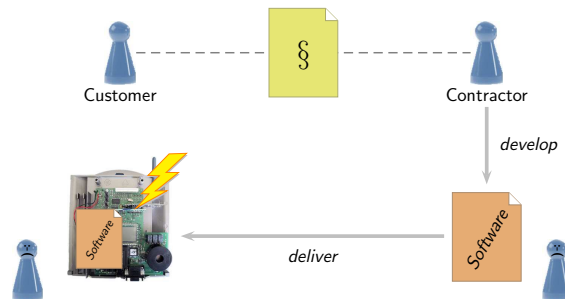
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Many SMEs conclude: subcontracting for software is too **risky**
due to these three main sources of **uncertainty**.

- **(Legal) certainty** is crucial for subcontracting between SMEs:
Outcomes of possible court judgements need to be **as clear as possible**.

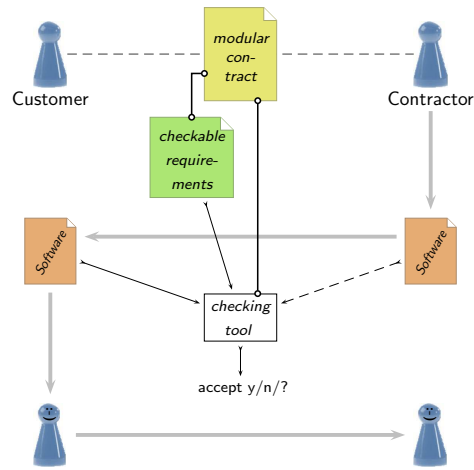
- To **achieve legal certainty**, we need
 - (a) **clear and precise requirements**,
they avoid the 1st source of uncertainty.
 - (b) **clear and precise acceptance testing procedures**,
they avoid the 2nd source of uncertainty.
 - (c) **standardised legal contracts** which integrate (a) and (b),
they avoid the 3rd source of uncertainty.

The contract allows a judge to decide on (a) and (b),
and thus increases legal certainty.

- Introduction
 - What is sub-contracting for software?
 - When is it succesful?
 - Why is it ofen not successful?
- ▶ The Salomo Approach:
 - Overview
 - Checkable Requirements, Checking Tool
 - Regulations in the Contract
- Related Work
- Conclusion and Further Work

Ingredients:

- new notion: **checkable requirement**,
- new notion: **checking tool**.
- a new, **modular** software development **contract**,



The **modular contract**

assumes: a subset of requirements is **designated** as **checkable requirements**,

includes: the **checkable requirements** in **machine-readable** form,

codifies: **agreement** that outcome of corresponding **checking tool** is — with few and exactly specified exceptions — **binding** for both parties,

provides: **legal certainty**.

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checkable Specification/Requirement checking Tool

- A **checkable specification** is a pair (φ, T) comprising a **program property** φ and a **backend** T .
- A **backend** maps a program p and a program property φ to a result $T(p, \varphi) \in \{Yes, No, Unknown\}$ such that the result is
 - **Yes only if** the program **has** the property,
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- A requirement is called **checkable requirement** if and only if a checkable specification can (mechanically) be derived from it.

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- **“The Program Compiles”**: wrapper applies compiler and yields
 - *Yes*, compiler C in version V produces a non-empty executable.
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 - *No*, normal termination and branch coverage below 100%,
 - *Unknown*, otherwise.
- **“Absence of Generic Errors”**: wrapper applies, e.g., Frama-C
 - *Yes*, all assertions related to safe memory access hold or not tried,
 - *No*, at least one assertion has status `surely_invalid`, and
 - *Unknown* otherwise.

- **“The Program Compiles”**: wrapper applies compiler and yields
 - *Yes*, compiler C in version V produces a non-empty executable.
 - *No*, otherwise.
- **“Test Coverage”**: wrapper applies unit-tests
 - *Yes*, normal termination of unit tests indicates 100% branch coverage,
 - *No*, normal termination and branch coverage below 100%,
 - *Unknown*, otherwise.
- **“Absence of Generic Errors”**: wrapper applies, e.g., Frama-C
 - *Yes*, all assertions related to safe memory access hold or not tried,
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- **“Certification”**: expert reviews of programs

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Regulations in the contract

- The **modular software development contract**
 - consists of a **framework contract**, referred to by individual contract,
 - customisation by several **contractual modules**.

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- The **modular software development contract**
 - consists of a **framework contract**, referred to by individual contract,
 - customisation by several **contractual modules**.
- The **acceptance checking procedure** is regulated in two clauses:
 - (i) **checkable requirements** tested with and only with checking tool.
Exit option: if
 - backend is evidently erroneous, or
 - the parties agree to consider the result erroneous, or
 - there is an “*Unknown*” among only “*Yes*”s and “*Unknown*”s, then the clause for other requirements applies.
 - (ii) testing procedure for **other requirements** determined by customer.

utline

- Introduction
 - What is sub-contracting for software?
 - When is it succesful?
 - Why is it ofen not successful?
- The Salomo Approach:
 - Overview
 - Checkable Requirements, Checking Tool
 - Regulations in the Contract
- ▶ Related Work
- Conclusion and Further Work

- (Berenbach, Lo & Sherman, 2010)
Scope limited to the time after the contract has been awarded, limited discussion regarding contract compliance check.
- (Governatori, Milosevic, & Sadiq, 2006) — formalise contract conditions
Use FCL to formalise requirements business rules and tools which decide compliance as acceptance checking procedure.
- (Breaux, Antón, Spafford, 2009) — delegation
We consider top-level obligations and verification sets without delegation.
- (Fanmuy, Fraga & Lloréns, 2012) — requirements verification
Use requirements verification as acceptance checking procedure if creation of a requirements document is subject of a contract.

Conclusion and further work

- We tackle a main challenge of contracting for software: **legal uncertainty**.
- We outline a possible approach to resolve three reasons of uncertainty:
a **modular legal contract** codifies the **mutual agreement**
that **checkable requirements** are verified by **checking tool** exclusively.
- Both, contractor and customer have **strong interest** in obtaining positive checking results since positive results mean **certainty**.
- Our contract is well-suited for a **gradual introduction of formal methods** — any backend is supported as long as both parties agree.
- Formal methods effort promises **increased confidence** in software quality.

Further work:

- legally support traceability, change-requests.
- consider a concept of delegation similar to (Breaux et al., 2009),
- provide more backends.

Thanks.



<http://www.salomo-projekt.de>

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Traces, Interpolants, and Automata:
a New Approach to Automatic Software Verification

Jochen Hoenicke

University of Freiburg

joint work with Andreas Podelski and Matthias Heizmann

16 July 2015

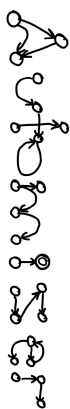
Software Verification

- prove or disprove that a given program satisfies a given specification

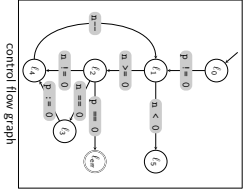
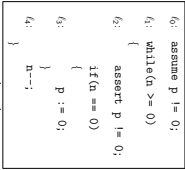
Software Verification

- prove or disprove that a given program satisfies a given specification
- problem is undecidable [Turing, 1936]

ULTIMATE



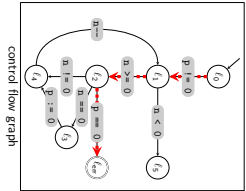
Example



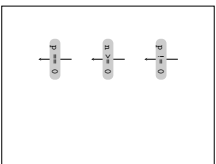
Example

```
l0: assume p != 0;  
l1: while(n >= 0)  
  {  
    assert p != 0;  
    if(n == 0)  
      {  
        p := 0;  
      }  
    n--;  
  }  
l2:  
l3:  
l4:  
}
```

pseudocode

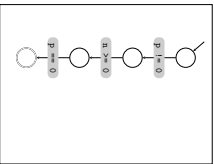


1. take trace m_1

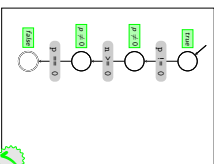


1. take trace π_1
2. consider trace as program P_1

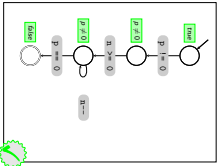
1. assume $p \neq 0$;
2. assume $n >= 0$;
3. assert $p \neq 0$;
pseudocode of P_1



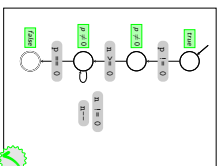
1. take trace π_1
2. consider trace as program P_1
3. analyze correctness of P_1



- $\{p \neq 0\} \vdash \pi \vdash \{p \neq 0\}$ is valid Hoare triple



- $\{p \neq 0\} \quad n \dots \quad \{p \neq 0\}$ is valid Hoare triple
 $\{p \neq 0\} \quad n != 0 \quad \{p \neq 0\}$ is valid Hoare triple

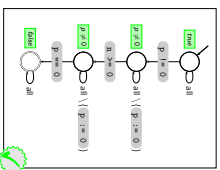


- ▶ add transitions

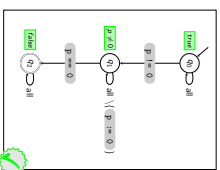


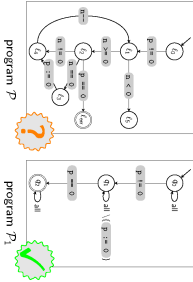
- ▶ add transitions

1. take trace π_1
2. consider trace as program P_1
3. analyze correctness of P_1
4. generalize program P_1
 - ▶ add transitions



1. take trace π_1
2. consider trace as program P_1
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4. generalize program P_1
 - ▶ add transitions
 - ▶ merge locations





New View on Programs

"A program defines a language over the alphabet of statements."

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- Set of statements: *alphabet of formal language*
e.g., $\Sigma = \{ \text{p} := 0, \text{ n} >= 0, \text{ n} = 0, \text{ p} := 0, \text{ n} := 0, \text{ p} == 0, \text{ n} < 0, \dots \}$

New View on Programs

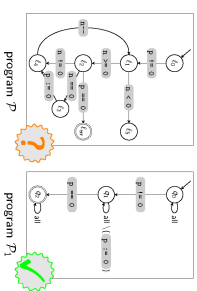
"A program defines a language over the alphabet of statements."

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- Control flow graph: *automation over the alphabet of statements*
- Error location: *accepting state of this automation*

New View on Programs

"A program defines a language over the alphabet of statements"

- ▶ Set of statements: **alphabet of formal language**
e.g., $\Sigma = \{ \text{p} := 0, \text{n} >= 0, \text{n} = 0, \text{p} := 0, \text{n} := 0, \text{p} := 0, \text{m}--, \text{n} < 0, \}$
- ▶ Control flow graph: **automaton over the alphabet of statements**
- ▶ Error location: **accepting state** of this automaton
- ▶ Error trace of program: **word** accepted by this automaton

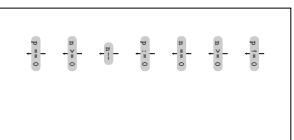




A Venn diagram consisting of two overlapping ellipses. The top ellipse is orange and labeled P . The bottom ellipse is green and labeled P_1 . The intersection of the two ellipses is shaded in a darker green color.

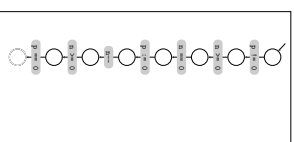


1. take trace π_2

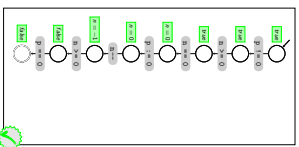


1. take trace π_2

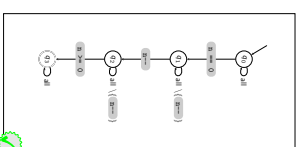
2. Consider trace as program P_2

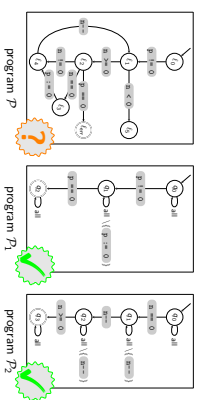


1. take trace π_2
2. consider trace as program P_2
3. analyze correctness or P_2



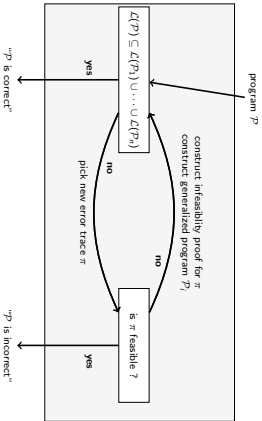
1. take trace π_2
2. consider trace as program P_2
3. analyze correctness of P_2
4. generalize program P_2
 - ▶ add transitions
 - ▶ merge locations



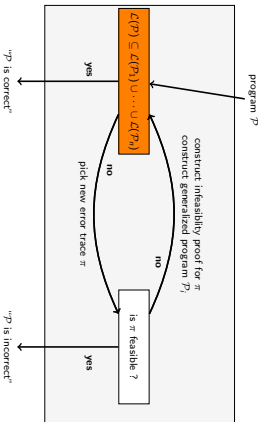


$$P \subseteq P_1 \cup P_2$$

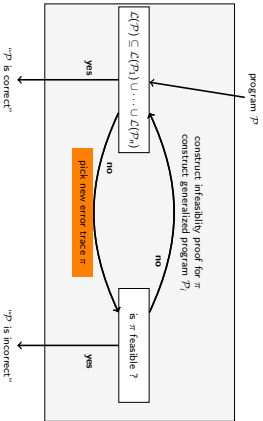
Verification Algorithm



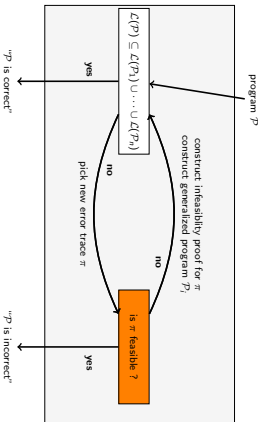
Verification Algorithm



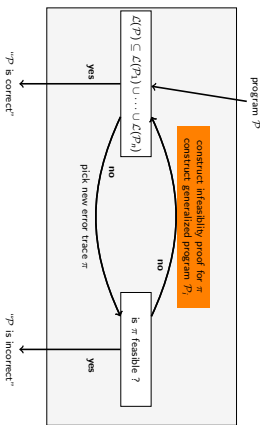
Verification Algorithm



Verification Algorithm

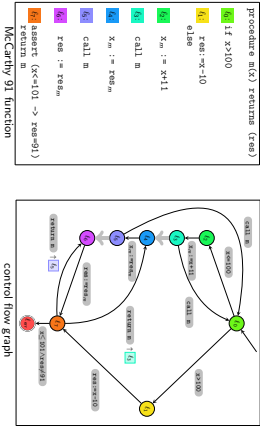


Verification Algorithm

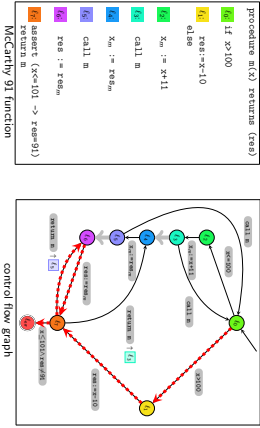


Interprocedural/Recursive Programs

Recursive Programs - Challenge 1: Control Flow



Recursive Programs - Challenge 1: Control Flow



Recursive Programs - Challenge 2: Local Annotations

What is an annotation for an interprocedural execution?

- ▶ state with a stack?
 - ↳ locality of annotation is lost



Recursive Programs - Challenge 2: Local Annotations

What is an annotation for an interprocedural execution?

- ▶ state with a stack?
- locality of annotation is lost



- ▶ only local valuations?

- call/return dependency lost,
- sequence of state assertions is not a proof

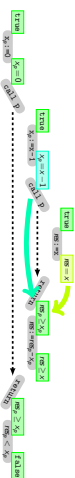


Recursive Programs - Challenge 2: Local Annotations

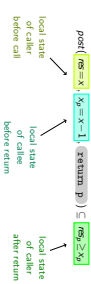
What is an annotation for an interprocedural execution?

Idea: "Nested Interpolants"

Define sequence of state assertions with respect to nested trace.



Define ternary post operator for return statements



Termination Analysis

Termination Analysis

- Challenge 1: counterexample to termination is infinite execution

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Solution: consider infinite traces, use ω -words and Buchi automata

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Solution: consider infinite traces, use ω -words and Buchi automata
- Challenge 2: An infinite trace may not have any execution although each finite prefix has an execution.
while ($x > 0$) {
 $x--$;
}
- E.g., $((x > 0 \ x--)^{\omega})^{\omega}$

Termination Analysis

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Solution: consider infinite traces, use ω -words and Buchi automata
 - Challenge 2: An infinite trace may not have any execution although each finite prefix has an execution.
while ($x > 0$) {
 $x--$;
}
 - E.g., $((x > 0 \ x--)^{\omega})^{\omega}$
- Solution: ranking functions (here: $f(x)=x$)

Ranking Function (for a Loop)

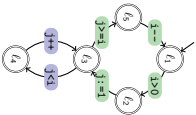
Function from program states to well-founded domain such that value is decreasing while executing the loop body.
Proof by contradiction for the absence of infinite executions.

Example: Bubble Sort

```
program sort(int i, int a[])
l1 while (i>0)
l2   int j:=1
l3   while(j<i)
l4     if (a[j]>a[j+1])
l5       swap(a,j,j+1)
l6     j++
l7   i--
```

Example: Bubble Sort

```
program sort(int i)
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l2   int j:=1
l3   while(j<i)
l4     j++
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Example: Bubble Sort

```

program sort(int i)
  l1 while (i>0)
    l2   inc j:=1
    l3   while(j<i)
    l4     j++
    l5   i--

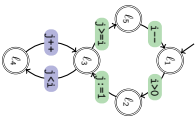
```

quadratic ranking function:

$$f(i,j) = i^2 - j$$

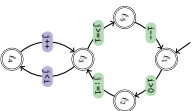
lexicographic ranking function:

$$f(i,j) = (i,j-j)$$



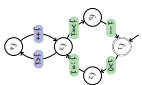
program P

$(\text{OUTER} + \text{INNER})^*$



module P_1

$(\text{INNER}^* \cdot \text{OUTER})^*$

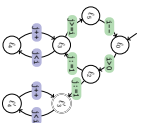


ranking function

$$f(i,j) = i$$

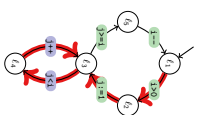
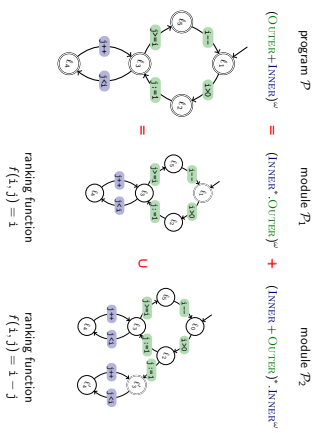
module P_2

$(\text{INNER} + \text{OUTER})^* \cdot \text{INNER}^*$



ranking function

$$f(i,j) = i - j$$



From ω -Trace to Terminating Program – Example

input: ultimately periodic trace

```
1>0 j:=1 (j<4 j++)ω
```

From ω -Trace to Terminating Program – Example

input: ultimately periodic trace

1. consider ω -trace as program with single while loop



From ω -Trace to Terminating Program – Example

input: ultimately periodic trace

1. consider ω -trace as program with single while loop



2. synthesize ranking function

$$f(i,j) = i - j$$

Cohen, Shinar	Synthesis of Linear Ranking Functions	(TACAS 2011)
Franklin, Podelski	A complete method for the synthesis of linear ranking functions	(VMCAI 2009)
Ranking Abstraction Schemes	Termination Analysis of Recursive Linear Loops	(CONCUR 2008)
Ranking Abstraction Schemes	Linear ranking with variability	(CADE 2008)
Ranking Abstraction Schemes	Thy proving ranking predicate	(CADE 2008)
Ranking Abstraction Schemes	Ranking functions for linear invariant loops	(FMC 2011)
Ranking Abstraction Schemes	Linear Ranking for Linear Loop Programs	(ATVA 2011)
Cohen, Shinar, Shinar, Vojtechovsky	Ranking function synthesis for all vector relations	(FMOD 2018)
Ranking	Ranking Functions for Linear Loops	(TACAS 2014)

From ω -Trace to Terminating Program – Example

input: ultimately periodic trace

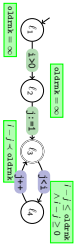
1. consider ω -trace as program with single while loop



2. synthesize ranking function

$$f(i,j) = i - j$$

3. compute rank certificate



From ω -Trace to Terminating Program – Example

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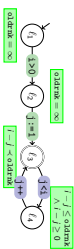
1. consider ω -trace as program with single while loop



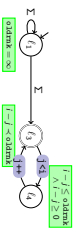
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$f(i,j) = i - j$

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4. add additional transitions



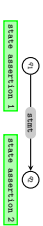
Generalization of Program with Rank Certificate

- Case 1: ϕ not accepting

Hoare triple

$\{ \text{rank_assertion } 1 \} \text{ while } \{ \text{rank_assertion } 2 \}$

automaton transition



Generalization of Program with Rank Certificate

► Case 1: q not accepting
Hoare triple $\{ \text{state assertion 1} \} \text{stmt} \{ \text{state assertion 2} \}$



► Case 2: q accepting
Hoare triple $\{ \text{state assertion 1} \} \text{statement}(x) \text{ stmt} \{ \text{state assertion 2} \}$



Implemented in

Ultimate Büchi Automizer
<http://ultimate.informatik.uni-freiburg.de/BuchiAutomizer/>

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For synthesis of ranking functions for single traces we use the tool:

Ultimate LassoRanker

<http://ultimate.informatik.uni-freiburg.de/LassoRanker/>
developed together with Jan Leike

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Programs with procedures and recursion? Büchi Nested Word Automata!

[illegible]

Future Work

- ▶ verification tasks \leftrightarrow automata
- ▶ optimized inclusion check for Buchi automata
- ▶ different ω -automata in termination analysis

Thank you for your attention!