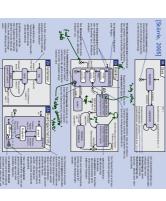
Software Design, Modelling and Analysis in UML

Lecture 15: Hierarchical State Machines I

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initial (shallow) history

fork/join junction, choice

deep history

2 .

UML distinguishes the following kinds of states:

The Full Story

UML State-Machines: What do we have to cover?

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Contents & Goals

Last Lecture:

RTC-Rules: Discard, Dispatch, Commence.

Step. RTC, Divergence

Putting It All Together — Obs for without state.

Rhapsody Demo

- This Lecture:

- Educational Objectives: Capabilities for following tasks/questions.
 What does this State Machine mean? What happens if I inject this event?
 Can you please model the following behaviour.
 What is: initial state.
 What does this horarchical State Machine mean? What may happen if I inject this event? and State Machine mean? What may happen if I inject this can.
 What is: AND-State, OR-State, pseudo-state, entry/exit/do. final state,
- Hierarchical State Machines Syntax

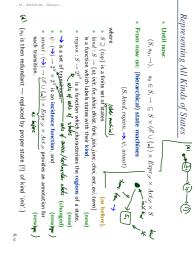
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Hierarchical State Machines

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Representing All Kinds of States







From UML to Hierarchical State Machines: By Example

 $(S,kind,region,\rightarrow,\psi,annot)$

 $\in S$ kind

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simple state (الملاسم الملاسم) final state

composite state

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st.

{ {s, s, s, s}}

pseudo-state

submachine state

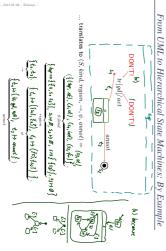
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AND

R.

{{51.51}, {54.52}, {54.52},



• ({c, d, c}, {\partial}, \quad \partial}, \quad \partial \partial \partial \quad \ · ({a,b,c}, {(a,b), (b,c)}) $\left(\left\{a,5,c\right\},\left\{\clubsuit,\wp\right\}\right)$ $\left\{\clubsuit\mapsto\left(a,b\right),\wp\mapsto\left(b,c\right)\right\}\right)$

 $\begin{array}{c} \begin{pmatrix} \ddots & \rightarrow \langle \hat{x} \rangle \rightarrow \langle \hat{x} \rangle \\ \\ & &$

thought system on $\sum_{s}^{p} \times \mathcal{EH}$

(S, Kind, region,), 4, accord)

\$ 100 man 1 man 1





	implicit top state	pseudo-state	composite state	final state	simple state		
	top	œ	œ	s	œ	$\in S$	
	st	init,	st	fin	st	kind	
-	{S ₁ }	0	$\{S_1,\ldots,S_n\}, n\geq 1$ $S_1\cup\cdots\cup S_n$	0	0	$region \subseteq 2^S, S_i \subseteq S$	
(M) (S))	S_1	0	$S_1 \cup \cdots \cup S_n$	0	0	$child \subseteq S$	K

 States e S with kind(s) = st may comprise regions.
 No region: simple state.
 One region: OR-state.
 Two or more regions: AND-state. Final and pseudo states don't comprise regions.



 The region function induces a child function. $P_{\text{option}}(\zeta) = \begin{cases} f_{S_1}, f_{S_2}, \\ f_{S_2}, f_{S_3}, \end{cases}$ g_{SA}

Follows from diagrams because not may not dues . Each state lexcept for top) his in exactly on region



Initial Pseudostates and Final States

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Well-Formedness: Initial State (requirement on diagram)

Plan

- No ingoing transitions to initial states.

No outgoing transitions from final states.

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[s] × ⊗ O **(3)**

- Initial pseudostate, final state.
- Composite states.
 Entry/do/exit actions, internal transitions.
 History and other pseudostates, the rest.

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Initial Pseudostate e then go to a state which is destination of an initiation transition, e execute the action of the chosen initiation transitions between exit and entry actions of seen and dehindra (lota). when entering a region without a specific destination state, with specific destination \$1 /act₂

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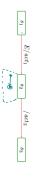
then go to a state which is destination of an initiation transition, execute the action of the chosen initiation transitions between exit and entry actions. when entering a region without a specific destination state, kact₁ 81 82 /act2

Special case: the region of top.

If class C has a state-machine, then "create-C transformer" is the
concatenation of

 the transformer of the "constructor" of C (here not introduced explicitly) and
 a transformer corresponding to one initiation transition of the top region.

Towards Final States: Completion of States



- Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".
- Dispatching (here: E) can then alternatively be viewed as
- (ii) take an enabled transition (here: to s_2). (i) fetch event (here: E) from the ether,
- (iii) remove event from the ether,
- (iv) after having finished entry and do action of current state (here: s_2) the state is then called <code>completed</code> —,
- (v) raise a completion event with strict priority over events from ether
 (vi) if there is a transition enabled which is sensitive for the competion event, then talle if (lener (so., s)).
 otherwise become stable.

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References

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Final States



- ullet a step of object u moves u into a final state (s,fin) , and
- then (conceptionally) a completion event for the current composite state \boldsymbol{s} is all sibling regions are in a final state,
- \bullet If there is a transition of a parent state (i.e., inverse of child) of s enabled which is sensitive for the completion event,
- then take that transition,
- otherwise kill u
- \rightsquigarrow adjust (2.) and (3.) in the semantics accordingly



References

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Final States



then (conceptionally) a completion event for the current composite state \boldsymbol{s} is raised. all sibling regions are in a final state, \bullet a step of object u moves u into a final state $(s,\mathit{fin}),$ and

 \bullet If there is a transition of a parent state (i.e., inverse of child) of s enabled which is sensitive for the completion event,

- then take that transition,
- ullet otherwise kill u
- \leadsto adjust (2.) and (3.) in the semantics accordingly
- One consequence: u never survives reaching a state (s,fin) with $s \in \mathit{child}(top)$.
- Now: in Core State Machines, there is no parent state.
 Later: in Hierarchical ones, there may be one.

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