

Software Design, Modelling and Analysis in UML

Lecture 17: Hierarchical State Machines II

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Contents & Goals

Last Lecture:

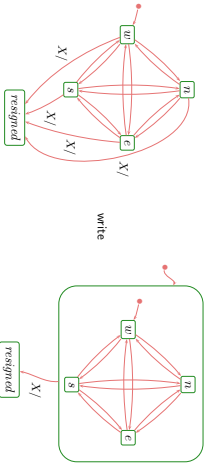
- State Machines and OCL
- Hierarchical State Machines Syntax
- Initial and Final State

This Lecture:

- Educational Objectives: Capabilities for following tasks/questions.
- What does this State Machine mean? What happens if I inject this event?
- Can you please model the following behaviour.
- What does this hierarchical State Machine mean? What may happen if I inject this event?
- What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state, ...
- Content:
- Composite State Semantics
- The Rest

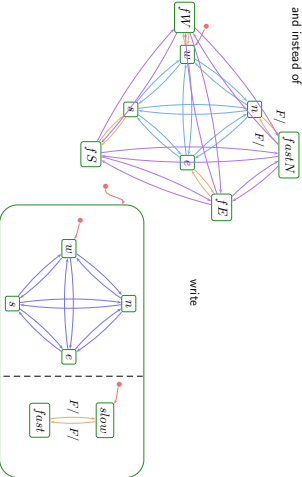
Composite States

- In a sense, composite states are about **abstraction, structuring, and avoiding redundancy.**
- Idea: in Tron, for the Player's StateMachine, instead of

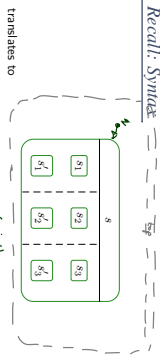


Composite States

and instead of



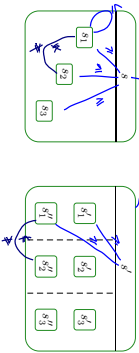
Composite States (formulation follows Demm et al., 2003)



$$\begin{aligned} & \text{translates to} \\ & \left((((s_1, s_2, s_3, s_4, s_1 \mid (s_1, s_2, s_3, s_4, s_1) \mid (s_2, s_3, s_4, s_1) \mid (s_3, s_4, s_1) \mid (s_4, s_1) \mid (s_1, s_2, s_3, s_4, s_1) \right) \right) \end{aligned}$$

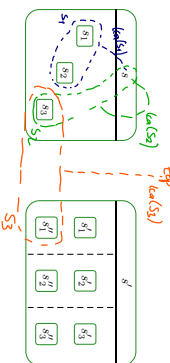
The substrate- (or child-) relation induces a **partial order on states**:

- $top \leq s_k$ for all $k \in S_1$
 - $s \leq s'$, for all $s' \in child(s)$,
 - transitive, reflexive, antisymmetric.
- $\forall s, s' \in S$
 $s \leq s' \wedge s' \leq s \implies s = s'$

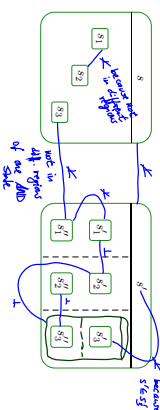


- The **least common ancestor** is the function $lca: 2^S \setminus \{\emptyset\} \rightarrow S$ such that
- The states in S_1 are (transitive) children of $lca(S_1)$, i.e. $lca(S_1) \leq s_i$ for all $s_i \in S_1 \subseteq S$.

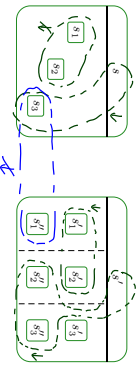
- $lca(S_1)$ is **unique**, i.e. if $s \leq s'$ for all $s \in S_1$, then $s \leq lca(S_1)$
- **Note**: $lca(S_1)$ exists for all $S_1 \subseteq S$ (last candidate: top)



- Two states $s_1, s_2 \in S$ are called **orthogonal**, denoted $s_1 \perp s_2$, if and only if
- they are **unrelated**, i.e. $s_1 \not\leq s_2$ and $s_2 \not\leq s_1$, and
- they **live** in different regions of an AND-state, i.e. $S_{\wedge s_1, s_2}(s) = \{s_1, \dots, s_n\} \exists 1 \leq i \neq j \leq n: s_i \in child^*(s_1) \wedge s_j \in child^*(s_2)$.

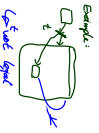


- A set of states $S_1 \subseteq S$ is called **consistent**, denoted by $\perp S_1$, if and only if for each $s_i, s_j \in S_1$,
- $s_i \leq s_j$, or
- $s_j \leq s_i$, or
- $s_i \perp s_j$.



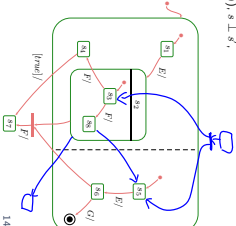
A hierarchical state-machine $(S, kind, region, \rightarrow, \psi, anno)$ is called **well-formed** if and only if for all transitions $t \in \rightarrow$,

- (i) source and destination are consistent, i.e. $\perp source(t)$ and $\perp target(t)$,
 - (ii) source (and destination) states are pairwise orthogonal, i.e.
 - $\forall s, s' \in source(t) (\in target(t)), s \perp s'$,
 - (iii) the top state is neither source nor destination, i.e.
 - $top \notin source(t) \cup target(t)$,
- Recall: final states are not sources of transitions.



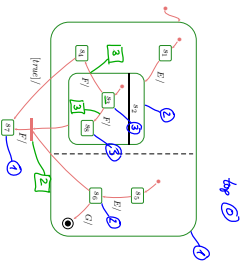
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- $depth(top) = 0$.
- $depth(s') = depth(s) + 1$, for all $s' \in child(s)$

Example



Entry/Do/Exit Actions, Internal Transitions

Enabledness in Hierarchical State-Machines

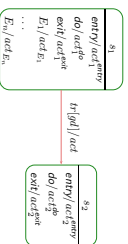
- The scope ("set of possibly affected states") of a transition t is the **least common region** of $source(t) \cup target(t)$.
- Two transitions t_1, t_2 are called **consistent** if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal).
- The **priority** of transition t is the depth of its innermost source state, i.e. $priority(t) := \max\{depth(s) \mid s \in source(t)\}$.
- A set of transitions $T \subseteq \rightarrow$ is **enabled** in an object u , if and only if
 - T is consistent,
 - T is maximal w.r.t. priority,
 - all transitions in T share the same trigger,
 - all guards are satisfied by $\sigma(u)$, and
 - for all $t \in T$, the source states are active, i.e. $source(t) \subseteq \sigma(u)(sk) \subseteq S$.

Transitions in Hierarchical State-Machines

- Let T be a set of transitions enabled in u .
 - Then $(\sigma, \rho) \xrightarrow{trans, S, \text{Std}} (\sigma', \rho')$ if
 - $\sigma'(u)(sk)$ consists of the target states of T , i.e. for simple states the simple states themselves, for composite states the initial states,
 - $\sigma', \rho', \text{cons}$, and S, Std are the effect of firing each transition $t \in T$ **one by one, in any order**, i.e. for each $t \in T$,
 - the exit transformer of all affected states, highest depth first,
 - the transformer of t ,
 - the entry transformer of all affected states, lowest depth first.
- ~> adjust (2), (3), (5) accordingly

Entry/Do/Exit Actions

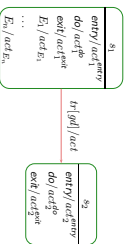
- In general, with each state $s \in S$ there is associated
 - an **entry**, a **do**, and an **exit** action (default: skip)
 - a possibly empty set of trigger/action pairs called **internal transitions**, (default: empty), $E_1, \dots, E_n \in \mathcal{E}, \text{entry}, \text{do}, \text{exit}$ are reserved named

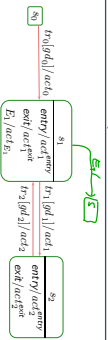


- Recall: each action's supposed to have a transformer. Here: $f_{act1^entry}, f_{act1^do}, \dots$
- Taking the transition above then amounts to applying $f_{act1^entry} \circ f_{act1^do} \circ f_{act1^exit}$ instead of only f_{act1}
- ~> adjust (2), (3) accordingly

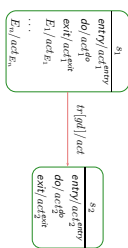
Internal Transitions

- For **internal transitions**, taking the one for E_i , for instance, still amounts to taking **only** $f_{act E_i}$.
- Intuition: The state is neither left nor entered, so: no exit, no entry.
- ~> adjust (2) accordingly.
- Note: internal transitions also start a run-to-completion step.
- Note: the standard seems not to clarify whether internal transitions have **priority** over regular transitions with the same trigger at the same state. Some code generators assume that internal transitions have priority!





- ... as abbreviation for ...
- That is: Entry/Internal/Exit don't add expressiveness to Core State Machines if internal actions should have priority: s1 can be embedded into an Or-state (see later).
- Abbreviation may avoid confusion in context of hierarchical states (see later).



- **Intuition:** after entering a state, start its do-action.
- If the doaction terminates,
- then the state is considered **completed**,
- otherwise,
- if the state is left before termination, the do-action is stopped.
- Recall the overall UML State Machine philosophy:
 - "An object is either **idle** or **doing a run-to-completion step**."
 - Now, what is it exactly while the do action is executing...?

References

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