# Software Design, Modelling and Analysis in UML

Lecture 16: Hierarchical State Machines I

2014-01-15

Prof. Dr. Andreas Podelski, Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

Hierarchical State Machines

If the developer makes a mistake, then  $\mathcal{M}'$  is inconsistent.

Not common: if SM is given, then constraints are also considered when choosing transitions in the RTC-algorithm. In other words: even in presence of mistakes, the SM never move to inconsistent configurations.

Contents & Goals

- Last Lecture:
  Putting it all together: UML model semantics (so far)
  Rhapsody demo, code generation

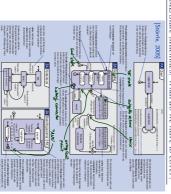
- This Lecture:
- Educational Objectives: Capabilities for following tasks/questions.
   What does this State Machine mean? What happens if I nject this event?
   Can you please model the following behaviour.
   What does this hierarchical State Machine mean? What may happen if I inject this event?
- What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state, ...
- State Machines and OCL
   Hierarchical State Machines Syntax
   Initial and Final State
   Composite State Semantics
- The Rest

State Machines and OCL

2/99

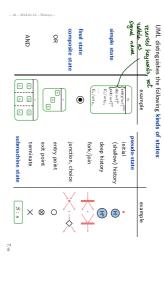
3/59

UML State-Machines: What do we have to cover?



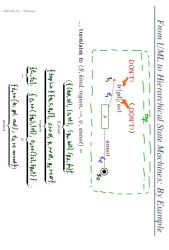
6,/59

### The Full Story



From UML to Hierarchical State Machines: By Example

- 16	- 2014-0	l-15 – Sh	iersyn –					
	pseudo-state	submachine state	AND	composite state OR	final state	simple state		(S
	•, (A),	(later) -			( Ranke	s	example	$(S, kind, region, \rightarrow, \psi, annot)$
(s,kind	٠,4		s	6	10	и	$\in S$	$\psi$ , ann
(s,kind(s)) for short	init, shist,	ı	¥	ĸ	Į.	#	kind	ot)
9/99	Ø		{ {5, 4}, {5, 5!}, {5, 5!},	{ {\$4, \$4, \$4}} region	Ø	Ø	region	



10/99

### Representing All Kinds of States

Representing All Kinds of States

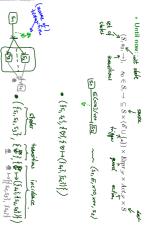
Until now:

• From now on: (hierarchical) state machines

 $(S,s_0,\rightarrow),\quad s_0\in S,\rightarrow \ \subseteq S\times (\mathscr{E}\cup\{{\bot}\})\times Expr_{\mathscr{S}}\times Act_{\mathscr{S}}\times S$  $(S, kind, region, \rightarrow, \psi, annot)$ 

where  $\frac{(adb.pradsind)}{s}$  (as before),  $s \le (ap)$  is a finite set of varies. s and  $s \le -(st. nit. fin. shist. obst. <math>st. fort. join., junc. choi. ent. exi. term) is a function which blood states with their kind. (new)$ 

•  $region: S \to 2^{2^S}$  is a function which characterises the regions of a state, gets of states (new)



# Well-Formedness: Regions (follows from diagram)

<ul><li>No region:</li><li>One region:</li></ul>	<ul> <li>States s ∈ S with</li> </ul>	<ul> <li>Each state (except for top) lies in exactly one region,</li> </ul>		implicit top state	pseudo-state	composite state	final state	simple state	
	kind(	t for t		top	s	œ	s	8	$\in S$
simple state OR-state.	s) = st ma	$p)$ lies in $\epsilon$		st	init,	st	fin	st	kind
	States $s \in S$ with $kind(s) = st$ may comprise regions.		M. V.	$\{S_1\}$	0	$\{S_1,\ldots,S_n\}, n \ge 1$	0	0	$region \subseteq 2^S, S_i \subseteq S$
= {54, 26} 0 \$53,54}	= {5,,52,57,54.}		( E   B	) Sz	0	$S_1 \cup \cdots \cup S_n$	0	0	$child \subseteq S$

The region function induces a child function

Final and pseudo states don't comprise regions.

Two or more regions: AND-state.

 $(s_0 ext{ is then redundant} -- ext{replaced by proper state (!) of kind '<math>init'$ .)  $\bullet$  — is a set of transitions, (**x** -force<sup>1</sup> $\delta$ **a**. \*\*upus') (changed)  $\bullet$   $\circ$  : (—) —  $2^{b} \times 2^{b}$  is an incidence function, and (new)  $\circ$  amost : (—) — ( $\delta$   $\cup$  ( $\bullet$ )  $\times$  Esp $r_{\mathcal{F}} \times$  Ad $_{\mathcal{F}}$  provides an amostation for each transition.

8/59

# Well-Formedness: Initial State (requirement on diagram)

- Each non-empty region has a reasonable initial state and at least one transition from there, i.e.
  for each s ∈ S with region(s) = (S,...,S<sub>n</sub>), n ≥ 1, for each 1 ≤ i ≤ n,
  there exists eactly one initial pseudo-state (S<sub>1</sub>, init) ∈ S<sub>1</sub> and at least one transition t ∈ → with s is a source,
  and such transition target s is in S<sub>1</sub>, and (for simplicity), insul(s) = S<sub>1</sub>, and (for simplicity), insul(s) = S<sub>1</sub>, and
- No ingoing transitions to initial states.
  No outgoing transitions from final states.
  ⊕—√≤ λb!



### Plan



- Initial pseudostate, final state.
- Composite states.
- Entry/do/exit actions, internal transitions.
   History and other pseudostates, the rest.

13/99

## Towards Final States: Completion of States

Initial Pseudostate

 $s_0$ 

annot

81 (act2)
82 (83)



- Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".
- $\bullet$  Dispatching (here: E) can then alternatively be viewed as
- fetch event (here: E) from the ether,
- (ii) take an enabled transition (here: to  $s_2$ ),
- (iii) remove event from the ether,

Special case: the region of top.

If clas C has a state-machine, then "create-C transformer" is the concatenation of constructor" of C (here not introduced explicitly) and a the transformer of the "constructor" of C (here not introduced explicitly) and a transformer corresponding to one initiation transition of the top region.

when entering a region without a specific destination state. الإنه المسائل المسائلة المسائلة

execute the action of the chosen initiation transitions between exit and entry actions(see Leta).

- (iv) after having finished entry and do action of current state (here: s<sub>2</sub>) the state is then called completed q "space"

  (v) raise a completion event with strict priority over events from ether!

- (vi) if there is a transition enabled which is sensitive for the completion event,
   then take it (here: (s2, s3)).
   otherwise become stable.

16/99

## Initial Pseudostates and Final States

14,59

Final States are pass shifted all sibling)regions are in a final state, a step of object u moves u into a final state (s, fin), and 

• If there is a transition of a parent state (i.e., inverse of child) of s enabled which is sensitive for the completion event,

then (conceptionally) a completion event for the current composite state  $\boldsymbol{s}$  is

raised.

- then take that transition,  $\bullet \mbox{ otherwise kill } u$
- $\leadsto$  adjust (2.) and (3.) in the semantics accordingly
- One consequence: u never survives reaching a state (s, fin) with s ∈ child(top).

Composite States
(formalisation follows [Damm et al., 2003])

write

Composite States

In a sense, composite states are about abbreviation, structuring, and avoiding redundancy.

Composite States
and instead of F/ [fastN]

fE.

write

Idea: in Tron, for the Player's Statemachine, instead of

X/ resigned

fS

e słow fast

18/99

19/99

### Syntax: Fork/Join

Recall: Syntax

translates to

\$\frac{\s\_1}{\sigma\_1} \\ \frac{\s\_2}{\sigma\_2} \\ \frac{\s\_2}{\sigma\_3} \\ \frac{\s\_3}{\sigma\_3} \\ \frac\_3} \\ \frac{\s\_3}{\sigma\_3} \\ \frac{\s\_3}{\sigma\_3} \\ \frac{\s

 $\{top \mapsto \{s\}, s \mapsto \{\{s_1, s_1'\}, \{s_2, s_2'\}, \{s_3, s_3'\}\}, s_1 \mapsto \emptyset, s_1' \mapsto \emptyset, \dots\}.$  $\{(top, \mathsf{st}), (s, \mathsf{st}), (s_1, \mathsf{st})(s_1', \mathsf{st})(s_2, \mathsf{st})(s_2', \mathsf{st})(s_3, \mathsf{st})(s_3', \mathsf{st})\},$ 

 $\rightarrow$ ,  $\psi$ , annot)

21/99

 $\,$  For brevity, we always consider transitions with (possibly) multiple sources and targets, i.e.

$$\psi: (\rightarrow) \rightarrow (2^S \setminus \emptyset) \times (2^S \setminus \emptyset)$$

For instance,

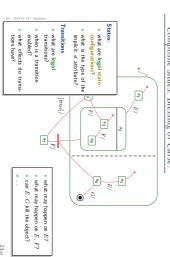


 $\underbrace{(S, \mathit{kind}, \mathit{region}, \underbrace{\{t_1\}}_{-}, \underbrace{\{t_1 \mapsto (\{s_2, s_3\}, \{s_5, s_6\})\}}_{\psi}, \underbrace{\{t_1 \mapsto (tr, \mathit{gd}, \mathit{act})\}})}_{\mathit{annot}}$ 

• Naming convention:  $\psi(t) = (source(t), target(t))$ .

22/99

## Composite States: Blessing or Curse?



### State Configuration

- The type of st is from now on a set of states, i.e.  $st:2^S$
- \* A set  $S_1\subseteq S$  is called (legal) state configurations if and only if  $s(p)\in S_1$ , and  $s(p)\in S_2$ , and  $s(p)\in S_3$  for each state  $s\in S_3$ , for each non-empty region  $\emptyset\neq R\in region(s)$ , exactly one (non pseudo-state) child of s (from R) is in  $S_1$ , i.e.

 $|\{s_0 \in R \mid kind(s_0) \in \{st, fin\}\} \cap S_1| = 1.$ 

24/99

### State Configuration

- The type of st is from now on a set of states, i.e.  $st:2^S$
- \* A set  $S_1\subseteq S$  is called (legal) state configurations if and only if \*  $top\in S_1$ , and \* for each state  $s\in S_1$ , for each non-empty region  $\emptyset \neq R\in region(s)$ , exactly one (non pseudo-state) child of s (from R) is in  $S_1$ , i.e.
- $|\{s_0 \in R \mid kind(s_0) \in \{st, fin\}\} \cap S_1| = 1.$

Examples:

\$3 \$2 \$2

24/99

### A Partial Order on States

A Partial Order on States

The substate- (or child-) relation induces a partial order on states:

• transitive, reflexive, antisymmetric, •  $s' \leq s$  and  $s'' \leq s$  implies  $s' \leq s''$  or  $s'' \leq s'$ .

•  $s \le s'$ , for all  $s' \in child(s)$ , top ≤ s, for all s ∈ S,

> The substate- (or child-) relation induces a partial order on states: top ≤ s, for all s ∈ S,

- $$\begin{split} &*s \leq s', \text{ for all } s' \in child(s), \\ &* \text{ transitive, reflexive, antisymmetric,} \\ &*s' \leq s \text{ and } s'' \leq s \text{ implies } s' \leq s' \text{ or } s'' \leq s'. \end{split}$$

S<sub>1</sub>' S<sub>2</sub>' S<sub>3</sub>' S<sub>3</sub>'

 $s_1$  $s_2$ 

 $s_3$ 

25/99

25/99

### State Configuration

- $\bullet\,$  The type of st is from now on a set of states, i.e.  $st:2^S$
- \* A set  $S_1\subseteq S$  is called (legal) state configurations if and only if s  $top \in S_1$ , and s for each state  $s \in S_1$ , for each non-empty region  $\emptyset \neq R \in region(s)$ , exactly one (non pseudo-state) child of s (from R) is in  $S_1$ , i.e.

 $|\{s_0 \in R \mid kind(s_0) \in \{st, fin\}\} \cap S_1| = 1.$ 

Examples:

24,59

Least Common Ancestor and Ting

\* The least common ancestor is the function  $laa: 2^S \setminus \{\emptyset\} \to S$  such that \* The states in  $S_1$  are (transitive) children of  $lca(S_1)$ , i.e.

 $lca(S_1) \le s$ , for all  $s \in S_1 \subseteq S$ ,

•  $lca(S_1)$  is minimal, i.e. if  $\hat{s} \leq s$  for all  $s \in S_1$ , then  $\hat{s} \leq lca(S_1)$ 

• Note:  $lca(S_1)$  exists for all  $S_1 \subseteq S$  (last candidate: top).

## Least Common Ancestor and Ting

- The least common ancestor is the function  $ka:2^S\setminus\{\emptyset\}\to S$  such that The states in  $S_1$  are (transitive) children of  $ka(S_1)$ , i.e.
- $lca(S_1) \le s$ , for all  $s \in S_1 \subseteq S$ ,
- $lca(S_1)$  is minimal, i.e. if  $\hat{s} \leq s$  for all  $s \in S_1$ , then  $\hat{s} \leq lca(S_1)$
- Note:  $lca(S_1)$  exists for all  $S_1 \subseteq S$  (last candidate: top).

S<sub>1</sub> S<sub>1</sub> S<sub>2</sub>'' S<sub>2</sub>' S<sub>3</sub>''

8  $s_2$  $s_3$ 

26/99

## Least Common Ancestor and Ting

Least Common Ancestor and Ting

• Two states  $s_1,s_2\in S$  are called **orthogonal**, denoted  $s_1\perp s_2$ , if and only if • they are unordered, i.e.  $s_1\not \leq s_2$  and  $s_2\not \leq s_1$ , and • they "live" in different regions of an AND-state, i.e.

 $\exists s, region(s) = \{S_1, \ldots, S_n\} \ \exists 1 \leq i \neq j \leq n: s_1 \in child^*(S_i) \land s_2 \in child^*(S_j),$ 

81  $s_2$  $s_3$ 

S<sub>1</sub> S<sub>2</sub> S<sub>3</sub> S<sub>3</sub> S<sub>3</sub>

- Two states  $s_1,s_2\in S$  are called **orthogonal**, denoted  $s_1\perp s_2$ , if and only if they are unordered, i.e.  $s_1\leq s_2$  and  $s_2\leq s_1$ , and they "live" in different regions of an AND-state, i.e.  $\exists s. region(s)=\{S_1,\dots,S_n\}\ \exists 1\leq i\neq j\leq n: s_1\in dilid^*(S_i)\land s_2\in child^*(S_j).$

27/59

### 27/99

### Legal Transitions

A hiearchical state-machine  $(S,kind,region,\rightarrow,\psi,annot)$  is called **well-formed** if and only if for all transitions  $t\in\rightarrow$ ,

• A set of states  $S_1\subseteq S$  is called consistent, denoted by  $\mid S_1,$  if and only if for each  $s,s'\in S_1,$  •  $s'\in S'$ , or •  $s'\leq s'$ , or •  $s'\leq s'$ , or

\* A set of states  $S_1\subseteq S$  is called consistent, denoted by  $\downarrow S_1$ , if and only if for each  $s,s'\in S_1$ , \*\*  $s\leq s'$ , or \*\*  $s'\leq s'$ , or \*\*  $s'\leq s'$ , or

Least Common Ancestor and Ting

Least Common Ancestor and Ting

- (i) source and destination are consistent, i.e. ↓ source(t) and ↓ target(t).
- (ii) source (and destination) states are pairwise orthogonal, i.e. • forall  $s,s'\in source(t)$  ( $\in target(t)$ ),  $s\perp s'$ ,
- (iii) the top state is neither source nor destination, i.e.
   top ∉ source(t) ∪ source(t).
- Recall: final states are not sources of transitions.

 $s_1$  $s_2$  $s_3$ 

 $s_3''$  $s_3$ 

28/99

28/99

### Legal Transitions

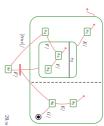
A hiearchical state-machine (S, kind, region, →, ψ, annot) is called well-formed if and only if for all transitions t ∈ →,

(i) source and destination are constent, i.e. † source() and † target(t),

(ii) source (and destination) states are prinvise orthogonal, i.e.

• forall s, s' ∈ source(t) (∈ target(t)), s ⊥ s',

- (ii) the top state is neither source nor destination, i.e.
   top ∉ source(t) ∪ source(t).
- Recall: final states are not sources of transitions.



# Enabledness in Hierarchical State-Machines

 $\bullet$  . The scope ("set of possibly affected states") of a transition t is the least common region of

 $source(t) \cup target(t)$ .

31/99

The Depth of States

$$\begin{split} \bullet \ depth(top) &= 0, \\ \bullet \ depth(s') &= depth(s) + 1, \text{ for all } s' \in child(s) \end{split}$$

30/59

The Depth of States

$$\begin{split} \bullet \ \ depth(top) &= 0, \\ \bullet \ \ depth(s') &= depth(s) + 1, \text{ for all } s' \in child(s) \end{split}$$

30/99

### SI E/ S4 F/ F/ \$3 F/ \$5 G/

## Enabledness in Hierarchical State-Machines

- $\bullet$  The scope ("set of possibly affected states") of a transition t is the least common region of
- $source(t) \cup target(t)$ .
- $\circ$  Two transitions  $t_1,t_2$  are called consistent if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal).

## Enabledness in Hierarchical State-Machines

- $\bullet$  . The scope ("set of possibly affected states") of a transition t is the least common region of
- $source(t) \cup target(t)$ .
- Two transitions t<sub>1</sub>, t<sub>2</sub> are called consistent if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal).
   The priority of transition t is the depth of its innermost source state, i.e.

 $prio(t) := \max\{depth(s) \mid s \in source(t)\}$ 

31/59

## Enabledness in Hierarchical State-Machines

 $\bullet$  The scope ("set of possibly affected states") of a transition t is the least common region of

 $source(t) \cup target(t)$ .

Two transitions t<sub>1</sub>, t<sub>2</sub> are called consistent if and only if their scopes are
orthogonal (i.e. states in scopes pairwise orthogonal).
 The priority of transition t is the depth of its innermost source state, i.e.

 $prio(t) := \max\{depth(s) \mid s \in source(t)\}$ 

• A set of transitions  $T \subseteq \rightarrow$  is **enabled** in an object u if and only if T is consistent,

• T is maximal wrt. priority, • all transitions in T share the same trigger, • all guards are satisfied by  $\sigma(u)$ , and • for all  $t \in T$ , the source states are active, i.e.

 $source(t) \subseteq \sigma(u)(st) \ (\subseteq S).$ 

31/99

## Transitions in Hierarchical State-Machines

- Let T be a set of transitions enabled in u.
- Then  $(\sigma, \varepsilon) \xrightarrow{(cons, Snd)} (\sigma', \varepsilon')$  if
- $\sigma'(u)(st)$  consists of the target states of t,
- i.e. for simple states the simple states themselves, for composite states the initial states,
- $\sigma'$ ,  $\varepsilon'$ , cons, and Snd are the effect of firing each transition  $t\in T$  one by one, in any order, i.e. for each  $t\in T$ ,
- the exit transformer of all affected states, highest depth first,
  the transformer of t,
  the entry transformer of all affected states, lowest depth first.

 $\rightsquigarrow$  adjust (2.), (3.), (5.) accordingly.

32/99

33,59

Entry/Do/Exit Actions

Entry/Do/Exit Actions • In general, with each state  $s \in S$  there is associated

an entry, a do, and an exit action (default: skip)

 $c_1$   $c_1$   $c_1$   $c_1$   $c_2$   $c_1$   $c_2$   $c_3$   $c_4$   $c_4$ 

 $tr[gd]/act = \frac{s_2}{entry/act_2^{san}}$   $do/act_2^{so}$   $exit/act_2^{so}$ 

a possibly empty set of trigger/action pairs called internal transitions,

(default: empty).  $E_1,\ldots,E_n\in\mathscr{E}$ , 'entry', 'do', 'exit' are reserved names!

• In general, with each state  $s \in S$  there is associated

 $entry/act_1^{entry}$   $do/act_1^{do}$   $exit/act_1^{exit}$   $E_1/act_{E_1}$ 

tr[gd]/act entry/actanary
do/actan

- an entry, a do, and an exit action (default: skip)
- a possibly empty set of trigger/action pairs called internal transitions,

(default: empty).  $E_1,\ldots,E_n\in\mathscr{E}$ , 'entry', 'do', 'exit' are reserved names!

- ullet Recall: each action's supposed to have a transformer. Here:  $t_{act_1^{out}}, t_{act_2^{out}}, \ldots$
- Taking the transition above then amounts to applying

 $t_{act_{s_2}^{entr}} \circ t_{act} \circ t_{act_{s_1}^{evit}}$ 

instead of only

 $\leadsto$  adjust (2.), (3.) accordingly.

34/99

34/99

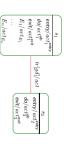
Entry/Do/Exit Actions, Internal Transitions

Internal Transitions



- For internal transitions, taking the one for E<sub>1</sub>, for instance, still amounts to taking only  $t_{act_{E_1}}$
- Intuition: The state is neither left nor entered, so: no exit, no entry.
- → adjust (2.) accordingly.
- Note: internal transitions also start a run-to-completion step.

### Internal Transitions



- ullet For internal transitions, taking the one for  $E_1$ , for instance, still
- Intuition: The state is neither left nor entered, so: no exit, no entry. amounts to taking only  $t_{acts_1}$

→ adjust (2.) accordingly.

Note: internal transitions also start a run-to-completion step.

 Note: the standard seems not to clarify whether internal transitions have priority over regular transitions with the same trigger at the same state. Some code generators assume that internal transitions have priority!

Alternative View: Entry/Exit/Internal as Abbreviations

	[	て.	$tr_0[ad_o]/act_0$		
$E_1/act_{E_1}$	exit/action	entry/act1	entry entry	81	
	$tr_2[gd_2]/act_2$	v (f. f.) v	tr, [ad, ] /act,		
	exit/acteat	entry/actantry	82		

... as abbrevation for ...

 $s_0$  $s_1$ 

 $s_2$ 

36/99

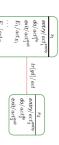
Do Actions

 $entry/act_1^{entry}$   $do/act_1^{to}$   $exit/act_1^{exit}$   $E_1/act_{E_1}$ tr[gd]/act  $entry/act_2^{ent}$   $do/act_2^{do}$   $exit/act_2^{ox}$ 

- Intuition: after entering a state, start its do-action
- If the do-action terminates,
- then the state is considered completed,
- otherwise.
   if the state is left before termination, the do-action is stopped.

37/99

Do Actions



- Intuition: after entering a state, start its do-action.
- If the do-action terminates,
- then the state is considered completed,
- otherwise,
   if the state is left before termination, the do-action is stopped.
- Recall the overall UML State Machine philosophy:
   "An object is either idle or doing a run-to-completion step."
   Now, what is it exactly while the do action is executing...?

37/99

Alternative View: Entry/Exit/Internal as Abbreviations



- ... as abbrevation for ...
- $s_1$

 $s_2$ 

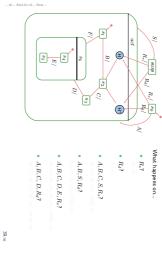
 $s_0$ 

- That is: Entry/Internal/Exit don't add expressive power to Core State Machines.
   If internal actions should have priority, s<sub>1</sub> can be embedded into an OR-state (see later).
- Abbreviation may avoid confusion in context of hierarchical states (see later).

36/59

The Concept of History, and Other Pseudo-States

## History and Deep History: By Example



### Junction and Choice

- Junction ("static conditional branch"):
- good: abbreviation
- unfolds to so many similar transitions with different guards, the unfolded transitions are then checked for enabledness
   at best, start with trigger, branch into conditions, then apply actions
- Choice: ("dynamic conditional branch")

ķ

evil: may get stuck

- enters the transition without knowing whether there's an enabled path
   at best, use "else" and convince yourself that it cannot get stuck
   maybe even better: avoid

Note: not so sure about naming and symbols, e.g., I'd guessed it was just the other way round...

40/99

### Junction and Choice

Junction ("static conditional branch"):



Junction and Choice

Junction ("static conditional branch"):

good: abbreviation
 antiods to so many similar transitions with different guards,
 the unfolded transitions are then checked for enabledness
 at best, start with trigger, branch into conditions, then apply actions

Choice: ("dynamic conditional branch")



Note: not so sure about naming and symbols, e.g., I'd guessed it was just the other way round...

¢

Choice: ("dynamic conditional branch")

×

Note: not so sure about naming and symbols, e.g., I'd guessed it was just the other way round...

40,59

40/99

# Entry and Exit Point, Submachine State, Terminate

- Hierarchical states can be "folded" for readability. (but: this can also hinder readability.)
- ullet Can even be taken from a different state-machine for re-use.  $\begin{tabular}{c} S:s \end{tabular}$



# Entry and Exit Point, Submachine State, Terminate

- Hierarchical states can be "folded" for readability. (but: this can also hinder readability.)
- ullet Can even be taken from a different state-machine for re-use.  $\begin{tabular}{c} S:s \end{tabular}$

Entry/exit points



- $\bigcirc$   $\otimes$  Provide connection points for finer integration into the current level, than just via initial state.
- Semantically a bit tricky:
- First the exit action of the exiting state,

- then the actions of the transition.
  then the entry actions of the entreed state,
  then action of the transition from the entry point to an internal state,
  and then that internal state's entry action.

41/99

# Entry and Exit Point, Submachine State, Terminate

- Hierarchical states can be "folded" for readability.
   (but: this can also hinder readability.)
- Can even be taken from a different state-machine for re-use. S:s
- Entry/exit points
- Provide connection points for finer integration into the current level, than just via initial state.
- Semantically a bit tricky:
- First the exit action of the exiting state,
- then the actions of the transition,
   then the entry actions of the entered state,
- then action of the transition from the entry point to an internal state,
- and then that internal state's entry action.
- Terminate Pseudo-State
   When a terminate pseudo-state is reached, the object taking the transition is immediately killed.

Deferred Events in State-Machines

× 41/99

### Deferred Events: Idea

For ages, UML state machines comprises the feature of deferred events.

Consider the following state machine:

The idea is as follows:



- Assume we're stable in s<sub>1</sub>, and F is ready in the ether.
- In the framework of the course, F is discarded.
   But we may find it a pity to discard the poor event and may want to remember it for later processing, e.g. in \*2, in other words, defer it.

### Deferred Events: Idea

For ages, UML state machines comprises the feature of deferred events.

Consider the following state machine:

The idea is as follows:



- Assume we're stable in s<sub>1</sub>, and F is ready in the ether.
- In the framework of the course, F is discarded.
   But we may find it a pity to discard the poor event and may want to remember it for later processing, e.g. in \*2, in other words, defer it.

### General options to satisfy such needs:

- Provide a pattern how to "program" this (use self-loops and helper attributes).
- Turn it into an original language concept.

43/99

43/99

### Deferred Events: Idea

For ages, UML state machines comprises the feature of deferred events.

Consider the following state machine:

The idea is as follows:

- $s_1 \xrightarrow{E/} s_2 \xrightarrow{F/} s_3$
- Assume we're stable in s<sub>1</sub>, and F is ready in the ether.
   In the framework of the course, F is discarded.

43,59

42/99

Deferred Events: Idea

For ages, UML state machines comprises the feature of deferred events.

The idea is as follows:

Consider the following state machine:



Assume we're stable in s<sub>1</sub>, and F is ready in the ether.

In the framework of the course, F is discarded.
 But we may find it a pity to discard the poor event and may want to remember it for later processing, e.g. in s<sub>2</sub>, in other words, defer it.

General options to satisfy such needs:

- Provide a pattern how to "program" this (use self-loops and helper attributes).
- Turn it into an original language concept. (← OMG's choice)

## Deferred Events: Syntax and Semantics

- Syntactically,
   Each state has (in addition to the name) a set of deferred events.
   Default: the empty set.

44/99

Active and Passive Objects [Harel and Gery, 1997]

Deferred Events: Syntax and Semantics

Deferred Events: Syntax and Semantics

Syntactically,
 Each state has (in addition to the name) a set of deferred events.
 Default: the empty set.

The semantics is a bit intricate, something like

• if an event E is dispatched, • and there is no transition enabled to consume E, • and E is in the deferred set of the current state configuration, • then stuff E into some "deferred events space" of the object, (e.g. into the ether (= extend  $\sigma$ ) or into the local state of the object (= extend  $\sigma$ ))

- Syntactically,
   Each state has (in addition to the name) a set of deferred events.
- Default: the empty set.
- The semantics is a bit intricate, something like

- if an event E is dispatched,
   and there is no transition enabled to consume E,
   and E is in the deferred set of the current state configuration,
   then suff E into some "deferred events space" of the object, (e.g. into the ether (= extend ε) or into the local state of the object (= extend σ))
- and turn attention to the next event.

44/99

 Is the order of deferred events preserved? • Is there a priority between deferred and regular events?

Not so obvious:

and turn attention to the next event.

[Fecher and Schönborn, 2007], e.g., claim to provide semantics for the complete Hierarchical State Machine language, including deferred events.

44,59

### What about non-Active Objects?

What about non-Active Objects?

 We're still working under the assumption that all classes in the class diagram (and thus all objects) are active. That is, each object has its own thread of control and is (if stable) at any time ready to process an event from the ether.

- We're still working under the assumption that all classes in the class diagram (and thus all objects) are active.
- That is, each object has its own thread of control and is (if stable) at any time ready to process an event from the ether.

But the world doesn't consist of only active objects.

For instance, in the crossing controller from the exercises we could wish to have the whole system live in one thread of control.

So we have to address questions like:

- Can we send events to a non-active object?
  And if so, when are these events processed?
  etc.

46/99

45/99

## Active and Passive Objects: Nomenclature

[Harel and Gery, 1997] propose the following (orthogonal!) notions:

- A class (and thus the instances of this class) is either active or passive as declared in the class diagram.
   An active object has (in the operating system sense) an own thread: an own program counter, an own stack, etc.
   A passive object doesn't.

47/99

So why don't we understand passive/reactive?

Passive and Reactive

• Assume passive objects  $u_1$  and  $u_2$ , and active object  $u_i$  and that there are events in the ether for all three.

Which of them (can) start a run-to-completion step...?

Do run-to-completion steps still interleave...?

48/99

## Active and Passive Objects: Nomenclature

[Harel and Gery, 1997] propose the following (orthogonal!) notions:

- $\,\circ\,$  A class (and thus the instances of this class) is either active or passive as declared in the class diagram.
- An active object has (in the operating system sense) an own thread: an own program counter, an own stack, etc.
   A passive object doesn't.
- A class is either reactive or non-reactive.
- A reactive class has a (non-trivial) state machine.
- A non-reactive one hasn't.

### 47/99

### Passive and Reactive

- So why don't we understand passive/reactive?
- Assume passive objects  $u_1$  and  $u_2$ , and active object u, and that there are events in the ether for all three.
- Which of them (can) start a run-to-completion step...?

  Do run-to-completion steps still interleave...?

### Reasonable Approaches:

- Avoid for instance, by
- require that reactive implies active for model well-formedness.
   requiring for model well-formedness that events are never sent to instances of non-reactive classes.
- Explain here: (following [Harel and Gery, 1997])
- Delegate all dispatching of events to the active objects.

48/99

## Active and Passive Objects: Nomenclature

[Harel and Gery, 1997] propose the following (orthogonall) notions:

- A class (and thus the instances of this class) is either active or passive as declared in the class diagram.
- An active object has (in the operating system sense) an own thread: an own program counter, an own stack, etc.
   A passive object doesn't.
- A class is either reactive or non-reactive.
- A reactive class has a (non-trivial) state machine.
- A non-reactive one hasn't.

### Which combinations do we understand?

non-reactive	reactive	
3	V	active
3	(*)	passive

47,59

## If u is an instance of an active class, then u<sub>a</sub> = u.

Passive Reactive Classes

• Firstly, establish that each object u knows, via (implicit) link itsAct, the active object  $u_{act}$  which is responsible for dispatching events to u.

### Passive Reactive Classes

- Firstly, establish that each object u knows, via (implicit) link itsAct,
   the active object uact which is responsible for dispatching events to u.
- If u is an instance of an active class, then u<sub>a</sub> = u.



### Sending an event:

- Establish that of each signal we have a version  $E_C$  with an association  $dest: C_{0,1}, C \in \mathscr{C}$ .
- Then n!E in  $u_1:C_1$  becomes: Create an instance  $u_e$  of  $E_{C_2}$  and set  $u_e$ 's dest to  $u_d:=\sigma(u_1)(n)$ .
- Send to  $u_a:=\sigma(\sigma(u_1)(n))(itsAct)$ , i.e.,  $\varepsilon'=\varepsilon\oplus(u_a,u_e)$ .

49/99

### Passive Reactive Classes

- Firstly, establish that each object u knows, via (implicit) link usAct,
   the active object uact which is responsible for dispatching events to u.
- If u is an instance of an active class, then ua = u.



And What About Methods?

### Sending an event:

- Establish that of each signal we have a version  $E_C$  with an association  $dest: C_{0,1}, C \in \mathscr{C}$ .
- Then n!E in u<sub>1</sub>: C<sub>1</sub> becomes: Create an instance u<sub>e</sub> of E<sub>C2</sub> and set u<sub>e</sub>'s dest to u<sub>d</sub> := σ(u<sub>1</sub>)(n).

### Dispatching an event:

 Say u<sub>e</sub> is ready in the ether for u<sub>a</sub>. Observation: the ether only has events for active objects.

 $* \ \, \text{Send to} \ \, u_a := \sigma(\sigma(u_1)(n))(\#sAd), \qquad * \ \, u_d \ \, \text{may in particular discard event.} \\ \text{i.e., } \varepsilon' = \varepsilon \oplus (u_a, u_e).$ • Then  $u_a$  asks  $\sigma(u_e)(dest) = u_d$  to process  $u_e$  — and waits until completion of corresponding RTC.

49/5

50,59

### And What About Methods?

And What About Methods?

In the current setting, the (local) state of objects is only modified by actions of transitions, which we abstract to transformers.

 UML follows an approach to separate
 the interface declaration from In general, there are also methods.

the implementation.

In C++ lingo: distinguish declaration and definition of method.

- In the current setting, the (local) state of objects is only modified by actions of transitions, which we abstract to transformers.
- In general, there are also methods.
- UML follows an approach to separate
- the interface declaration from
- In C++ lingo: distinguish declaration and definition of method. the implementation.
- In UML, the former is called behavioural feature and can (roughly) be

• a call interface  $f( au_{1},\ldots, au_{n_{1}}): au_{1}$  $., \tau_{1,n_1}) : \tau_1 P_1$ 

a signal name E

51/99

Note: The signal list is redundant as it can be looked up in the state machine of the class. But: certainly useful for documentation.

51/99

Behavioural Features

### Semantics:

- The implementation of a behavioural feature can be provided by:
- An operation.

The class' state-machine ("triggered operation").

### Behavioural Features

$(\mathcal{D}_{i,1},, \mathcal{D}_{i,n_1}) : \mathcal{D}_i P_1$ $(\mathcal{D}_{i,1},, \mathcal{D}_{i,n_2}) : \mathcal{D}_i P_2$ nal() E
---

- The implementation of a behavioural feature can be provided by:
- An operation.
- In a setting with Java as action language: operation is a method body. In our setting, we simply assume a transformer like  $T_l$ . It is then, e.g. clear how to admit method calls as actions on transitions: function composition of transformers (clear but tedious: non-termination).
- The class' state-machine ("triggered operation").

52/99

- The class' state-machine ("triggered operation").

Behavioural Features

- The implementation of a behavioural feature can be provided by:
- An operation.
- In our setting, we simply assume a transformer like  $T_f$ . It is then, e.g. clear how to admit method calls as actions on transitions: function composition of transformers (clear but tedious: non-termination).
- In a setting with Java as action language: operation is a method body.

- Calling F with n<sub>2</sub> parameters for a stable instance of C creates an auxiliary event E and dispatches it (bypassing the ether).
   Transition actions my fill in the return value.
   On completion of the RTC step, the call returns.
   For a non-stable instance, the caller blocks until stability is reached again.

52/99

Semantic Variation Points Pessimistic view: They are legion... For instance,

Discussion.

Behavioural Features: Visibility and Properties

Visibility:

Extend typing rules to sequences of actions such that a well-typed action sequence only calls visible methods.

• Useful properties:

 concurrency
 concurrency
 concurrency
 concurrency
 concurrency
 concurrency
 guarded — some mechanism ensures/should ensure mutual exclusion
 sequential — is not thread safe, users have to ensure mutual exclusion isQuery — doesn't modify the state space (thus thread safe)

Exercise: Search the standard for "semantical variation point".

 (implicitly) enforce determinism, e.g. by considering the order in which things have been added to the CASE tool's repository, or graphical order allow absence of initial pseudo-states can then "be" in enclosing state without being in any substate; or assume one of the children states non-deterministically

54/99

For simplicity, we leave the notion of steps untouched, we construct our semantics around state machines.
 Yet we could explain pre/post in OCL (if we wanted to).

53/99

Behavioural Features: Visibility and Properties

Extend typing rules to sequences of actions such that a well-typed action sequence only calls visible methods.

53/59

### Semantic Variation Points

### Pessimistic view: They are legion...

- For instance,
- allow absence of initial pseudo-states
   can then "be" in enclosing state without being in any substate; or assume one of the children states non-deterministically
- (implicity) enforce determinism, e.g.
   by considering the order in which things have been added to the CASE tool's repository, or graphical order
- Exercise: Search the standard for "semantical variation point".
- [Crane and Dingel, 2007], e.g., provide an in-depth comparison of Statemate, UML, and Rhapsody state machines the bottom line is:
- the intersection is not empty
   (i.e. there are pictures that mean the same thing to all three communities)
   • none is the subset of another
   (i.e. for each pair of communities exist pictures meaning different things)

55/99

Pessimistic view: They are legion..

Semantic Variation Points

- For instance,
- allow absence of initial pseudo-states can then "be" in enclosing state without being in any substate; or assume one of the children states non-deterministically
- (implicitly) enforce determinism, e.g.
   by considering the order in which things have been added to the CASE tool's repository, or graphical order
- Exercise: Search the standard for "semantical variation point"
- [Crane and Dingel, 2007], e.g., provide an in-depth comparison of Statemate, UML, and Rhapsody state machines the bottom line is:
- (ie. there are pictures that mean the same thing to all three communities)

  none is the subset of another

  (ie. for each pair of communities exist pictures meaning different things)

Optimistic view: tools exist with complete and consistent code generation.

55/99

Course Map

 $\mathcal{S} = (\mathcal{F}, \mathcal{C}, V, atr), SM$ 

٠ •

. Θ∈ 0CL

References

8

57/99

G = (N, E, f)

Mathematics  $w_{\pi} = ((\sigma_i, cons_i, Snd_i))_{i \in \mathbb{N}}$ 

58/99

You are here.

56,59

References

[Crane and Dingel, 2007]. Crane, M. L. and Dingel, J. (2007). UML vs. classical vs. rhapsody statecharts: not all models are created equal. *Software and Systems Modeling*, 6(4):415–435.

[Damm et al., 2003] Damm, W., Josko, B., Volintessa, A., and Pouali, A. (2003). A formal semantics for a UML lend language 1.2. IST/33522/WP 1.1/D11.2-Part1. Version 1.2. Fecher and Schönborn, 2007] Fecher, H. and Schönborn, J. (2007). UML 2.0 state 1.2. machines: Complete formal semantics via core state machines. In Brim, L., Haverbort, B. R., Luetzer, M., and van de Pol, J., editors, FMICS/PDMC, volume 4346 of LNCS, pages 244–260. Springer.

[Harel and Gery, 1997] Harel, D. and Gery, E. (1997). Executable object modeling with statecharts. *IEEE Computer*, 30(7):31–42.

[OMG, 2007] OMG (2007). Unified modeling language: Superstructure, version 2.1.2.
Technical Report formal/07-11-02. [Harel and Kugler, 2004] Harel, D. and Kugler, H. (2004). The rhapsody semantics of statecharts. In Ehrig, H., Damm, W., Golbi-Bhode, M., Reif, W., Schnieder, E., and Westkämper, E., eddores, Integration of Software Specification Techniques for Applications in Engineering, number 3147 in LNCS, pages 325–334. Springer-Verlag.

[Störrle, 2005] Störrle, H. (2005). UML 2 für Studenten. Pearson Studium.