

Software Design, Modelling and Analysis in UML

Lecture 16: Hierarchical State Machines I

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Contents & Goals

Last Lecture:

- Missing transformers: create and destroy
- Step and run-to-completion (RTC) step, divergence

This Lecture:

- **Educational Objectives:** Capabilities for following tasks/questions.
 - What does this State Machine mean? What happens if I inject this event?
 - Can you please model the following behaviour.
 - What does this **hierarchical** State Machine mean? What **may happen** if I inject this event?
 - What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state, ...
- **Content:**
 - Putting it all together: UML model semantics (so far)
 - State Machines and OCL
 - Hierarchical State Machines Syntax
 - Initial and Final State

Putting It All Together

The Missing Piece: Initial States

Recall: a labelled transition system is (S, \rightarrow, S_0) . We **have**

- S : system configurations (σ, ε)
- \rightarrow : labelled transition relation $(\sigma, \varepsilon) \xrightarrow[u]{(cons,Snd)} (\sigma', \varepsilon')$.

Wanted: initial states S_0 .

Proposal:

Require a (finite) set of **object diagrams** \mathcal{OD} as part of a UML model

$$(\mathcal{CD}, \mathcal{SM}, \mathcal{OD}).$$

And set

$$S_0 = \{(\sigma, \varepsilon) \mid \sigma \in G^{-1}(\mathcal{OD}), \mathcal{OD} \in \mathcal{OD}, \varepsilon \text{ empty}\}.$$

Other Approach: (used by Rhapsody tool) multiplicity of classes.

We can read that as an abbreviation for an object diagram.

Semantics of UML Model — So Far

The **semantics** of the **UML model**

$$\mathcal{M} = (\mathcal{CD}, \mathcal{SM}, \mathcal{OD})$$

where

- some classes in \mathcal{CD} are stereotyped as ‘signal’ (standard), some signals and attributes are stereotyped as ‘external’ (non-standard),
- there is a 1-to-1 relation between classes and state machines,
- \mathcal{OD} is a set of object diagrams over \mathcal{CD} ,

is the **transition system** (S, \rightarrow, S_0) constructed on the previous slide.

The **computations of** \mathcal{M} are the computations of (S, \rightarrow, S_0) .

State Machines and OCL

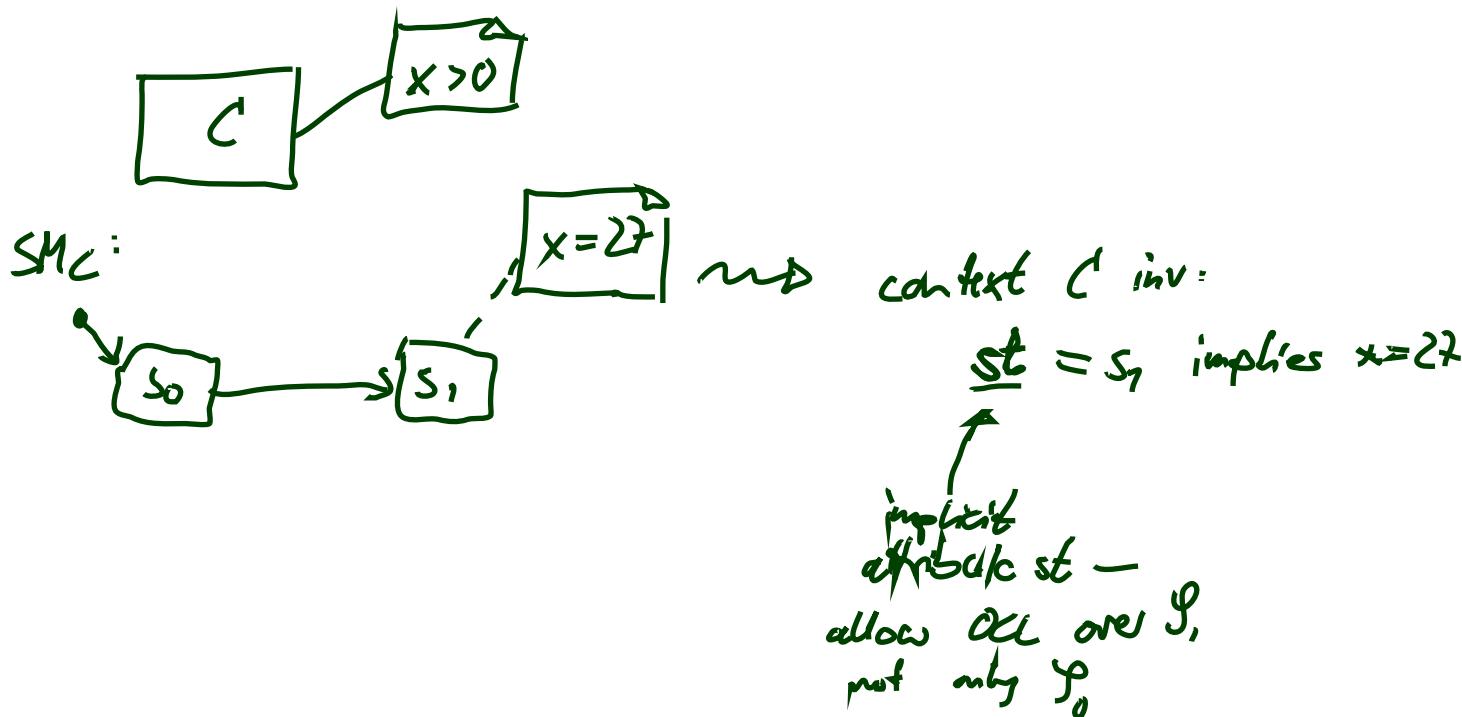
OCL Constraints and Behaviour

- Let $\mathcal{M} = (\mathcal{CD}, \mathcal{SM}, \mathcal{OD})$ be a UML model.
- We call \mathcal{M} **consistent** iff, for each OCL constraint $expr \in Inv(\mathcal{CD}) \cup Inv(\mathcal{SM})$

$\sigma \models expr$ for each “reasonable point” (σ, ε) of computations of \mathcal{M} .

(Cf. exercises and tutorial for discussion of “reasonable point”.)

Note: we could define $Inv(\mathcal{SM})$ similar to $Inv(\mathcal{CD})$.



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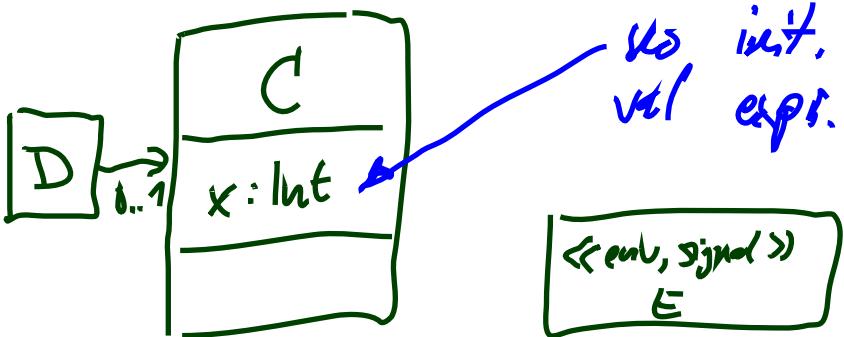
Note: we could define $Inv(\mathcal{SM})$ similar to $Inv(\mathcal{CD})$.

→ OUR CHOICE: check for each (σ, ε) in a computation (skip granularity)

Pragmatics:

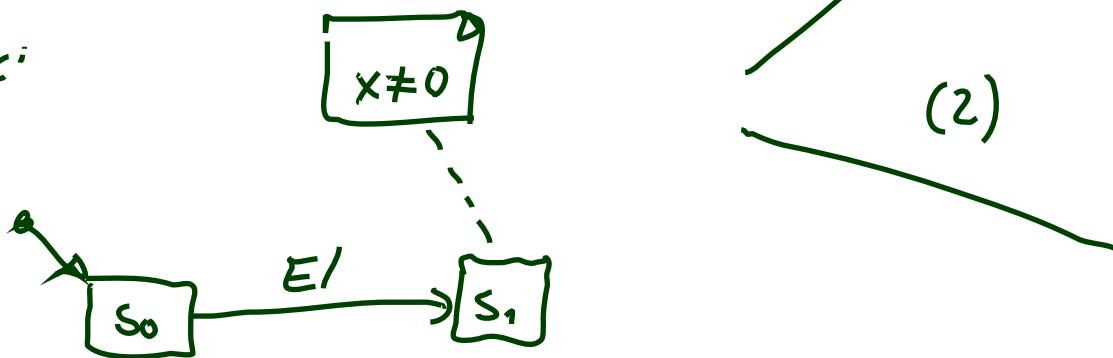
- In **UML-as-blueprint mode**, if \mathcal{SM} doesn't exist yet, then $\mathcal{M} = (\mathcal{CD}, \emptyset, \mathcal{OD})$ is typically asking the developer to provide \mathcal{SM} such that $\mathcal{M}' = (\mathcal{CD}, \mathcal{SM}, \mathcal{OD})$ is consistent.
If the developer makes a mistake, then \mathcal{M}' is inconsistent.
- Not common:** if \mathcal{SM} is given, then constraints are also considered when choosing transitions in the RTC-algorithm. In other words: even in presence of mistakes, the \mathcal{SM} never move to inconsistent configurations.

Pragmatics: Example

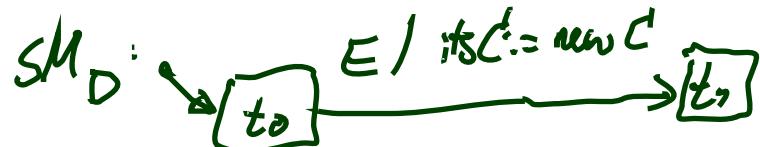


M is not consistent ("broken") because there is a comp. path. leading to a (σ, ε) s.t. $\sigma \neq \text{Inv}(\mu)$

sk_C:



"dear developer, I want
- states S_0, S_1
- E must more use
from S_0 to S_1 ,
- in S_1 , x must not
be 0
DO THAT!"

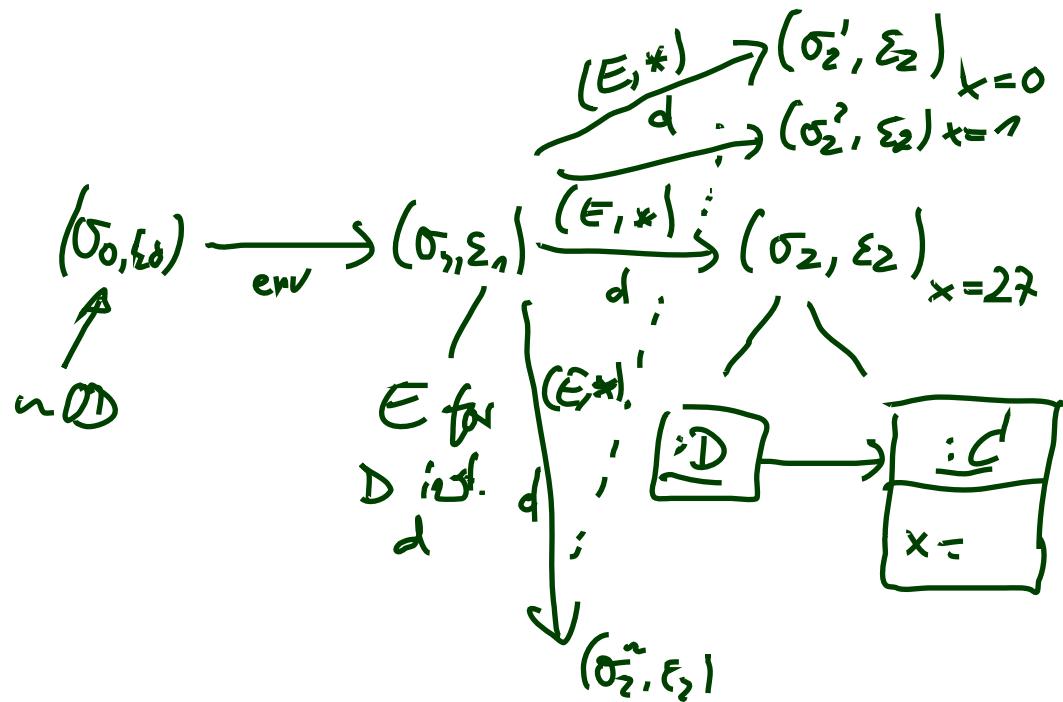


IN EACH SYSTEM STATE σ ,

FOR EACH ALIVE OBJECT $v \in \text{dom}(\sigma), v \in \text{O}(C)$

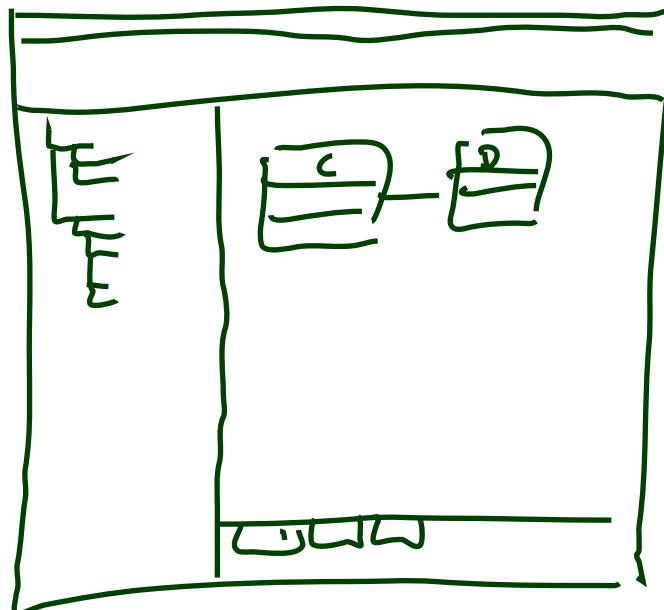
EACH OF v 'S ATTRIBUTES HAS
A (DEFINITE) VALUE !

$$\forall v \in \text{attr}(C) \circ \sigma(v)(v) \in \mathcal{D}(\text{typ}(v))$$



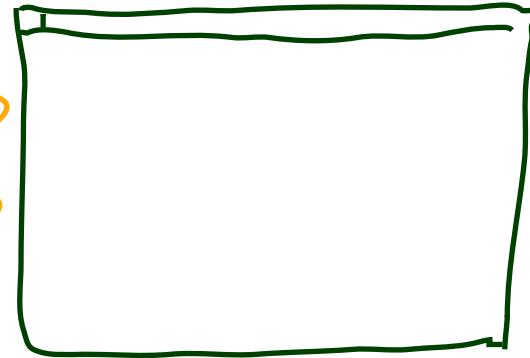
Rhapsody Demo II

Rhapsody

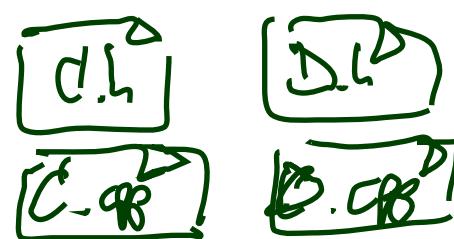


E

D just stopped
from S₀ to S_n
by trans. t



generate



Main Def lang - CB

build / make
COMPILER



run