

Contents & Goals

- Last Lecture:
- Live Sequence Charts Semantics
- This Lecture:
- Educational Objectives: Capabilities for following tasks/questions.
    - What's the UML Substitution Principle?
    - What is late/early binding?
    - What is the subset, what the uplink semantics of inheritance?
    - What's the effect of inheritance on LSCs, State Machines, System States?
  - Content:
    - Inheritance in UML: concrete syntax
    - UML Substitution Principle — desired semantics
    - Two approaches to obtain desired semantics

Motivations for Generalisation

- Re-use,
- Sharing,
- Avoiding Redundancy,
- Modularisation,
- Separation of Concerns,
- Abstraction,
- Extensibility,
- ...

→ See textbooks on object-oriented analysis, development, programming.

Inheritance: Syntax

UML: Inheritance

Example: In the system we always have a sensor. In the system we have a temperature sensor. In the system we have a temperature sensor 2.

UML: Inheritance

UML: Inheritance

Abstract Syntax

Recall: a signature (with signals) is a tuple  $\mathcal{S} = (\mathcal{S}, \mathcal{V}, \text{attr}, \mathcal{E})$ .

Now (finally): extend to

$$\mathcal{S} = (\mathcal{S}, \mathcal{V}, \text{attr}, \mathcal{E}, F, \text{meth}, \mathcal{R})$$

where  $F/\text{meth}$  are methods, analogously to attributes and  $\mathcal{R}$  is a generalisation relation such that  $C \triangleleft^+ C'$  for no  $C \in \mathcal{C}$  ("acyclic").

UML: Inheritance

UML: Inheritance

## Reflexive, Transitive Closure of Generalisation

**Definition.** Given classes  $C_0, C_1, D \in \mathcal{C}$ , we say  $D$  inherits from  $C_0$  via  $C_1$  if and only if there are  $C_0^1 \dots C_0^m, C_1^1 \dots C_1^m \in \mathcal{C}$  such that

$$C_0 \triangleleft C_1^1 \triangleleft \dots \triangleleft C_1^m \triangleleft C_1 \triangleleft C_1^1 \triangleleft \dots \triangleleft C_1^m \triangleleft D.$$

We use ' $\triangleleft^*$ ' to denote the reflexive, transitive closure of ' $\triangleleft$ '.

In the following, we assume

- that all attribute (method) names are of the form  $C::n, C \in \mathcal{C} \cup \mathcal{E}, (C::f, C \in \mathcal{F}),$
- that we have  $C::x \in \text{attr}(C)$  resp.  $C::f \in \text{meth}(C)$  if and only if  $x (f)$  appears in an attribute (method) compartment of  $C$  in a class diagram.



## Extend Typing Rules

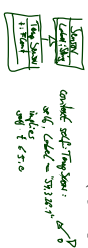
## Well-Typedness with Inheritance

**Recall:** With extension for visibility we obtained

$$\begin{aligned} v(\langle \emptyset \rangle) &: \tau_C \rightarrow \tau(\emptyset) & (v: \tau \xi, \text{expr}_0, P_0) \in \text{attr}(C), w: \tau_C \\ v(\text{expr}_1(w)) &: \tau_{C_1} \rightarrow \tau(\emptyset) & (v: \tau \xi, \text{expr}_0, P_0) \in \text{attr}(C_1), \\ & \text{expr}_1(w): \tau_{C_1}, w: \tau_{C_1}, \text{ and } C_1 = C_2 \text{ or } \xi = + \end{aligned}$$

**Now:**

$$\begin{aligned} v(\langle \emptyset \rangle) &: \tau_C \rightarrow \tau(\emptyset) & (v: \tau \xi, \text{expr}_0, P_0) \in \text{attr}(C), \\ & w: \tau_{C_1}, \tau_C \leq \tau_{C_1} \\ v(\text{expr}_1(w)) &: \tau_{C_1} \rightarrow \tau(\emptyset) & (v: \tau \xi, \text{expr}_0, P_0) \in \text{attr}(C_2), \\ & \text{expr}_1(w): \tau_{C_2}, w: \tau_{C_2}, \text{ and } C_1 = C_2 \text{ or } \xi = + \text{ or } (C_2 \leq C_1 \text{ and } \xi = \#) \end{aligned}$$



## Inheritance: System States

## System States

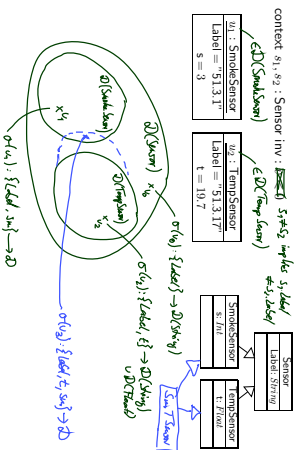
**Wanted:** a formal representation of "if  $C \leq D$  then  $D$  is a  $C$ ", i.e.,

- (i)  $D$  has the same attributes as  $C$ , and
- (ii)  $\mathcal{D}$  objects (identities) can be used in any context where  $\mathcal{C}$  objects can be used.

We'll discuss two approaches to semantics:

- Domain-inclusion Semantics (more theoretical)
- Umlink Semantics (more technical)

## Domain Inclusion Semantics

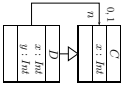


- Let  $\mathcal{D} = (\mathcal{C}, V, \text{attr}, \delta, F, \text{meth}, \triangleleft)$  be a signature.
- Now a structure  $\mathcal{D}$
- [as before] maps types, classes, associations to domains,
  - [or completeness] methods to transformers,
  - [as before] identities of instances of classes not (transitively) related by generalisation are disjoint,
  - [changed] the identities of a super-class comprise all identities of sub-classes, i.e.
- $$\forall C \in \mathcal{C} : \mathcal{D}(C) \supseteq \bigcup_{C \leq D} \mathcal{D}(D).$$

Note: the old setting coincides with the special case  $\triangleleft = \emptyset$ .

- Now: a system state of  $\mathcal{D}$  wrt.  $\mathcal{D}$  is a type-consistent mapping
- $$\sigma : \mathcal{D}(C) \mapsto (V \mapsto ((\mathcal{D}(C) \cup \mathcal{D}(C_0)) \cup \mathcal{D}(C_1)))$$
- that is, for all  $u \in \text{dom}(\sigma) \cap \mathcal{D}(C)$ ,
- [as before]  $\sigma(u)(v) \in \mathcal{D}(C)$  if  $v : \tau \in \mathcal{D}$  or  $\tau \in \{C_0, C_1\}$ ,
  - [changed]  $\text{dom}(\sigma(u)) = \bigcup_{C_0 \leq C_1} \text{attr}(C_0)$ .

Note: the old setting still coincides with the special case  $\triangleleft = \emptyset$ .



Satisfying OCL Constraints (Domain Inclusion)

- Let  $\mathcal{M} = (\mathcal{D}, \sigma, \mathcal{D}, \mathcal{M}, \mathcal{J})$  be a UML model, and  $\mathcal{D}$  a structure.
- We (continue to) say  $\mathcal{M} \models \text{expr}$  for context  $C : \text{inv} : \text{expr} \in \text{Inv}(\mathcal{M})$  iff
 
$$\forall \pi = (\sigma_1, \sigma_2) \in \pi \quad \forall v \in \mathbb{N} \quad \forall u \in \text{dom}(\sigma_1) \cap \mathcal{D}(C) : \llbracket \text{expr} \rrbracket[(\sigma_1, \{\sigma_2\} \mapsto u)] = 1.$$
- $\mathcal{M}$  is (still) consistent if and only if it satisfies all constraints in  $\text{Inv}(\mathcal{M})$ .
- Example:

Transformers (Domain Inclusion)

- Transformers also remain the same, e.g. [VL12, p. 18]
 
$$\text{update}(\text{expr}_1, v, \text{expr}_2) : (\sigma, \varepsilon) \mapsto (\sigma', \varepsilon)$$
- with
 
$$\sigma' = \sigma \cup \{v \mapsto \sigma(u)(v) \mapsto \llbracket \text{expr}_2 \rrbracket[\sigma]\}$$
- where  $u = \llbracket \text{expr}_1 \rrbracket[\sigma]$ .

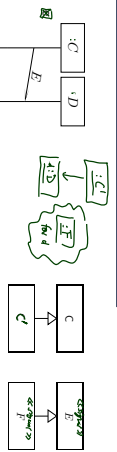
Inheritance and State Machines: Triggers

- Wanted: triggers shall also be sensitive for inherited events, sub-class shall execute super-class' state-machine (unless overridden).
- $$(\sigma, \varepsilon) \xrightarrow{\text{trans, state}} (\sigma', \varepsilon') \text{ if}$$
  - $\exists u \in \text{dom}(\sigma) \cap \mathcal{D}(C) \exists \text{trig} \in \mathcal{D}(C) : \text{trig} \in \text{mach}(C, u)$
  - $u$  is stable and in state machine-state  $s$ , i.e.  $\sigma(u)(\text{state}) = 1$  and  $\sigma(u)(s) = s$
  - a transition is enabled, i.e.
 
$$\exists (s, F, \text{expr}, \text{act}, s') \in \text{SM}(C) : F = E \wedge \llbracket \text{expr} \rrbracket[\sigma] = 1$$
 where  $\sigma = \sigma \cup \{u, \text{promise}_F \mapsto u\}$ ,
  - and
 
$$(\sigma', \varepsilon') \text{ result from applying } \text{act} \text{ to } (\sigma, \varepsilon) \text{ and removing } u \text{ from the offer, i.e.}$$

$$(\sigma', \varepsilon') = (\sigma' \cup \{u, \text{act}\} \mapsto s', \text{act}(\varepsilon) \cup \{u\}) \setminus \{u\}$$
- where  $\delta$  depends:
- If  $u$  becomes stable in  $\sigma'$ , then  $\delta = 1$ . It does become stable if and only if there is no transition without trigger enabled for  $u$  in  $(\sigma', \varepsilon')$ .
  - Otherwise  $\delta = 0$ .



## Domain Inclusion and Interactions



- Similar to satisfaction of OCL expressions above:
- An instance line stands for all instances of  $C$  (exact or inheriting)
- Satisfaction of event observation has to take inheritance into account, too, so we have to fix, e.g.
 
$$\sigma, \text{vars}, \text{Inst} \models_{\beta} E_{\sigma, \beta}$$
 if and only if
 
$$\beta(x) \text{ sends an } F\text{-event to } \beta(y) \text{ where } E \preceq F.$$
- $C$ -instance line also binds to  $C'$ -objects.

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## Uplink Semantics

- **Idea:**
- Continue with the existing definition of **structure**, i.e. disjoint domains for identities.
- Have an **implicit association** from the child to each parent part (similar to the implicit attribute for stability).
 
$$\begin{array}{c} C \\ \text{---} \\ \text{---} \\ D \end{array}$$
- Apply (a different) pre-processing to make appropriate use of that association, e.g. rewrite  $C++$ 

$$x = 0;$$
 in  $D$  to
 
$$\text{uplink}_C \rightarrow x = 0;$$

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## Uplink Semantics

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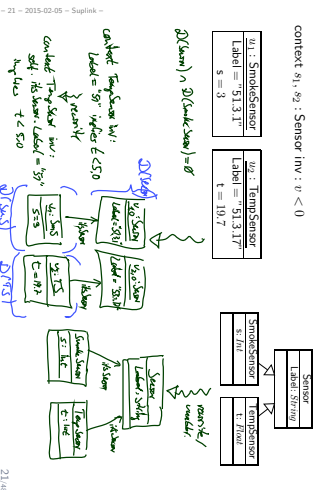
## Pre-Processing for the Uplink Semantics

- For each pair  $C \triangleleft D$ , extend  $D$  by a (fresh) association
 
$$\text{uplink}_C : C \text{ with } \mu = [1..1], \xi = +$$
- (**Exercise:** public necessary?)
- Given expression  $e$  (or  $f$ ) in the **context** of class  $D$ ,
- let  $C$  be the **smallest** class w.r.t. " $\preceq$ " such that
  - $C \preceq D$ , and
  - $C::\mu \in \text{atr}(D)$
- then there exists (by definition)  $C \triangleleft C_1 \triangleleft \dots \triangleleft C_n \triangleleft D$ ,
- **normalise**  $e$  to (= replace by)
 
$$\text{uplink}_{C_n} \rightarrow \dots \rightarrow \text{uplink}_{C_1} C::\mu$$
- If no (unique) smallest class exists, the modal is considered **not well-formed**; the expression is ambiguous.

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## Uplink Semantics: Idea



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## Uplink Structure, System State, Typing

- Definition of structure remains **unchanged**.
- Definition of system state remains **unchanged**.
- Typing and transformers remain **unchanged** — the preprocessing has put everything in shape.

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## Satisfying OCL Constraints (Uplink)

- Let  $\mathcal{M} = (\mathcal{D}, \theta, \mathcal{M}, \mathcal{J})$  be a UML model and  $\mathcal{D}$  a structure.
- We (continue to) say

$$\mathcal{M} \models \text{expr}$$

for

$$\text{context } C \text{ inv : expr}_0 \in \text{Inv}(\mathcal{M})$$

= expr

if and only if

$$\begin{aligned} \forall \pi = (\sigma_1), \text{env} \in \llbracket \mathcal{M} \rrbracket \\ \forall i \in \mathbb{N} \\ \forall u_i \in \text{dom}(\sigma_i) \cap \mathcal{D}(C) : \\ \llbracket \text{expr}_0 \rrbracket_{(\sigma_1, \{\text{sel} \mapsto u_i\})} = 1. \end{aligned}$$

- $\mathcal{M}$  is (still) consistent if and only if it satisfies all constraints in  $\text{Inv}(\mathcal{M})$ .

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## Transformers (Uplink)

- What has to change is the create transformer:
- Assume,  $C$ 's inheritance relations are as follows.

$$C_{1,1} \triangleleft \dots \triangleleft C_{1,n_1} \triangleleft C_i$$

$$\dots$$

$$C_{n_1,1} \triangleleft \dots \triangleleft C_{n_1,n_1} \triangleleft C_i$$

- Then, we have to

- create one fresh object for each part, e.g.

$$u_{1,1}, \dots, u_{1,n_1}, \dots, u_{n_1,1}, \dots, u_{n_1,n_1}$$

- set up the uplinks recursively, e.g.

$$\sigma(u_{1,1}) (\text{uplink}_{C_{1,1}}) = u_{1,1}$$

- And, if we had constructors, be careful with their order.

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## Cast-Transformers

- $C$ :
- $c$ :
- $D$ :
- $d$ :
- Identify upcast (C++):
- $C^* \text{ cp} = \&d$ :
- Identify downcast (C++):
- $D^* \text{ dp} = (D^*) \text{ cp}$ :
- Value upcast (C++):
- $*c = *d$ :

// assign address of 'd' to pointer 'cp'

// assign address of 'd\*' to pointer 'dp'

// copy attribute values of 'd' into 'c', or

// more precisely, the values of the C-part of 'd'

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## Cases in Domain Inclusion and Uplink Semantics

	Domain Inclusion	Uplink
$C^* \text{ cp} = \&d$	easy: immediately compatible (in underlying system state) because $\&d$ yields an identity from $\mathcal{D}(D) \subseteq \mathcal{D}(C)$ .	easy: By pre-processing, $C^* \text{ cp} = \text{duplink}_C$ .
$D^* \text{ dp} = (D^*) \text{ cp}$	easy: the value of $\text{cp}$ is in $\mathcal{D}(D) \cap \mathcal{D}(C)$ because the pointer to object is a $D$ . Otherwise, error condition.	difficult: we need the identity of the $D$ whose C-slice is denoted by $\text{cp}$ (see next slide).
$c = d$	bit difficult: set (for all $C \leq D$ ) $(C) \langle \cdot, \cdot \rangle : \tau_C \times \Sigma \rightarrow \Sigma_{\text{loc}(C)}$ $(u_1, \sigma) \mapsto \sigma(u_1) \uparrow_{\text{loc}(C)}$ . Note: $\sigma' = \sigma \uparrow_{\text{loc}(C)} \mapsto \sigma'(u_2)$ is not type-compatible!	easy: By pre-processing, $c = \&(d \uparrow_{\text{uplink}_C})$ .

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## Domain Inclusion vs. Uplink Semantics

- Recall (C++):  $D$ :  $d$ ;  $C^* \text{ cp} = \&d$ ;  $D^* \text{ dp} = (D^*) \text{ cp}$ .
- Problem: we need the identity of the  $D$  whose  $C$ -slice is denoted by  $\text{cp}$ .
- One technical solution:
- Give up disjointness of domains for one additional type comprising all identities, i.e. have

$$\text{all} \in \mathcal{D} \quad \mathcal{D}(\text{all}) = \bigcup_{C \in \mathcal{C}} \mathcal{D}(C)$$

- In each  $\leq$ -minimal class have associations "mostspec" pointing to most specialised slices, plus information of which type that slice is.
- Then downcast means, depending on the mostspec type (only finitely many possibilities), going down and then up as necessary, e.g.

```

satech(mostspec, type) {
  case C :
    dp = cp -> mostspec -> uplink_D_n -> ... -> uplink_C_n -> uplink_D;
  ...
}

```

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## Domain Inclusion vs. Uplink: Differences

- **Note:** The uplink semantics views inheritance as an abbreviation:
    - We only need to touch transformers (create) — and if we had constructors, we didn't even need that (we could encode the recursive construction of the upper slices by a transformation of the existing constructors).
  - **So:**
    - Inheritance **doesn't add** expressive power.
    - And it also **doesn't improve** conciseness **soo dramatically**.
- As long as we're "early binding", that is...

## Domain Inclusion vs. Uplink: Motivations

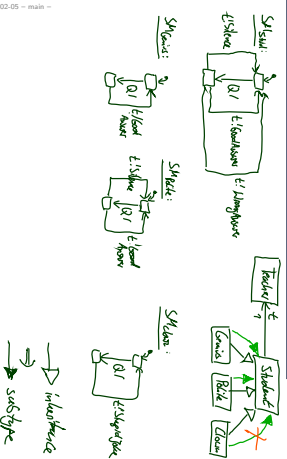
- **Exercise:**

What's the point of

  - having the **tedious** adjustments of the theory if it can be approached **technically**?
  - having the **tedious** technical **pre-processing** if it can be approached **cleanly** in the theory?

*More Interesting: Behaviour*

## Example: Behaviour of Kinds of Students



## Desired Semantics of Specialisation: Subtyping

There is a classical description of what one expects from **sub-types**, which in the OO domain is closely related to inheritance:

The principle of type substitutability [Liskov, 1988; Liskov and Wing, 1994; (Liskov Substitution Principle (LSP))

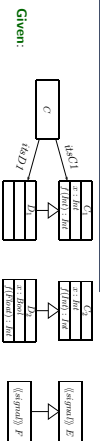
"If for each object  $o_1$  of type  $S$  there is an object  $o_2$  of type  $T$  such that for all programs  $P$  defined in terms of  $T$ , the behavior of  $P$  is unchanged when  $o_1$  is substituted for  $o_2$  then  $S$  is a subtype of  $T$ ."

In other words: [Fischer and Wehrheim, 2000]

"An instance of the **sub-type** shall be **usable** whenever an instance of the **super-type** was expected, **without a client being able to tell the difference**."

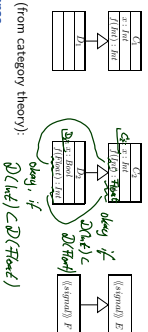
So, what's "usable"? Who's a "client"? And what's a "difference"?

"...shall be usable..." for UML



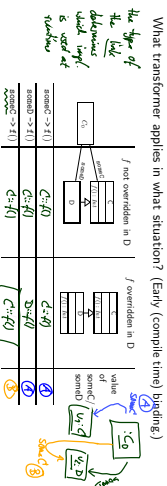
- Given:**
- $x > 0$  also well-typed for  $D_1$ .
  - assignment  $fsC1 := fsD1$  being well-typed
  - $fsC1, x = 0, fsC1, f(0), fsC1, F$  being well-typed (and doing the right thing).

- Wanted:**
- Simply define it as being well-typed, adjust system state definition to do the right thing.



- Notions (from category theory):**
- invariance,
  - covariance,
  - contravariance.
- We could call, e.g. a method, **sub-type preserving**, if and only if it
- accepts **more general** types as input (contravariant),
  - provides a **more specialised** type as output (covariant).
- This is a notion used by many programming languages — and easily type-checked.

### Late Binding



What one could want is something different: (Late binding.)

type of	early	late
method	→ f() (1)	→ f() (2)
assignment	→ f() (1)	→ f() (2)
expression	→ f() (1)	→ f() (2)

### Late Binding in the Standard and in Prog. Langs.

- In the standard, Section 11.3.10, "CallOperationAction":  
 "Semantic Variation Points  
 The mechanism for determining the method to be invoked as a result of a call operation is unspecified." [OMG, 2007b, 247]
- In C++:  
 • methods are by default "early" compile-time binding.  
 • can be declared to be "late binding" by keyword "virtual".  
 • the declaration applies to all inheriting classes.
- In Java:  
 • methods are "late binding".  
 • there are patterns to imitate the effect of "early binding".

**Exercise:** What could be the rationale of the designers of C++?  
**Note:** late binding typically applies only to **methods**, **not** to **attributes**.  
 (But: getter/setter methods have been invented recently.)

Back to the Main Track: "...tell the difference..." for UML

- ...we're **done** (if we realise it correctly in the framework)
  - Then
  - if we're calling method  $f$  of an object  $u$ ,
  - which is an instance of  $D$  with  $C \leq D$
  - via a C-link, *use the knowledge provided by C*
  - then we (by definition) only see and change the C-part.
  - We cannot tell whether  $u$  is a C or an D instance.
- So we immediately also have behavioural/dynamic subtyping.

- $C::f$  and  $D::f$  are **type compatible**, but  $D$  is **not necessarily** a sub-type of  $C$ .

• Examples: (C++)

```
int C::f(int) {
    return 0;
};
```

```
int D::f(int) {
    return 1;
};
```

vs.

```
int C::f(int) {
    return (rand() % 2);
};
```

```
int D::f(int x) {
    return (x % 2);
};
```

vs.



- In the standard, Section 7.3.6, "Operation":
  - **Semantic Variation Points**
    - [...] When operations are redefined in a specialization, rules regarding invariance, covariance, or contravariance of types and preconditions determine whether the specialized classifier is substitutable for its more general parent. Such rules equate semantic variation points with respect to redefinition of operations. [OMG, 2007a/106]
  - So, better: call a method **sub-type preserving**, if and only if it
    - (i) accepts **more input values** (contravariant),
    - (ii) on the **old values**, has **fewer behaviour** (covariant).
  - **Note:** ~~the~~ (i) is no longer a matter of simple type-checking!
  - And not necessarily the end of the story:
    - One could, e.g. want to consider execution time.
    - Or, like [Fischer and Wehrheim, 2000], relax to "fewer observable behaviour", thus admitting the sub-type to do more work on inputs.
  - **Note:** "testing" differences depends on the **granularity** of the semantics.
  - **Related:** "has a weaker pre-condition" (contravariant), "has a stronger post-condition." (covariant)

Ensuring Sub-Typing for State Machines

- In the CASE tool we consider, multiple classes in an inheritance hierarchy can have state machines.
- But the state machine of a sub-class **cannot** be drawn from scratch.
- Instead, the state machine of a sub-class can only be obtained by applying actions from a restricted set to a copy of the original one. Roughly (cf. User Guide, p. 760, for details),
  - add things into (hierarchical) states,
  - add more states,
  - attach a transition to a different target (limited).



- They **ensure**, that the sub-class is a **behavioural sub-type** of the super class. (But method implementations can still destroy that property.)
- Technically, the idea is that (by late binding) only the state machine of the most specialized classes are running.
- By knowledge of the framework, the (code for) state machines of super-classes is still accessible — but using it is hardly a good idea...

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