Contents

Lecture 11: Core State Machines I
   Slide 11, Example: FIFO Queue ................................................................. 1

Lecture 13, Core State Machines III
   Slide 12, Example: Continue ................................................................. 1

Lecture 17, Live Sequence Charts I
   Slide 14, LSC Body: Abstract Syntax ..................................................... 1

Lecture 11: Core State Machines I

Slide 11, Example: FIFO Queue
   • the definition of ready should read as follows:

   \[(\varepsilon, u) \mapsto \begin{cases} \{(u, E)\} & \text{if } \varepsilon = (u, E) . \varepsilon' \text{ for some (possibly empty) } \varepsilon' \in Eth \\ \emptyset & \text{otherwise} \end{cases} \]

Lecture 13, Core State Machines III

Slide 12, Example: Continue
   • According to Rule (i), discarded signal instances do contribute to cons, so the two rightmost transitions shown on the slide should be labelled with \((\{u\}, \emptyset)\) and \((\{v\}, \emptyset)\), respectively, (and not \((\emptyset, \emptyset)\) both).

Lecture 17, Live Sequence Charts I

Slide 14, LSC Body: Abstract Syntax
   • The abstract syntax representation of the local invariant is \((l_{1,1}, \circ, v = 0, \bullet, l_{1,2})\).
   • Note: the slide (with the error) is embedded into Lecture 18, slide 4.