

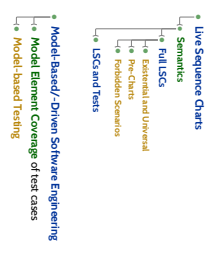
Software Design, Modelling and Analysis in UML

Lecture 21: Model-based Software Development

2017-02-06

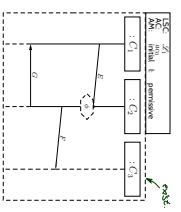
Prof. Dr. Andreas Podelski, Dr. Bernd Westphal
 Albert-Ludwigs-Universität Freiburg, Germany

Content



Full LSCs

- A full LSC $\mathcal{L} = ((U, \Sigma, \sim), I, M, G, Cond, Lacthv, \Theta, \mathcal{J})$ consists of
- body $(U, \Sigma, \sim), I, M, G, Cond, Lacthv, \Theta$;
 - activation condition $act \in Expr_{\mathcal{L}}$;
 - strictness flag $strict$ (if false, \mathcal{L} is called **permissive**)
 - activation mode $om \in \{initial, invariant\}$;
 - chart mode **existential** ($\Theta_{\mathcal{L}} = call$) or **universal** ($\Theta_{\mathcal{L}} = hot$)



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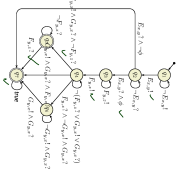
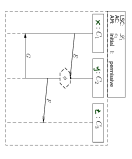
A set of words $W \subseteq (Expr_{\mathcal{L}} \rightarrow B)^*$ is accepted by \mathcal{L} if and only if

$\Theta_{\mathcal{L}}$	$om = initial$	$om = invariant$
cold	$\exists J \exists W \in W \bullet \exists \rho \models \rho \models act \wedge \neg \text{valid}(C_1)$ $\wedge \forall \rho \models \rho \models \text{valid}(C_1) \wedge \exists \rho' \models \rho' \models \text{valid}(C_2)$	$\exists J \exists W \in W \exists E \in R \bullet \exists \rho \models \rho \models act \wedge \text{valid}(C_1)$ $\wedge \forall \rho \models \rho \models \text{valid}(C_1) \wedge \exists \rho' \models \rho' \models E \in C_1(R, \rho')$
hot	$\forall J \forall W \in W \bullet \exists \rho \models \rho \models act \wedge \text{valid}(C_1)$ $\implies \forall \rho' \models \rho' \models \text{valid}(C_1) \wedge \forall I \in C_1(R, \rho')$	$\forall J \forall W \in W \forall E \in R \bullet \exists \rho \models \rho \models act \wedge \text{valid}(C_1)$ $\implies \forall \rho' \models \rho' \models \text{valid}(C_1) \wedge \forall I \in C_1(R, \rho')$

where C_1 is the minimal (w.r.t. \subseteq) call.

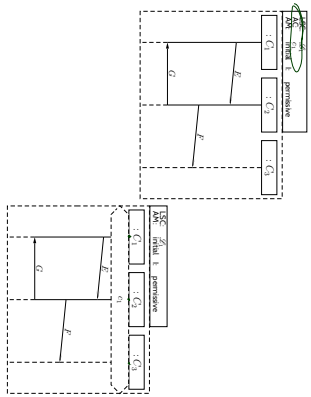
Live Sequence Charts — Full LSC Semantics

Full LSC Semantics: Example

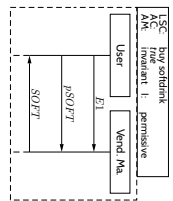


$$W^1 = \left\{ (s_1, s_2) \xrightarrow{w} (s_1, s_2, s_3) \xrightarrow{w} (s_1, s_2, s_3, s_4) \xrightarrow{w} (s_1, s_2, s_3, s_4, s_5) \xrightarrow{w} (s_1, s_2, s_3, s_4, s_5, s_6) \dots \right\}$$

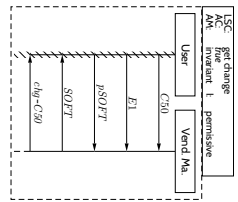
W^1 is accepted



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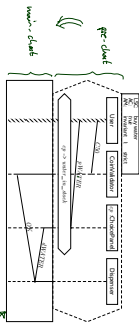


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Live Sequence Charts — Precharts

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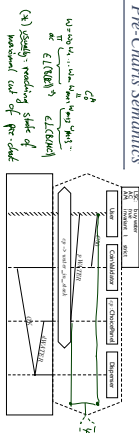
Pre-Charts



- A full LSC $\mathcal{Z} = (PC, MC, \text{env}, \text{am}, \Theta_{\mathcal{Z}})$ actually consist of
 - pre-chart $PC = (U, P, S, P, \sim, Q), Z, P, S, \text{Map}, \text{Cond}, \text{Lodiv}, \Theta_{\mathcal{Z}}$ (possibly empty).
 - main-chart $MC = (U, M, S, M, \sim, Q), Z, M, S, \text{Map}, \text{Cond}, \text{Lodiv}, \Theta_{\mathcal{Z}}$ (non-empty).
- activation condition $\text{env} : \text{Env} \in \text{Env} \mathcal{Z}$.
- strictest flag strict (otherwise called permissiv)
- activation mode $\text{am} \in \{\text{init}, \text{invariant}\}$.
- chartmode existential ($\Theta_{\mathcal{Z}} = \text{exst}$) or universal ($\Theta_{\mathcal{Z}} = \text{univ}$)

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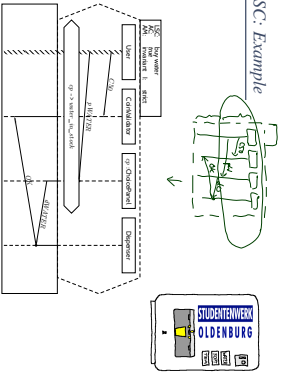
Pre-Charts Semantics



$\Theta_{\mathcal{Z}}$	$\text{env} = \text{init}$	$\text{env} = \text{invariant}$
$\Theta = \text{cold}$	$\exists E \exists w \in W \exists m \in M \bullet$ $\bigwedge m' \in M \bullet \exists s \in S \bullet \exists q \in Q \bullet$ $\bigwedge m'' \in M \bullet \exists s' \in S \bullet \exists q' \in Q \bullet$ $\bigwedge m''' \in M \bullet \exists s'' \in S \bullet \exists q'' \in Q \bullet$ $\bigwedge m'''' \in M \bullet \exists s''' \in S \bullet \exists q''' \in Q \bullet$	$\exists E \exists w \in W \exists E \exists m \in M \bullet$ $\bigwedge m' \in M \bullet \exists s \in S \bullet \exists q \in Q \bullet$ $\bigwedge m'' \in M \bullet \exists s' \in S \bullet \exists q' \in Q \bullet$ $\bigwedge m''' \in M \bullet \exists s'' \in S \bullet \exists q'' \in Q \bullet$ $\bigwedge m'''' \in M \bullet \exists s''' \in S \bullet \exists q''' \in Q \bullet$
$\Theta = \text{hot}$	$\exists E \exists w \in W \exists m \in M \bullet$ $\bigwedge m' \in M \bullet \exists s \in S \bullet \exists q \in Q \bullet$ $\bigwedge m'' \in M \bullet \exists s' \in S \bullet \exists q' \in Q \bullet$ $\bigwedge m''' \in M \bullet \exists s'' \in S \bullet \exists q'' \in Q \bullet$ $\bigwedge m'''' \in M \bullet \exists s''' \in S \bullet \exists q''' \in Q \bullet$	$\exists E \exists w \in W \exists E \exists m \in M \bullet$ $\bigwedge m' \in M \bullet \exists s \in S \bullet \exists q \in Q \bullet$ $\bigwedge m'' \in M \bullet \exists s' \in S \bullet \exists q' \in Q \bullet$ $\bigwedge m''' \in M \bullet \exists s'' \in S \bullet \exists q'' \in Q \bullet$ $\bigwedge m'''' \in M \bullet \exists s''' \in S \bullet \exists q''' \in Q \bullet$

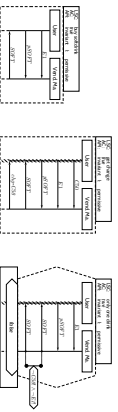
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Universal LSC: Example



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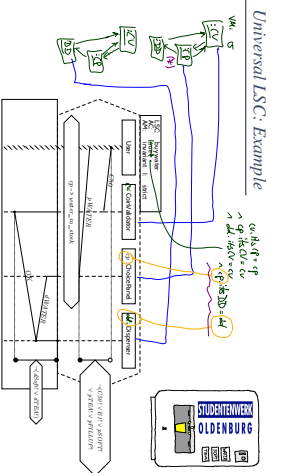
Note: Sequence Diagrams and (Acceptance) Test



- Existential LSC's may hint at test-cases for the acceptance test!
(- as well as (positive) scenarios in general, like use-cases)
- Universal LSCs (and negative/anti-scenarios) in general need **exhaustive analysis!**
(because they require that the software **never ever** exhibits the unwanted behaviour)

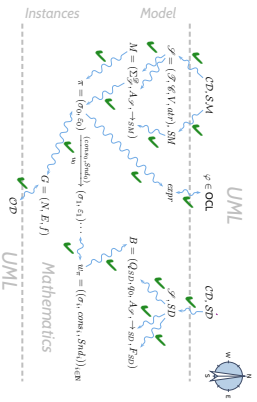
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Universal LSC: Example



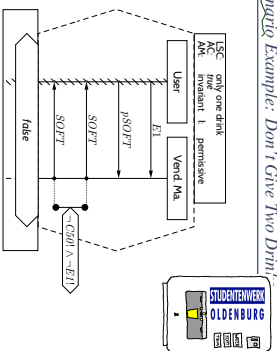
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Course Map



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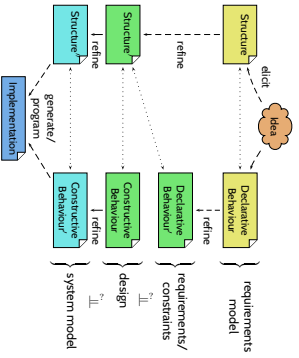
Fehlbedien Szenario Example: Don't Give Two Drinks!



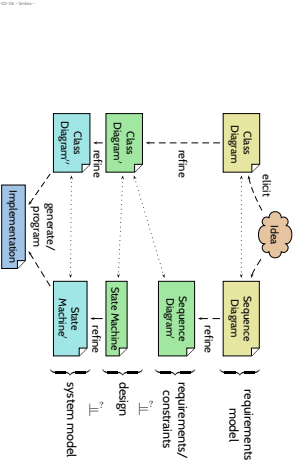
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Model-Based-Driven Software Engineering

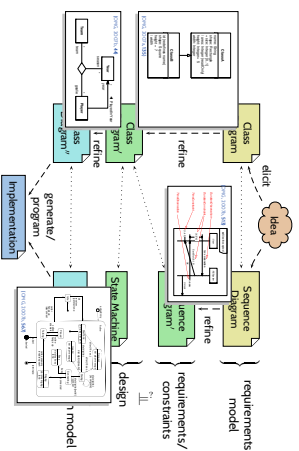
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Model-Based Testing

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Recall: Test Case

Definition. A test case T is a pair (h, S_{out}) consisting of

- a description h of sets of finite input sequences,
- a description S_{out} of expected outcomes,
- and an interpretation $[\]$ of these descriptions.

A test execution τ is $(\tau^0, \dots, \tau^i, \dots, \tau^k) \in H$ for some $i \in \mathbb{N}_0$, is called

- successful (or **passed**) if τ^i discovered an error, i.e. if $\tau \notin [S_{out}]$ (alternative test item S failed to pass test: confining "vertical")
- unsuccessful (or **rejected**) if τ did not discover an error (alternative test item S passed test: delay "vertical")

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Class-Box Testing: Coverage

- Coverage is a property of test cases and test suites.
- Execution τ of test case T achieves $p\%$ statement coverage f and only if $p = \frac{|\text{Stmt}_f \cap \text{exec}(\tau)|}{|\text{Stmt}_f|} \cdot 100$ (if $|\text{Stmt}_f| \neq 0$).
- Test case T achieves $p\%$ statement coverage f and only if $p = \frac{\min_{\tau \in \text{exec}(T)} |\text{Stmt}_f \cap \text{exec}(\tau)|}{|\text{Stmt}_f|} \cdot 100$ (if $|\text{Stmt}_f| \neq 0$).
- Execution τ of T achieves $p\%$ branch coverage f and only if $p = \frac{\min_{\tau \in \text{exec}(T)} |\text{Branch}_f \cap \text{exec}(\tau)|}{|\text{Branch}_f|} \cdot 100$ (if $|\text{Branch}_f| \neq 0$).
- Test case T achieves $p\%$ branch coverage f and only if $p = \frac{\min_{\tau \in \text{exec}(T)} |\text{Branch}_f \cap \text{exec}(\tau)|}{|\text{Branch}_f|} \cdot 100$ (if $|\text{Branch}_f| \neq 0$).
- Define $p = 100$ for empty program.
- Statement/branch coverage conceptually extends to test suite $T = \{T_1, \dots, T_n\}$, e.g. given executions τ_1, \dots, τ_n , T achieves $p = \frac{|\bigcup_{i=1}^n \text{Stmt}_f \cap \text{exec}(\tau_i)|}{|\text{Stmt}_f|} \cdot 100$ (if $|\text{Stmt}_f| \neq 0$), statement coverage.

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Coverage Example

```

int f(int x, int y, int z)
{
    if (x > 100 && y > 10)
        z = z + 2;
    else
        z = z + 1;
    if (z > 500 && y >= 30)
        return z;
}
    
```



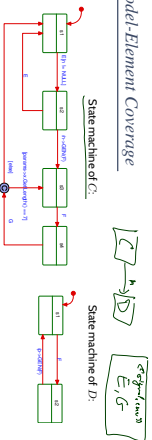
Requirement: [true] / [true] (normal/normal) i.e. S01 = $\Sigma^* \cup \Sigma^*$

T_n	z_n	y_n	x_n	$z_n > 500$	$y_n >= 30$	$z_n > 500 \wedge y_n >= 30$	C_n	$C_n \neq \emptyset$	%	$100 \cdot \%$
200,1,0	✓	✓	✓	✓	✓	✓	200	200	100	100
500,0,0	✓	✓	✓	✓	✓	✓	100	75	25	25
0,0,0	✓	✓	✓	✓	✓	✓	100	100	75	75
0,20,0	✓	✓	✓	✓	✓	✓	100	100	100	100

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Model-Element Coverage



100% Element coverage of C's state machine:

- a set of test cases (e.g. Sequence Diagrams) such that
 - when conducting these test cases
 - each state of C is reached at least once,
 - each transition of C is taken at least once.
- (state coverage)
(transition coverage)
- In general: State coverage of a set of test cases
- number-of-states reached / number-of-states in state machine.

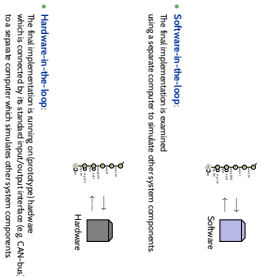
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Model-based Testing

- Given a set of test cases passing for the model
 - and an implementation of the model (maybe hand-written)
- Execute the test cases on the implementation for the final system!
- This may need an appropriate interpretation. For example, if the test case says
- send "CSO" to the Calculator.
 - rather insert a 50 Cent coin into the vending machine.
- If the vending machine does not behave according to the test.
 - then there's something wrong (wrong test conduct, wrong implementation etc.)
 - If the vending machine does behave according to the test.
 - then we know that this scenario works – not more.

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Vocabulary



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Excursion: Automatic Test Generation

References

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References

- Dobling, B. and Pearson, J. (2008). How UML is used. Communications of the ACM, 49(5):409-114.
- OMG (2007a). Unified modeling language Infrastructure, version 2.1.2. Technical Report formal/07-11-04.
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