

Software Design, Modelling and Analysis in UML

Lecture 10: Modelling Behaviour

2016-12-01

Prof. Dr. Andreas Podelski, Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

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Content

- What makes a class diagram a **good class diagram**?
 - The Elements of UML 2.0 Style Cont'd
 - Example: Game Architecture
- Purposes of **Behavioural Models**
- **Constructive Behavioural Models in UML**
- UML **State Machines**
 - Brief History
 - **Syntax**
 - **The Basic Causality Model**

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Design Guidelines for (Class) Diagram

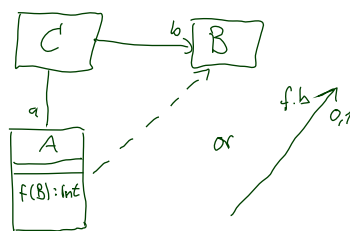
(partly following Ambler (2005))

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Class Diagram Guidelines Ambler (2005)

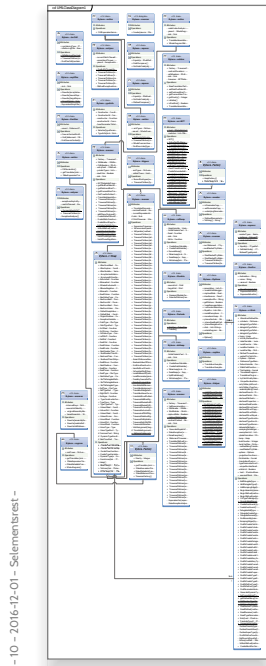
- 5.3 Relationships
 - 112. Model Relationships Horizontally
 - 115. Model a Dependency When the Relationship is Transitory
 - 117. Always Indicate the Multiplicity
 - 118. Avoid Multiplicity “*”
 - 119. Replace Relationship Lines with Attribute Types



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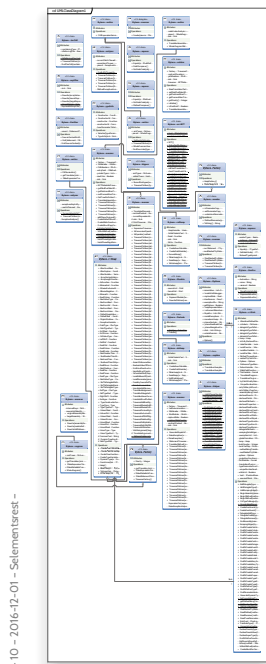
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Some Example Class Diagrams

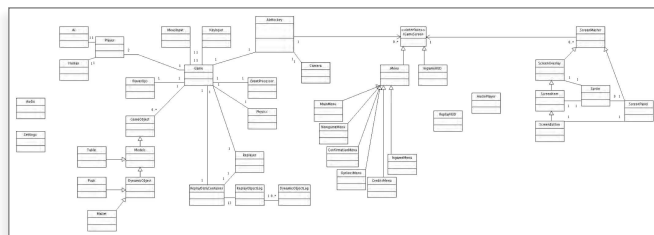


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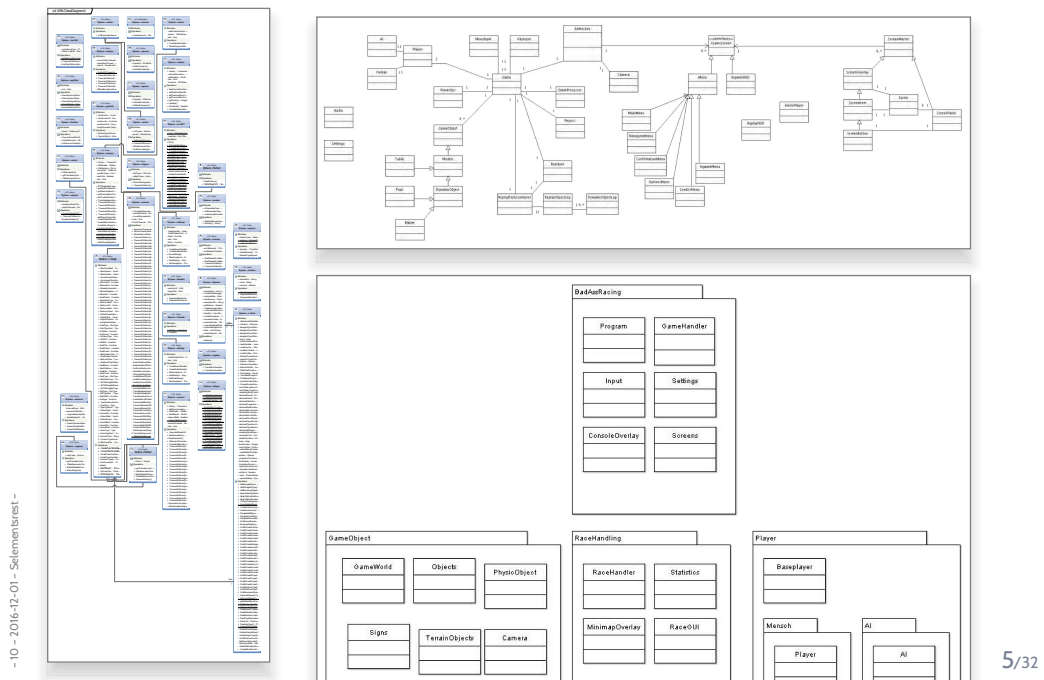
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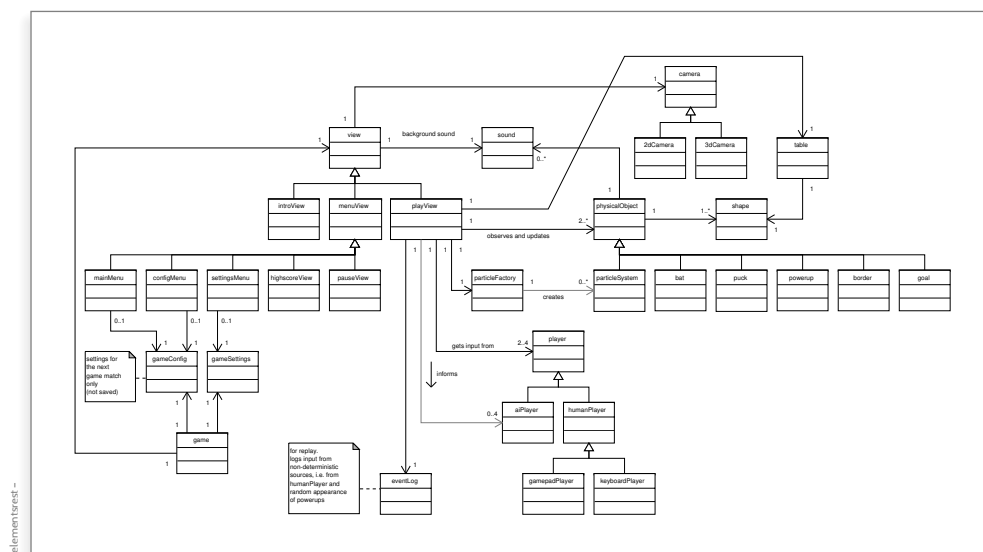
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Some Example Class Diagrams



More Example Class Diagrams



Example: Modelling Games

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Modelling Structure: Common Architectures

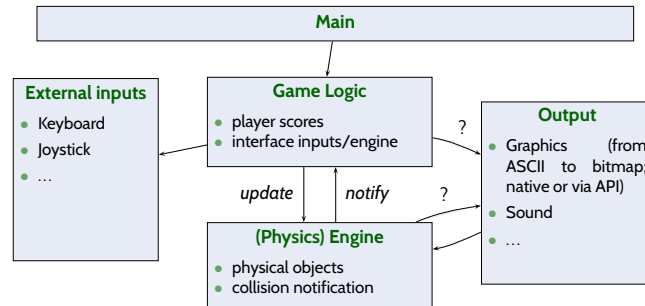
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- For games, for example:

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Modelling Structure: Common Architectures

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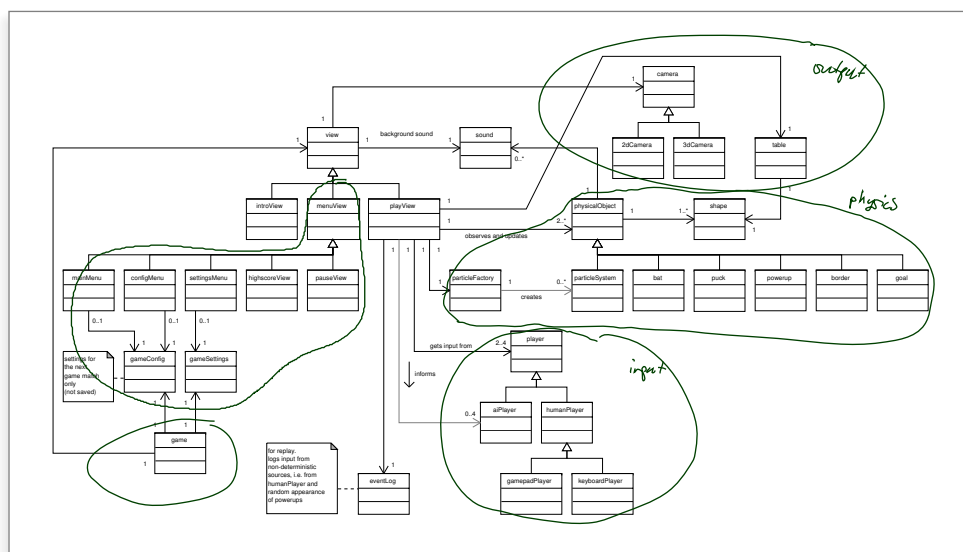


- Adept readers try to see/find/match the common architecture if they know that a model is from a particular domain.
- We can do those readers a favour by grouping/positioning things in the diagram so that seeing/finding/matching is easy.

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Example Re-Considered



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Modelling Behaviour

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Stocktaking...

Have: Means to model the **structure** of the system.

- Class diagrams graphically, concisely describe sets of system states.
- OCL expressions logically state constraints/invariants on system states.

Want: Means to model **behaviour** of the system.

- Means to describe how system states **evolve**(over time)
that is, to describe sets of **sequences**

$$\sigma_0, \sigma_1, \dots \in \Sigma^\omega$$

of **system states**.

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What Can Be Purposes of Behavioural Models?

Example: Pre-Image

(the UML model is supposed to be the blue-print for a software system).

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"After (inserting money and choosing a drink) the drink is dispensed (if in stock)."
(If the implementation insists on taking the money first, that's a fair choice.)

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- **Forbid** Behaviour.

"This sequence of getting both, a water and all money back, must not be possible." (Otherwise the software is broken.)

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Note: the latter two are trivially satisfied by doing nothing...

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Image

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- **Require** Behaviour.

"System definitely does this"

"This sequence of inserting money and requesting and getting water must be possible."

(Otherwise the software for the vending machine is completely broken.)

- **Allow** Behaviour.

"System does subset of this"

"After inserting money and choosing a drink, the drink is dispensed (if in stock)."

(If the implementation insists on taking the money first, that's a fair choice.)

- **Forbid** Behaviour.

"System never does this"

"This sequence of getting both, a water and all money back, must not be possible." (Otherwise the software is broken.)

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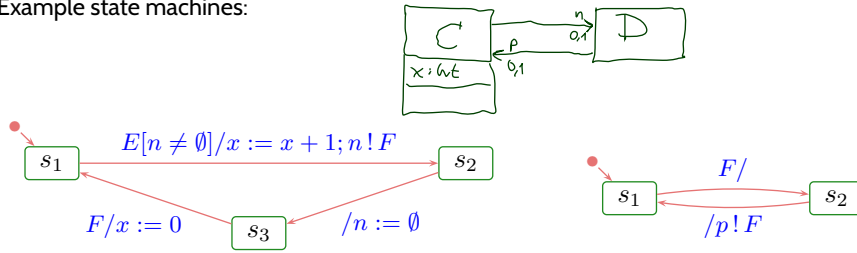
Constructive Behaviour in UML

UML provides two visual formalisms for constructive description of behaviours:

- **Activity Diagrams**
- **State-Machine Diagrams**

We (exemplary) focus on State-Machines because

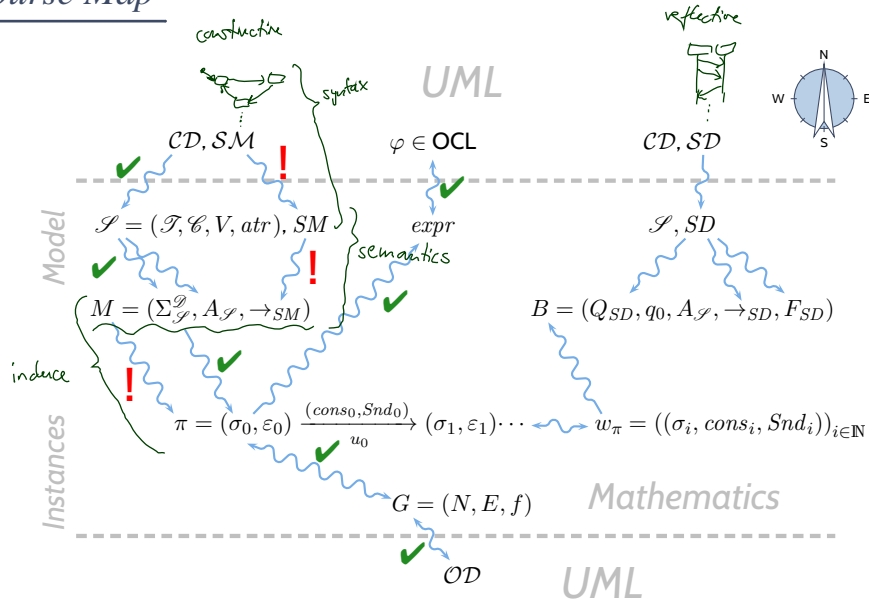
- somehow “practice proven” (in different flavours),
- prevalent in embedded systems community,
- indicated useful by [Dobing and Parsons \(2006\)](#) survey, and
- Activity Diagram’s intuition changed (between UML 1.x and 2.x) from transition-system-like to petri-net-like...
- Example state machines:



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Course Map



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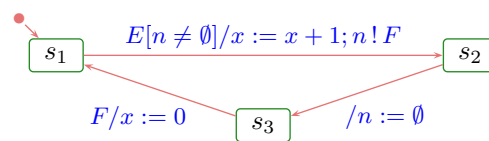
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UML State Machines: Overview

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UML State Machines

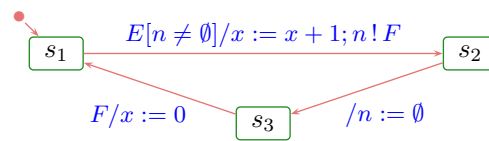


Brief History:

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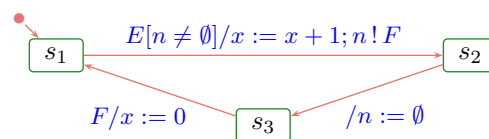
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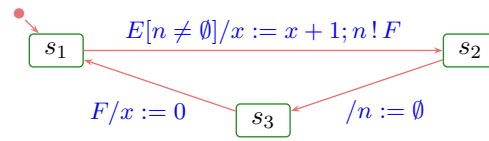


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- Harel (1987): **Statecharts** as a concise notation, introduces in particular hierarchical states.



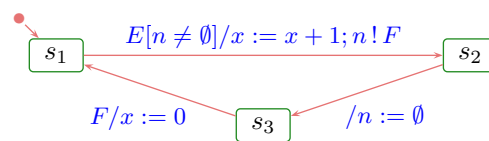
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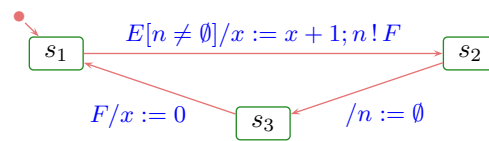
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UML State Machines



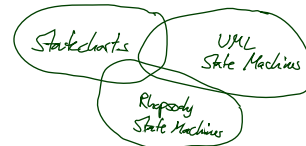
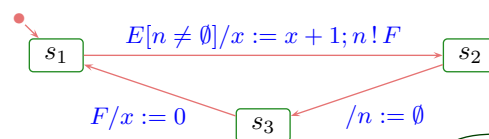
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Note: there is a common core, but each dialect interprets some constructs subtly different Crane and Dingel (2007).
(Would be too easy otherwise...)

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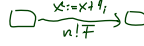
Roadmap: Chronologically

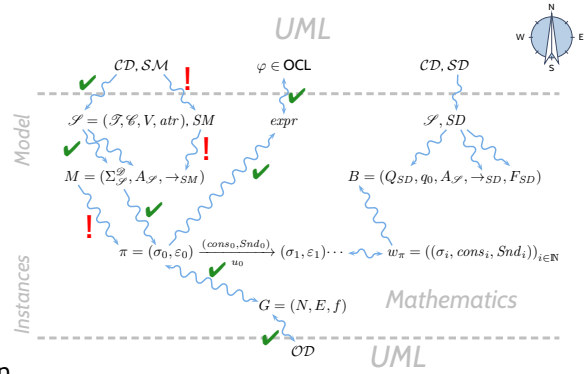
Syntax:

- (i) UML State Machine Diagrams.
- (ii) Def.: Signature with **signals**.
- (iii) Def.: **Core state machine**.
- (iv) Map UML State Machine Diagrams to core state machines.

Semantics:

The Basic Causality Model

- (v) Def.: **Ether** (aka. event pool)
- (vi) Def.: **System configuration**.
- (vii) Def.: **Event**.
- (viii) Def.: **Transformer**. 
- (ix) Def.: **Transition system**, computation.
- (x) Transition relation induced by core state machine.
- (xi) Def.: **step, run-to-completion step**.
- (xii) Later: Hierarchical state machines.



UML State Machines: Syntax

Signature With Signals

Definition. A tuple

$$\mathcal{S} = (\mathcal{T}, \mathcal{C}, V, atr, \mathcal{E}), \quad \mathcal{E} \text{ a set of signals,}$$

is called **signature (with signals)** if and only if

$$(\mathcal{T}, \mathcal{C} \cup \mathcal{E}, V, atr)$$

is a signature (as before).

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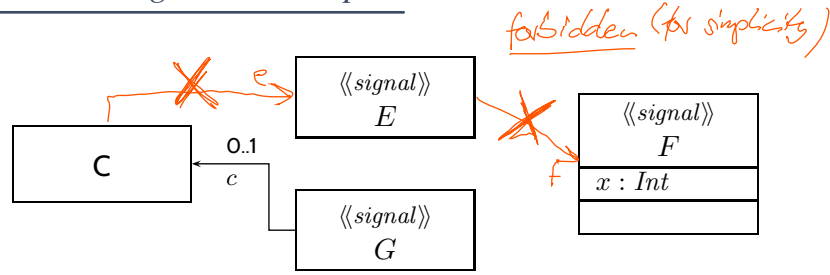
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is a signature (as before).

Note: Thus conceptually, **a signal is a class** and can have attributes of plain type, and participate in associations.

Signature with Signals: Example



$$\mathcal{S} = \left(\{Int\}, \{C\}, \{x: Int, c: C_{a1}\}, \right. \\ \left. \{C \mapsto \emptyset, E \mapsto \emptyset, G \mapsto \{c\}, F \mapsto \{x\}\}, \right. \\ \left. \{E, F, G\} \right)$$

Core State Machine

Definition.

A **core state machine** over signature $\mathcal{S} = (\mathcal{I}, \mathcal{C}, V, atr, \mathcal{E})$ is a tuple

$$M = (S, s_0, \rightarrow)$$

where

- S is a non-empty, finite set of **(basic) states**,
- $s_0 \in S$ is an **initial state**,
- and

$$\rightarrow \subseteq S \times \underbrace{(\mathcal{E} \dot{\cup} \{-\})}_{\text{trigger}} \times \underbrace{Expr_{\mathcal{S}}}_{\text{guard}} \times \underbrace{Act_{\mathcal{S}}}_{\text{action}} \times S$$

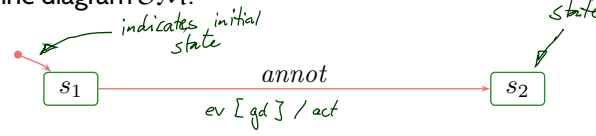
source state destination state

is a labelled transition relation.

We assume a set $Expr_{\mathcal{S}}$ of boolean expressions over \mathcal{S} (for instance OCL, may be something else) and a set $Act_{\mathcal{S}}$ of **actions**.

From UML to Core State Machines: By Example

UML state machine diagram SM :



$$annot ::= [\langle event \rangle [. \langle event \rangle]^*] [[\langle guard \rangle]] [/ [\langle action \rangle]]$$

with

- $event \in \mathcal{E}$,
- $guard \in Expr_{\mathcal{S}}$ (default: *true*, assumed to be in $Expr_{\mathcal{S}}$)
- $action \in Act_{\mathcal{S}}$ (default: *skip*, assumed to be in $Act_{\mathcal{S}}$)

maps to

$$M(SM) = \left(\underbrace{\{s_1, s_2\}}_{=S}, \underbrace{\{s_1\}}_{=s_0}, \underbrace{\{s_1, ev, gd, act, s_2\}}_{=\Rightarrow} \right)$$

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Abbreviations and Defaults in the Standard

Reconsider the syntax of transition annotations:

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What if things are missing?

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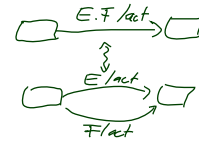
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In the standard, the syntax is even more elaborate:

- $E(v)$ – when consuming E in object u , attribute v of u is assigned the corresponding attribute of E .
- $E(v : T)$ – similar, but v is a local variable, scope is the transition

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State-Machines belong to Classes

In the following, we assume that

- a UML model consists of a set \mathcal{CD} of class diagrams and a set \mathcal{SM} of **state chart diagrams** (each comprising one **state machine** SM).
- each state machine $SM \in \mathcal{SM}$ is **associated with a class** $C_{SM} \in \mathcal{C}(\mathcal{S})$.

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If not explicitly given, then this one:

$$SM_0 := (\{s_0\}, s_0, \overset{\emptyset}{(s_0, \perp, \text{true}, \text{skip}, s_0)}).$$


We will see later that this choice does no harm semantically.

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Intuition 1: SM_C describes the behaviour of **the instances** of class C .

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Note: we don't consider **multiple state machines** per class. We will see later that this case can be viewed as a single state machine with as many AND-states.

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Rhapsody Demo II

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Towards UML State Machines Semantics: The Basic Causality Model

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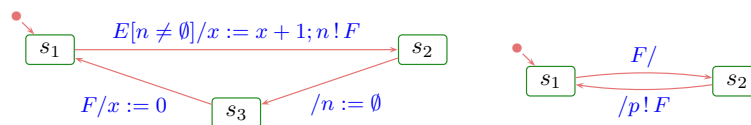
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6.2.3 The Basic Causality Model (OMG, 2011b, 11)

“‘**Causality model**’ is a specification of how things happen at run time [...].

The causality model is quite straightforward:

- Objects respond to **messages** that are generated by objects executing communication actions.
- When these messages arrive, the receiving objects eventually respond by executing the behavior that is **matched** to that message.
- The dispatching method by which a particular behavior is associated with a given message depends on the higher-level formalism used and is not defined in the UML specification
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The causality model also **subsumes** behaviors **invoking each other** and passing information to each other through arguments to parameters of the invoked behavior, [...].

This purely ‘procedural’ or ‘process’ model can be used by itself or in conjunction with the object-oriented model of the previous example.”

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15.3.12 StateMachine (OMG, 2011b, 574)

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- **Run-to-completion processing** means that an event [...] can only be taken from the pool and dispatched if the processing of the previous [...] is fully completed.
- The processing of a single event occurrence by a state machine is known as a **run-to-completion step**.

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- The **run-to-completion assumption** simplifies the transition function of the StM, since concurrency conflicts are avoided during the processing of event, allowing the StM to safely complete its **run-to-completion step**.

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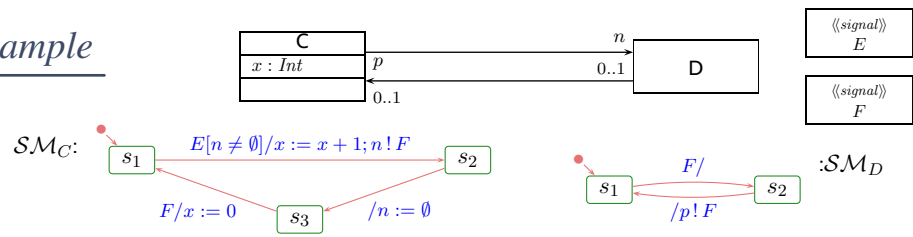
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- The order of dequeuing is **not defined**, leaving open the possibility of modeling different priority-based schemes.
- Run-to-completion may be implemented in **various ways**. [...]

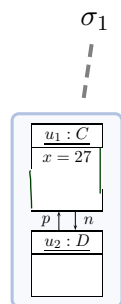
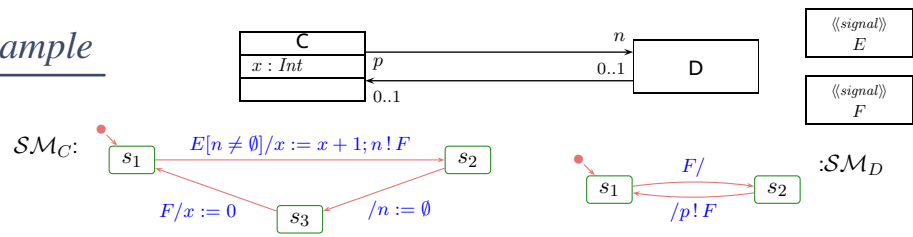
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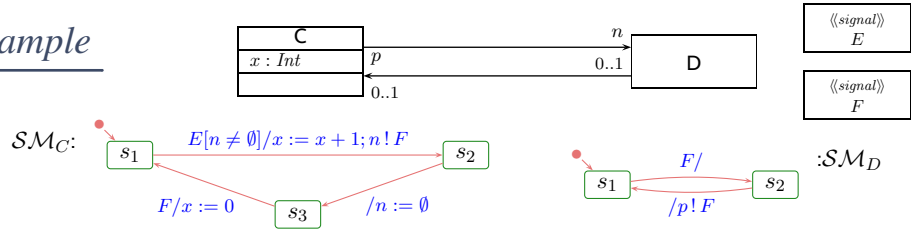
Example



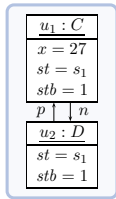
Example



Example



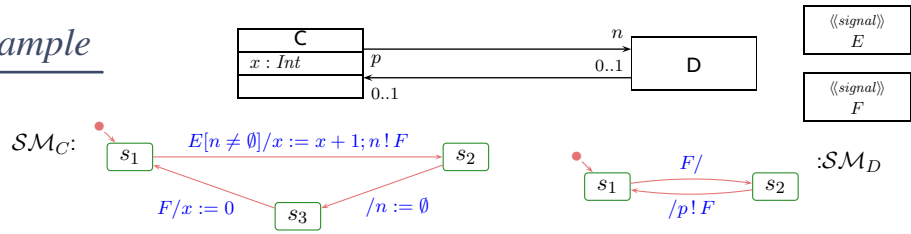
σ_1



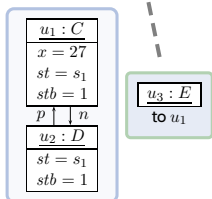
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Example



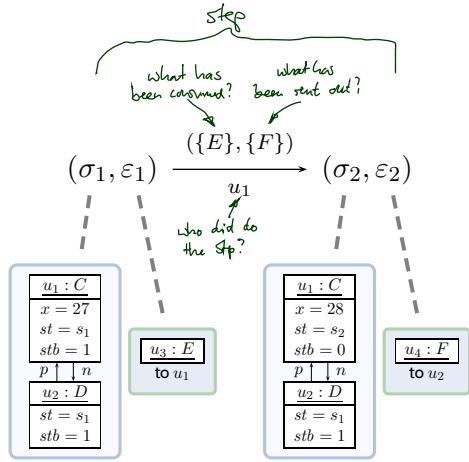
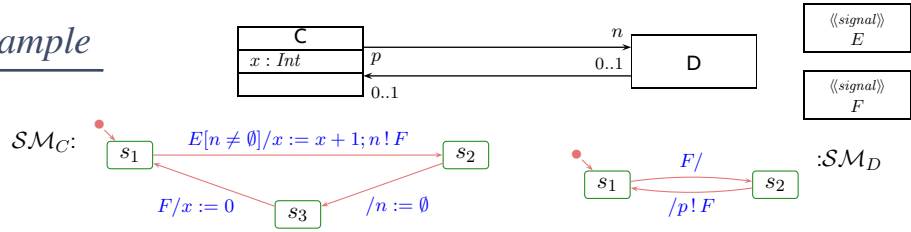
$(\sigma_1, \varepsilon_1)$



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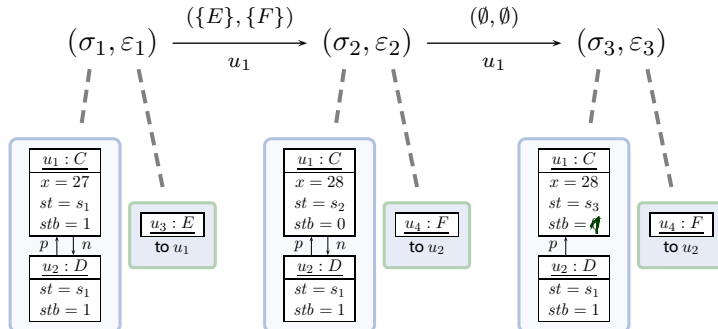
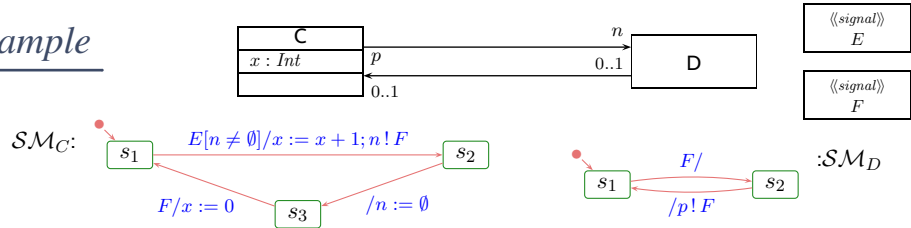
Example



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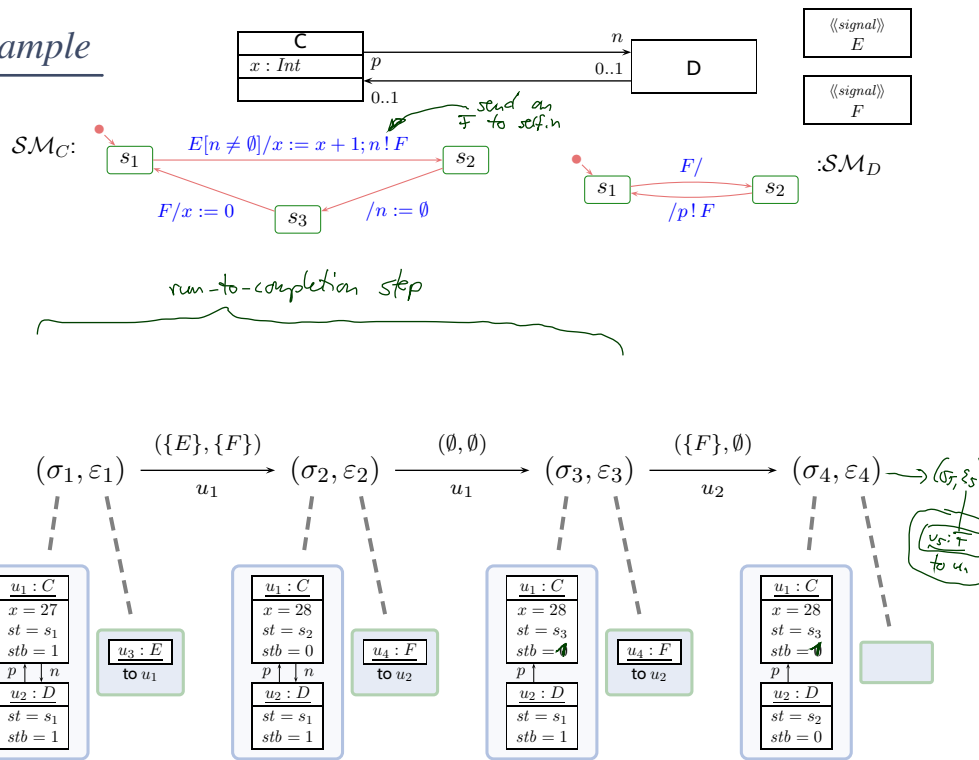
Example



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Example



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Tell Them What You've Told Them...

- Ambler (2005): **The Elements of UML 2.0 Style**.
- One rule-of-thumb:
if there is a standard architecture, make it easy to recognise how the standard architecture is concretised.
- Behaviour can be modelled using UML **State Machines**.
- UML **State Machines** are inspired by Harel's **Statecharts**.
- State Machines **belong to** Classes.
- State machine behaviour follows the **Basic Causality Model** of UML, in particular
 - Objects process **events**.
 - Objects can be **stable** or not.
 - Events are processed in a **run-to-completion step**, processing only starts when being **stable**,

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