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Content

- What makes a class diagram a **good** class diagram?
 - The Elements of UML 2.0 Style: Coré
 - Example: Game Architecture
-
- **Purposes of Behavioural Models**
 - **Constructive Behavioural Models in UML**
 - UML State Machines
 - Brief History
 - **Syntax**
 - **The Basic Causality Model**

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Design Guidelines for (Class) Diagram
(partly following Ambler (2005))

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Class Diagram Guidelines Ambler (2005)

- **5.3 Relationships**

- 112. Model Relationships Horizontally
- 115. Model a Dependency When the Relationship is Transitory
- 117. Always Indicate the Multiplicity
- 118. Avoid Multiplicity “..”
- 119. Replace Relationship Lines with Attribute Types



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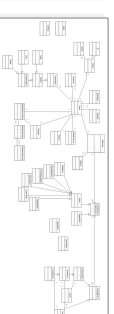
Some Example Class Diagrams



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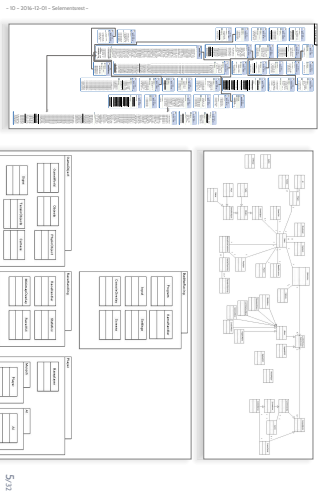
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Some Example Class Diagrams

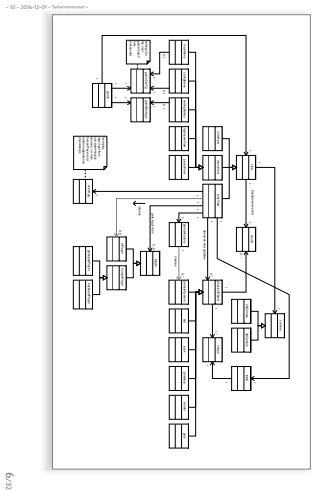


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Some Example Class Diagrams



More Example Class Diagrams



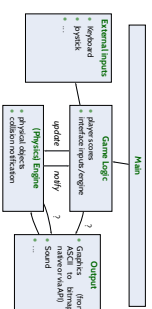
Example: Modelling Games

Modelling Structure: Common Architectures

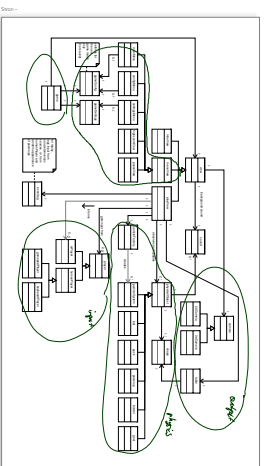
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Modelling Structure: Common Architectures

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- Adept readers try to see/find/match the common architecture if they know that a model is from a particular domain.
- We can do those readers a favour by grouping/positioning things in the diagram so that seeing/finding/matching is easy.



Modelling Behaviour

Stocktaking...

- Have Means to model the structure of the system.
 - Class diagrams graphically, concisely describe sets of system states.
 - OCL expressions logically state constraints/invariants on system states.
 - Want Means to model behaviour of the system.
 - Means to describe how system states evolve over time that is, to describe sets of sequences
- $s_0, r_1, \dots \in \Sigma^{\omega}$
- of system states.

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Image

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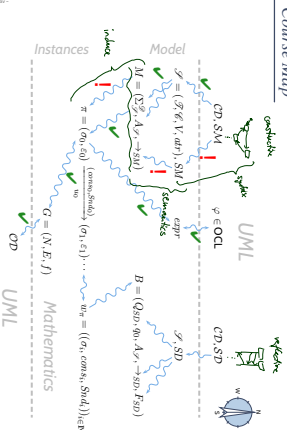
- **Require Behaviour.**
"This sequence of inserting money and requesting and getting water must be possible."
(Otherwise the software for the vending machine is completely broken)
- **Allow Behaviour.**
"System does subset of this"
"After inserting money and choosing a drink, the drink is dispensed (if in stock)."
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• **Forbid Behaviour.**
"System never does this"
"This sequence of getting both a water and all money back, must not be possible." (Otherwise the software is broken)

Note: the latter two are trivially satisfied by doing nothing...

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Course Map



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UML State Machines: Overview

Constructive Behaviour in UML

UML provides two visual formalisms for constructive description of behaviours:

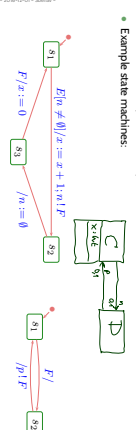
- **Activity Diagrams**
- **State-Machine Diagrams**

We (exemplary) focus on State-Machines because

- somehow "practice proven" (in different flavours),
- prevalent in embedded systems community,
- indicated useful by Dobing and Pagans (2006) survey and
- Activity Diagrams intuition changed (between UML 1.x and 2.x)

from transition-system-like to petri-net-like.

Example state machines:



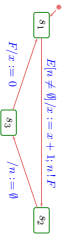
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UML State Machines

Brief History:



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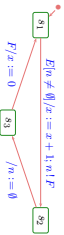
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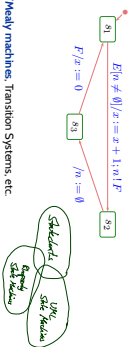
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Note: there is a common core, but each dialect interprets some constructs slightly different. *Came and Dingsel (2007). (Would be too easy otherwise...)*

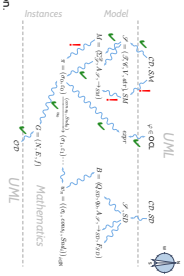
Syntax:

- (i) UML State Machine Diagrams
- (ii) Def.: Signature with signals.
- (iii) Def.: Core state machine.
- (iv) Map UML State Machine Diagrams to core state machines.

Semantics

The Basic Causality Model

- (v) Def.: Either (aka event pool)
- (vi) Def.: System configuration.
- (vii) Def.: Event.
- (viii) Def.: Transform.
- (ix) Def.: Transition relation induced by core state machine.
- (x) Def.: step, run-to-completion step.
- (xi) Later: Hierarchical state machines.



UML State Machines: Syntax

Signature With Signals

Definition. A tuple

$\mathcal{S} = (\mathcal{S}, \mathcal{E}, V, \text{attr}, \delta), \quad \delta \text{ a set of signals,}$

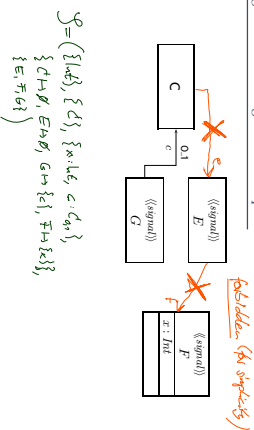
is called **signature (with signals)** if and only if

$$(\mathcal{S}, \mathcal{E} \cup \delta, V, \text{attr})$$

is a signature (as before).

Note. Thus conceptually, a **signal** is a class and can have attributes of plain type and participate in associations.

Signature with Signals: Example



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Core State Machine

Definition.

A **core state machine** over signature $\mathcal{S} = (\mathcal{S}, \mathcal{E}, V, \text{attr}, \delta)$ is a tuple

$$M = (S, s_0, \rightarrow)$$

where

- S is a non-empty, finite set of (basic) states,

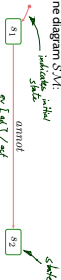
- $s_0 \in S$ is an initial state,

- and $\rightarrow \subseteq S \times (\mathcal{E} \cup \{\perp\}) \times \text{Expr}_{\mathcal{S}} \times \text{Act}_{\mathcal{S}} \times S$

is a labelled transition relation.

We assume a set $\text{Expr}_{\mathcal{S}}$ of boolean expressions over \mathcal{S} (for instance OCL, may be something else) and a set $\text{Act}_{\mathcal{S}}$ of actions.

UML state machine diagram SM



$$annot ::= [\langle event \rangle, \langle event \rangle^*] [[\langle guard \rangle]] [/ \langle action \rangle]$$

with

- $event \in \mathcal{E}$,
- $guard \in Expr_{\mathcal{G}}$ **(default: true, assumed to be in $Expr_{\mathcal{G}}$)**
- $action \in Act_{\mathcal{G}}$ **(default: skip, assumed to be in $Act_{\mathcal{G}}$)**

maps to

$$P(SM) = \left(\underbrace{\{s_1, s_2\}}_{\mathcal{S}} \left\{ \underbrace{\{s_1 \xrightarrow{event, guard, action} s_2, s_2 \xrightarrow{event, action} s_2\}}_{\mathcal{A}} \right\} \right)$$

Reconsider the syntax of transition annotations:

$$annot ::= [\langle event \rangle, \langle event \rangle^*] [[\langle guard \rangle]] [/ \langle action \rangle]$$

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What if things are missing?

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In the standard, the syntax is even more elaborate:

- $E(v)$ — when consuming E in object v , attribute v of u is assigned the corresponding attribute of E .
- $E(v : T)$ — similar but v is a local variable, scope is the transition

In the following, we assume that

- a UML model consists of a set $\mathcal{C}_{\mathcal{G}}$ of class diagrams and a set $\mathcal{S}_{\mathcal{M}}$ of state chart diagrams (each comprising one state machine SM).
- each state machine $SM \in \mathcal{S}_{\mathcal{M}}$ is associated with a class $C_{SM} \in \mathcal{C}(\mathcal{C})$.

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- For simplicity, we even assume a bijection, i.e. we assume that each class $C \in \mathcal{C}(\mathcal{C})$ has a state machine SM_C and that its class C_{SM_C} is C . If not explicitly given, then this one:

$$SM_C := (\{s_0\}, s_0, \text{true} \xrightarrow{\text{event}} \text{true} \xrightarrow{\text{skip}} s_0)$$

We will see later that this choice does no harm semantically.



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Note: we don't consider multiple state machines per class. We will see later that this case can be viewed as a single state machine with as many AND-states.

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Rhapsody Demo II

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6.2.3 The Basic Causality Model (OMG, 2011b, 11)

“**Causality model** is a specification of how things happen at run time [...]

The causality model is quite straightforward:

- Objects respond to messages that are generated by objects executing communication actions.
- When these messages arrive, the receiving objects eventually respond by executing the behavior that is *matched* to that message.
- The dispatching method by which a particular behavior is associated with a given message depends on the higher-level formalism used and is not defined in the UML specification (i.e., it is a semantic variation point).



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The causality model also *assumes* behaviors *invoking each other* and *passing information* to each other through arguments or parameters of the invoked behavior. [...] This purely procedural or process model can be used by itself or in conjunction with the object-oriented model of the previous example.”

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Towards UML State Machines Semantics: The Basic Causality Model

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- Event occurrences are detected, dispatched, and then processed by the state machine, one at a time.
- The semantics of event occurrence processing is based on the **run-to-completion assumption**, interpreted as **run-to-completion processing**.
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- The same conditions apply after the **run-to-completion step** is completed.
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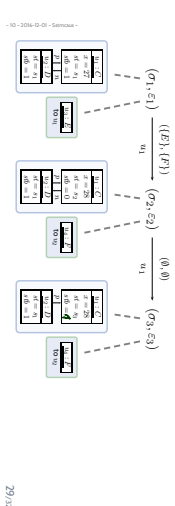
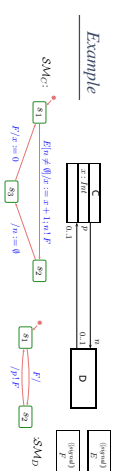
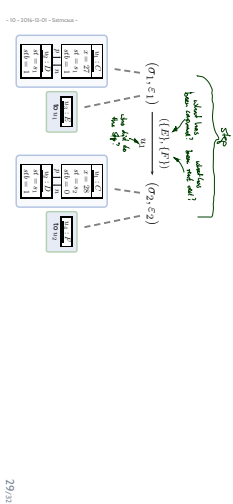
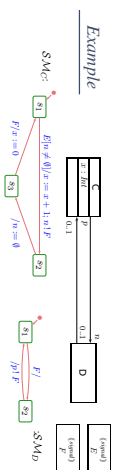
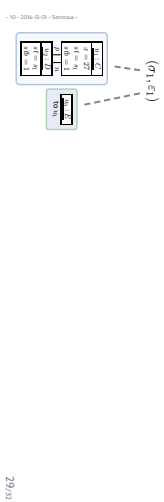
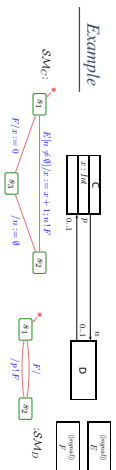
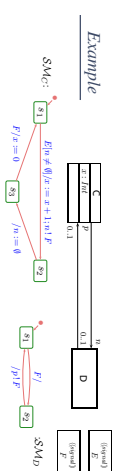
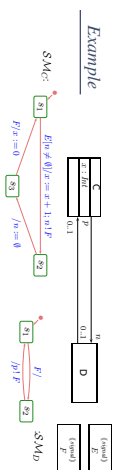
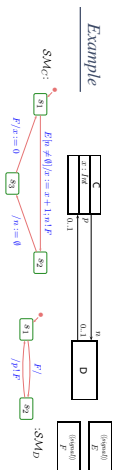
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- The same conditions apply after the **run-to-completion step** is completed.
- Thus, an event occurrence will never be processed [...] in some intermediate and inconsistent situation.
- [IOV4] The **run-to-completion step** is the order between two state configurations of the state machine.
- **Run-to-completion processing** means that an event [...] can only be taken from the pool and dispatched if the processing of the previous [...] is fully completed.
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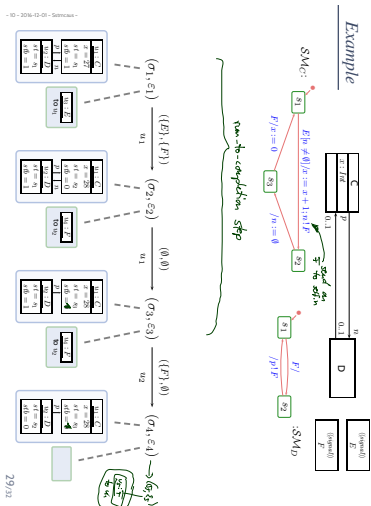
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Example



Tell Them What You've Told Them...

- Ambler (2005). **The Elements of UML 2.0 Style.**
- One rule-of-thumb: if there is a standard architecture, make it easy to recognise how the standard architecture is concretised.
- Behaviour can be modelled using UML State Machines.
- UML State Machines are inspired by Harel's Statecharts.
- State Machines belong to Classes.
- State machine behaviour follows the Basic Causality Model of UML, in particular
 - Objects process events.
 - Objects can be stable or not.
 - Events are processed in a run-to-completion step, processing only starts when being stable.

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