## Software Design, Modelling and Analysis in UML Lecture 10: Modelling Behaviour

2016-12-01

Prof. Dr. Andreas Podelski, Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

#### Content

- What makes a class diagram a good class diagram?
  - The Elements of UML 2.0 Style Cont'd
  - Example: Game Architecture
- Purposes of Behavioural Models
- Constructive Behavioural Models in UML
- UML State Machines
  - Brief History
  - Syntax
  - → The Basic Causality Model

# 10 - 2016-12-01 - main -

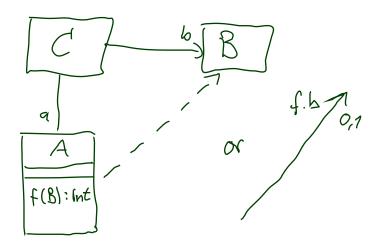
#### Design Guidelines for (Class) Diagram

(partly following Ambler (2005))

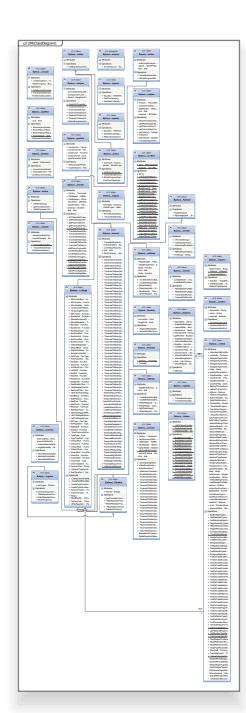
#### Class Diagram Guidelines Ambler (2005)

#### 5.3 Relationships

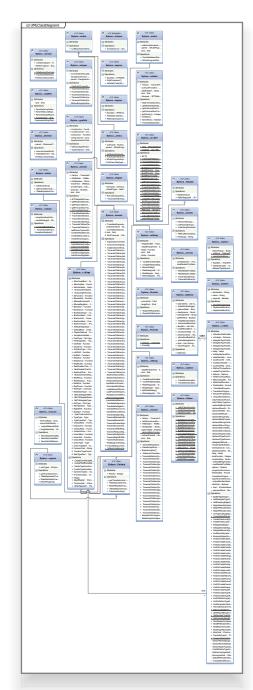
- 112. Model Relationships Horizontally
- 115. Model a Dependency When the Relationship is <u>Transitory</u>
- 117. Always Indicate the Multiplicity
- 118. Avoid Multiplicity "\*"
- 119. Replace Relationship Lines with Attribute Types

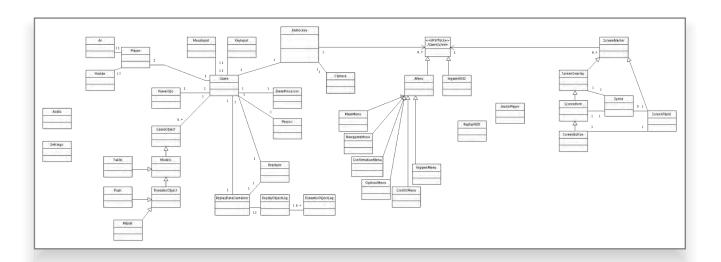


#### Some Example Class Diagrams

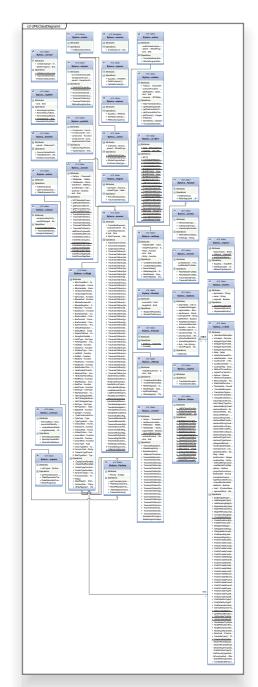


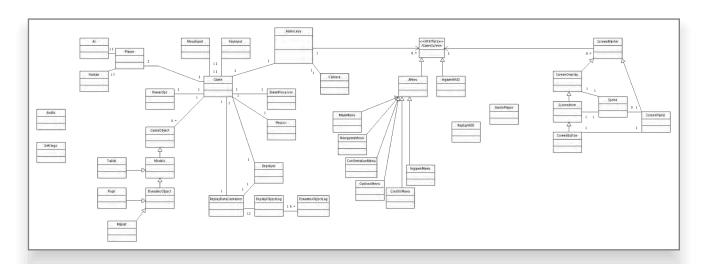
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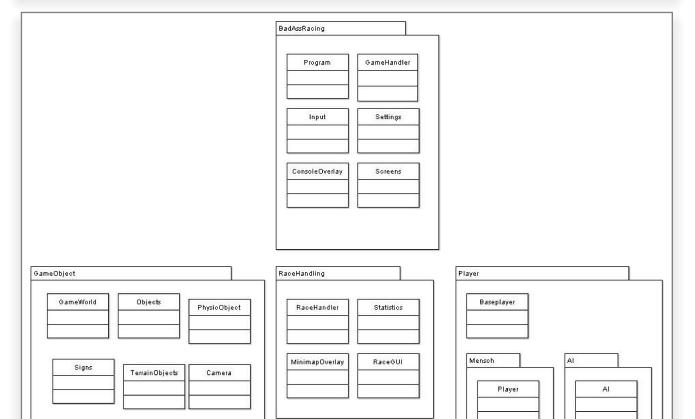




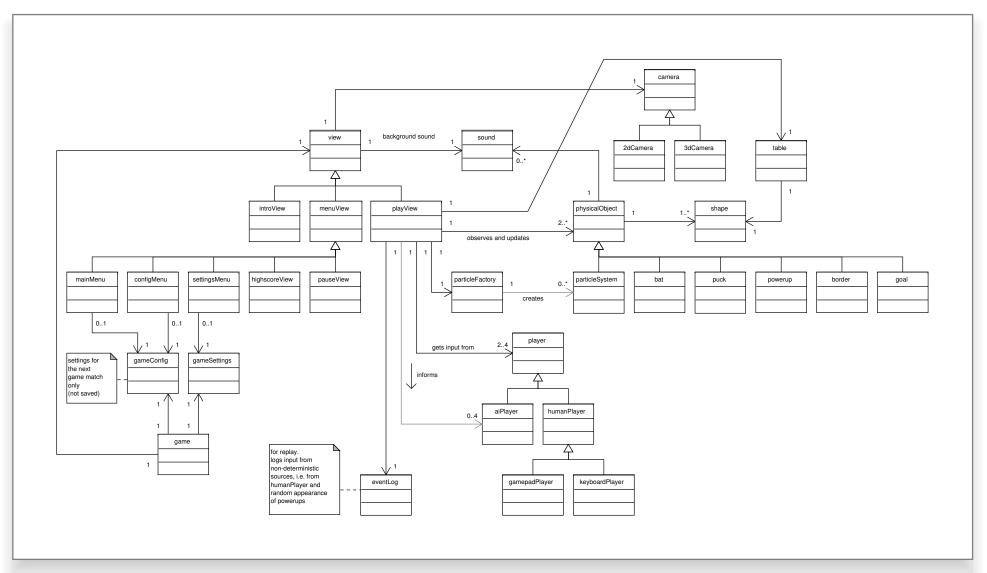
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#### More Example Class Diagrams



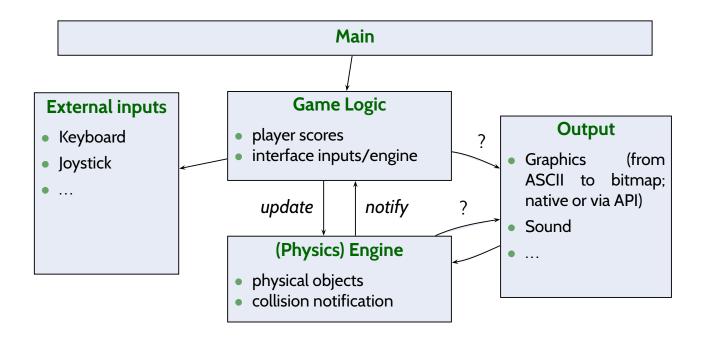
Example: Modelling Games

#### Modelling Structure: Common Architectures

- Many domains have common, canonical architectures.
- For games, for example:

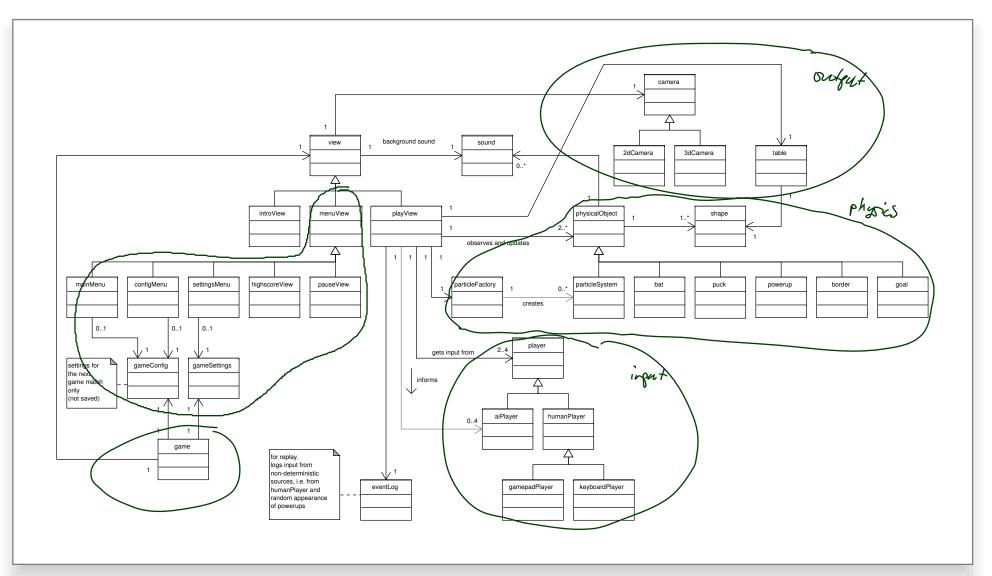
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- Many domains have common, canonical architectures.
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- Adept readers try to see/find/match the common architecture if they know that a model is from a particular domain.
- We can do those readers a favour by grouping/positioning things in the diagram so that seeing/finding/matching is easy.

#### Example Re-Considered



### Modelling Behaviour

#### Stocktaking...

Have: Means to model the structure of the system.

- Class diagrams graphically, concisely describe sets of system states.
- OCL expressions logically state constraints/invariants on system states.

Want: Means to model behaviour of the system.

 Means to describe how system states evolve over time, that is, to describe sets of sequences

$$\sigma_0, \sigma_1, \dots \in \Sigma^{\omega}$$

of system states.

**Example**: Pre-Image (the UML model is supposed to be the blue-print for a software system).

A description of behaviour could serve the following purposes:

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#### What Can Be Purposes of Behavioural Models?

**Example**: Pre-Image (the UML model is supposed to be the blue-print for a software system).

A description of behaviour could serve the following purposes:

Require Behaviour.

"This sequence of inserting money and requesting and getting water must be possible." (Otherwise the software for the vending machine is completely broken.)

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Allow Behaviour.

"After inserting money and choosing a drink the drink is dispensed (if in stock)." (If the implementation insists on taking the money first, that's a fair choice.)

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Forbid Behaviour.

"This sequence of getting both, a water and all money back, must not be possible." (Otherwise the software is broken.)

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Note: the latter two are trivially satisfied by doing nothing...

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A description of behaviour could serve the following purposes:

• Require Behaviour.

"System definitely does this"

"This sequence of inserting money and requesting and getting water must be possible." (Otherwise the software for the vending machine is completely broken.)

Allow Behaviour.

"System does subset of this"

"After inserting money and choosing a drink, the drink is dispensed (if in stock)." (If the implementation insists on taking the money first, that's a fair choice.)

Forbid Behaviour.

"System never does this"

"This sequence of getting both, a water and all money back, must not be possible." (Otherwise the software is broken.)

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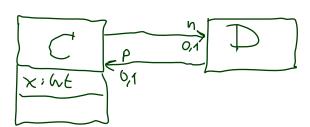
#### Constructive Behaviour in UML

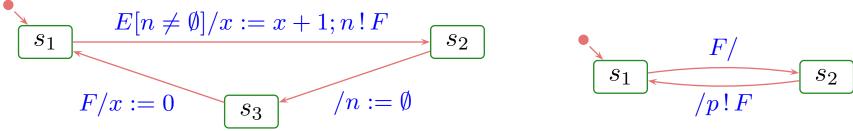
UML provides two visual formalisms for constructive description of behaviours:

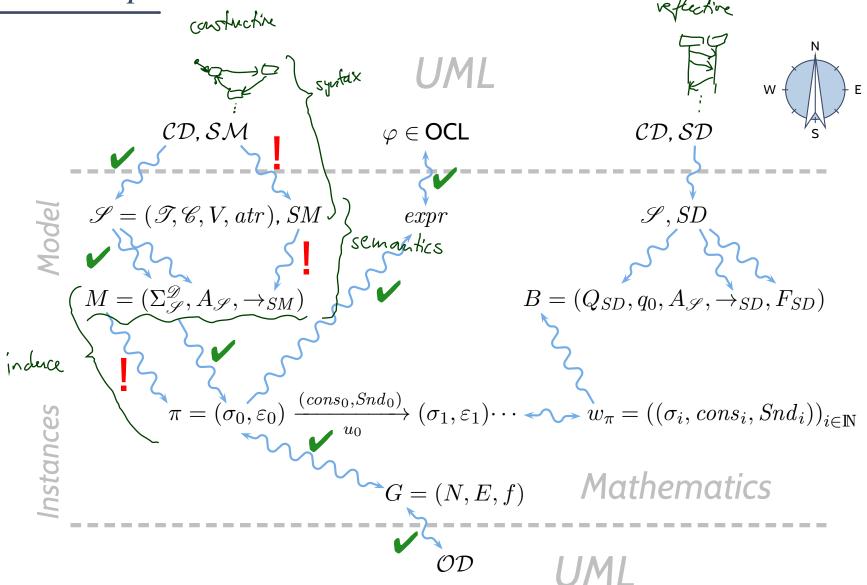
- Activity Diagrams
- State-Machine Diagrams

We (exemplary) focus on State-Machines because

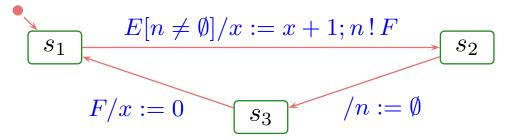
- somehow "practice proven" (in different flavours),
- prevalent in embedded systems community,
- indicated useful by Dobing and Parsons (2006) survey, and
- Activity Diagram's intuition changed (between UML 1.x and 2.x)
   from transition-system-like to petri-net-like...
- Example state machines:

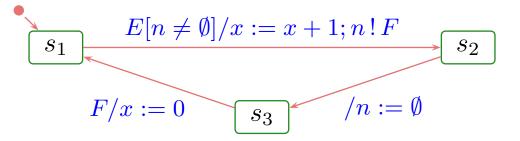






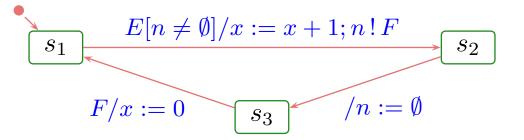
#### UML State Machines: Overview



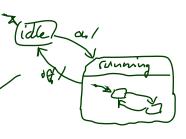


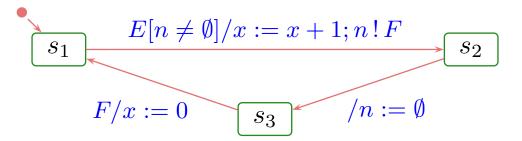
#### **Brief History**:

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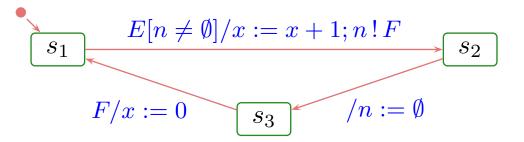


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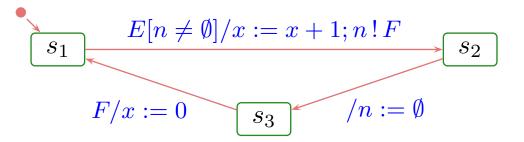




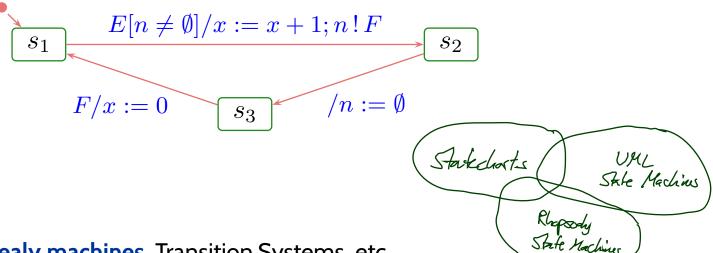
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**Note**: there is a common core, but each dialect interprets some constructs subtly different Crane and Dingel (2007). (Would be too easy otherwise...)

#### Roadmap: Chronologically

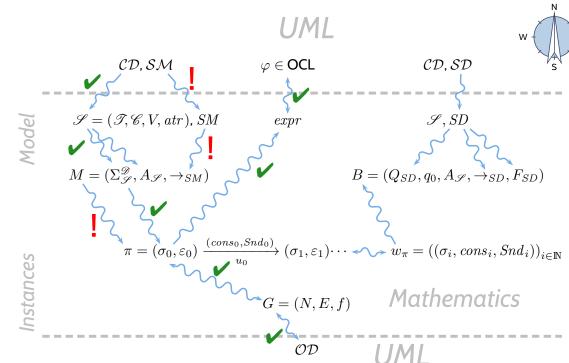
#### Syntax:

- (i) UML State Machine Diagrams.
- (ii) Def.: Signature with signals.
- (iii) Def.: Core state machine.
- (iv) Map UML State Machine Diagrams to core state machines.

#### **Semantics**:

The Basic Causality Model

- (v) Def.: Ether (aka. event pool)
- (vi) Def.: System configuration.
- (vii) Def.: Event.
- (viii) Def.: Transformer.
- ~ ×:=×+ 1;
- (ix) Def.: Transition system, computation.
- (x) Transition relation induced by core state machine.
- (xi) Def.: step, run-to-completion step.
- (xii) Later: Hierarchical state machines.



#### UML State Machines: Syntax

#### Signature With Signals

#### **Definition**. A tuple

$$\mathscr{S} = (\mathscr{T}, \mathscr{C}, V, atr, \mathscr{E}), \qquad \mathscr{E} \text{ a set of signals},$$

is called signature (with signals) if and only if

$$(\mathscr{T},\mathscr{C}\cup\mathscr{E},V,atr)$$

is a signature (as before).

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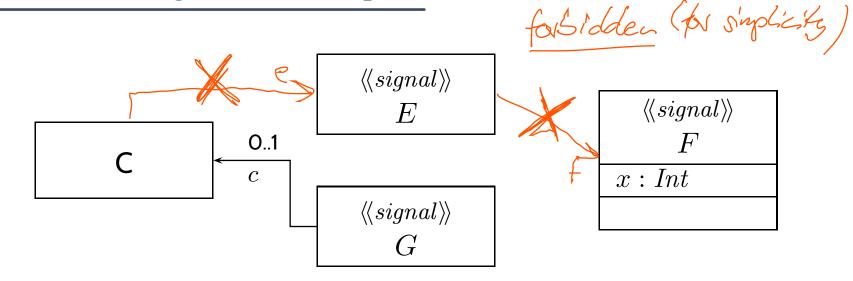
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is a signature (as before).

**Note:** Thus conceptually, a signal is a class and can have attributes of plain type, and participate in associations.

#### Signature with Signals: Example



#### Definition.

A core state machine over signature  $\mathscr{S}=(\mathscr{T},\mathscr{C},V,atr,\mathscr{E})$  is a tuple

$$M = (S, s_0, \to)$$

#### where

• S is a non-empty, finite set of (basic) states,

•  $s_0 \in S$  is an initial state, source state

and

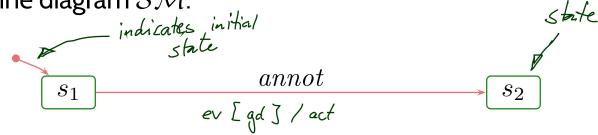
$$\rightarrow \subseteq S \times \underbrace{(\mathscr{E} \cup \{\_\})}_{\text{trigger}} \times \underbrace{Expr_{\mathscr{S}}}_{\text{guard}} \times \underbrace{Act_{\mathscr{S}}}_{\text{action}} \times S$$

is a labelled transition relation.

We assume a set  $Expr_{\mathscr{S}}$  of boolean expressions over  $\mathscr{S}$  (for instance OCL, may be something else) and a set  $Act_{\mathscr{S}}$  of actions.

# From UML to Core State Machines: By Example

UML state machine diagram SM:



$$annot ::= [\langle event \rangle [.\langle event \rangle]^*] [[\langle guard \rangle]] [/[\langle action \rangle]]$$

#### with

- $event \in \mathcal{E}$ ,
- $guard \in Expr_{\mathscr{S}}$
- $action \in Act_{\mathscr{S}}$

(default: true, assumed to be in  $Expr_{\mathscr{S}}$ )

(default: skip, assumed to be in  $Act_{\mathscr{S}}$ )

### maps to

$$M(SM) = (\{S_1, S_2\}, \{S_1\}, \{\{S_1, ev, gd, act, S_2\}\})$$

$$= S = S_6 = S_6$$

# Abbreviations and Defaults in the Standard

### **Reconsider** the syntax of transition annotations:

```
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#### What if things are missing?

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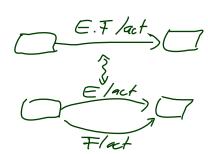
### What if things are missing?

$$\sim \sim _{-}$$
 [true] / skip

/  $\sim \sim _{-}$  [true] / skip

 $E / \sim \sim E$  [true] / skip

/  $act \sim \sim _{-}$  [true] /  $act$ 
 $E / act \sim \sim E$  [true] /  $act$ 



In the standard, the syntax is even more elaborate:

- E(v) when consuming E in object u, attribute v of u is assigned the corresponding attribute of E.
- E(v:T) similar, but v is a local variable, scope is the transition

### In the following, we assume that

- a UML model consists of a set  $\mathscr{CD}$  of class diagrams and a set  $\mathscr{SM}$  of state chart diagrams (each comprising one state machine  $\mathscr{SM}$ ).
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- For simplicity, we even assume a bijection, i.e. we assume that each class  $C \in \mathscr{C}(\mathscr{S})$  has a state machine  $\mathcal{SM}_C$  and that its class  $C_{\mathcal{SM}_C}$  is C. If not explicitly given, then this one:

$$\mathcal{SM}_0 := (\{s_0\}, s_0, | (s_0, \underline{\hspace{0.5cm}}, \textit{true}, \textit{skip}, s_0)).$$

We will see later that this choice does no harm semantically.

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**Note**: we don't consider **multiple state machines** per class. We will see later that this case can be viewed as a single state machine with as many AND-states.

# Rhapsody Demo II

# Towards UML State Machines Semantics: The Basic Causality Model

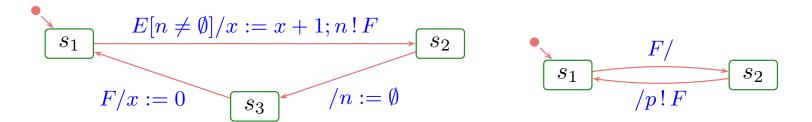
# 6.2.3 The Basic Causality Model (OMG, 2011b, 11)

"Causality model' is a specification of how things happen at run time [...].

The causality model is quite straightforward:

- Objects respond to messages that are generated by objects executing communication actions.
- When these messages arrive, the receiving objects eventually respond by executing the behavior that is matched to that message.
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The causality model also **subsumes** behaviors **invoking each other** and passing information to each other through arguments to parameters of the invoked behavior, [...].

This purely 'procedural' or 'process' model can be used by itself or in conjunction with the object-oriented model of the previous example."

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- [IOW,] The run-to-completion step is the passage between two state configurations of the state machine.
- The run-to-completion assumption simplifies the transition function of the StM, since concurrency conflicts are avoided during the processing of event, allowing the StM to safely complete its run-to-completion step.

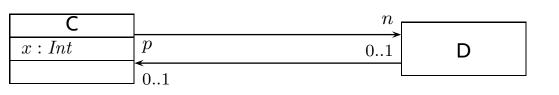
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- [IOW,] The run-to-completion step is the passage between two state configurations of the state machine.
- The run-to-completion assumption simplifies the transition function of the StM, since concurrency conflicts are avoided during the processing of event, allowing the StM to safely complete its run-tocompletion step.
- The order of dequeuing is not defined, leaving open the possibility of modeling different priority-based schemes.

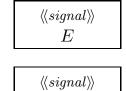
- Event occurrences are detected, dispatched, and then processed by the state machine, one at a time.
- The semantics of event occurrence processing is based on the run-to-completion assumption, interpreted as run-to-completion processing.
- Run-to-completion processing means that an event [...] can only be taken from the pool and dispatched if the processing of the previous [...] is fully completed.
- The processing of a single event occurrence by a state machine is known as a run-to-completion step.
- Before commencing on a run-tocompletion step, a state machine is in a stable state configuration with all entry/exit/internal-activities (but not necessarily do-activities) completed.

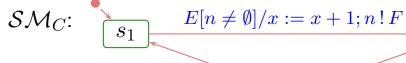
- The same conditions apply after the runto-completion step is completed.
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- The run-to-completion assumption simplifies the transition function of the StM, since concurrency conflicts are avoided during the processing of event, allowing the StM to safely complete its run-tocompletion step.
- The order of dequeuing is not defined, leaving open the possibility of modeling different priority-based schemes.
- Run-to-completion may be implemented in various ways. [...]

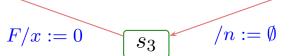


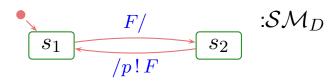


 $s_2$ 

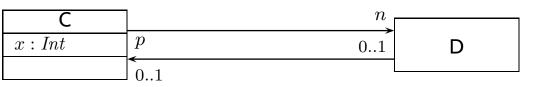


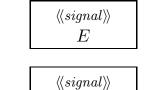




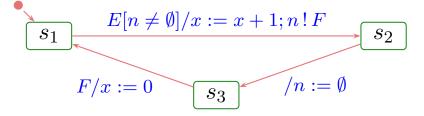


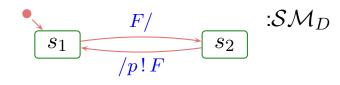




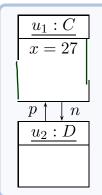




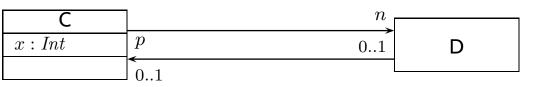


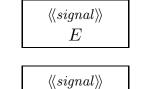




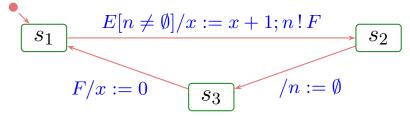


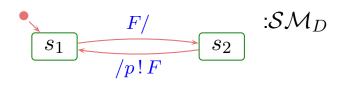




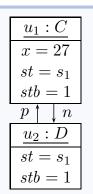




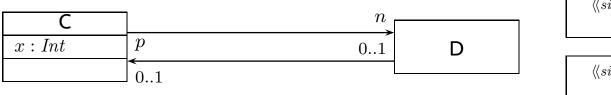


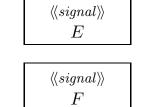


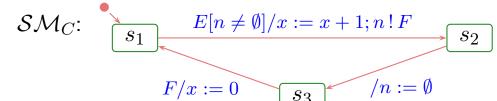




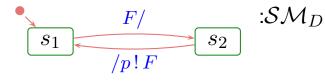


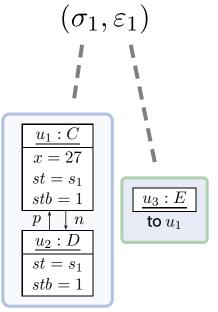




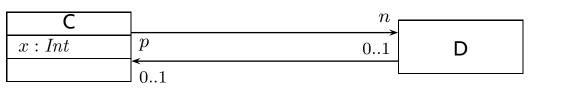


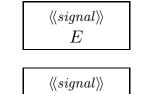
 $s_3$ 

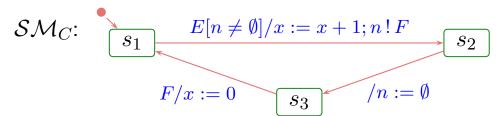


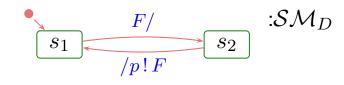


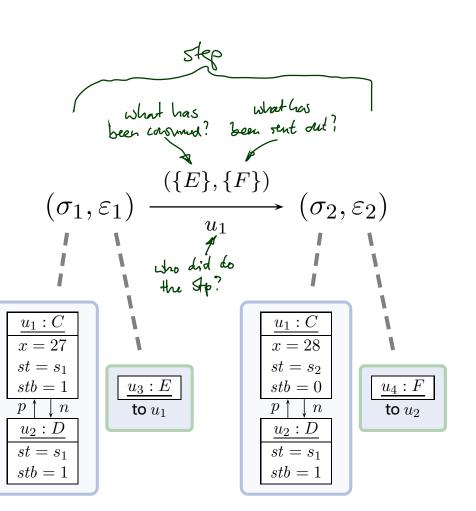




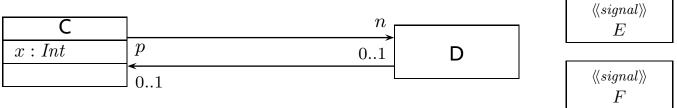


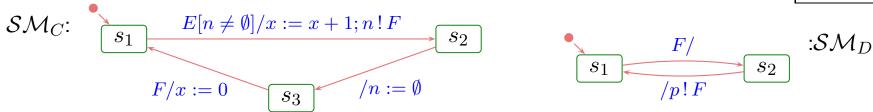


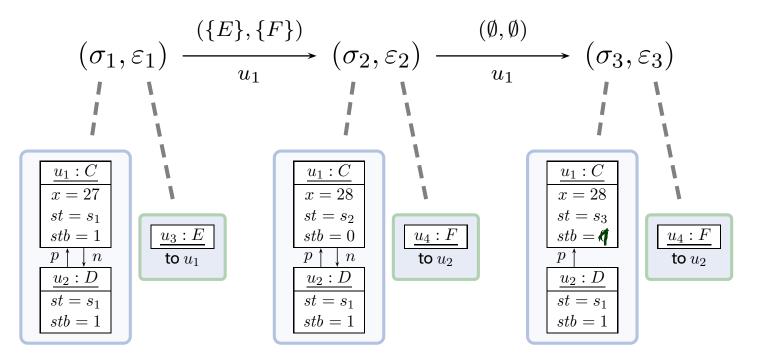


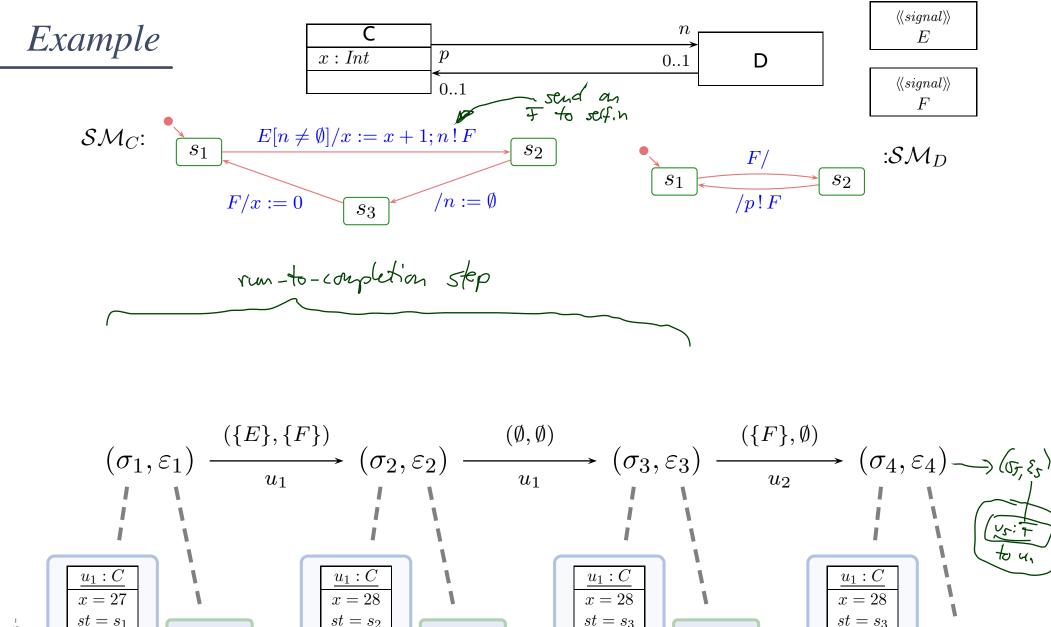












 $u_4:F$ 

to  $u_2$ 

stb = 0

 $p \uparrow \downarrow n$ 

 $u_2:D$ 

 $st = s_1$ 

stb = 1

 $u_4:F$ 

to  $u_2$ 

stb = 1

 $\underline{u_2:D}$ 

 $st = s_2$ 

stb = 0

stb = 4

 $\underline{u_2:D}$ 

 $\overline{st} = s_1$ 

stb = 1

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stb = 1

 $\underline{u_2}:D$ 

 $\overline{st} = s_1$ 

stb = 1

 $\downarrow n$ 

 $u_3:E$ 

to  $u_1$ 

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### Tell Them What You've Told Them...

- Ambler (2005): The Elements of UML 2.0 Style.
- One rule-of-thumb:
   if there is a standard architecture, make it easy to recognise how
   the standard architecture is concretised.
- Behaviour can be modelled using UML State Machines.
- UML State Machines are inspired by Harel's Statecharts.
- State Machines belong to Classes.
- State machine behaviour follows the Basic Causality Model of UML, in particular
  - Objects process events.
  - Objects can be stable or not.
  - Events are processed in a run-to-completion step, processing only starts when being stable,

# References

# References

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