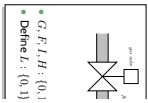


Duration Calculus: Preview

- Duration Calculus is an interval logic.
- Formulae are evaluated in an (implicitly given) interval.



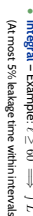
Strangest operators – Example: $[C]$

(holds in a given interval $[a, b]$ iff the gas valve is open almost everywhere)

• **always everywhere** – Example: $[C]$

• **chop** – Example: $([-1]; [1]; [-1]) \Rightarrow \ell \geq 1$
(ignition phases last at least one time unit)

• **integral** – Example: $\ell \geq 0 \Rightarrow \int L \leq \frac{\ell}{20}$
(at most 5% leakage time within intervals of at least 60 time units)



Content

- Semantics-based Correctness Proofs
 - Example: Gas Burner Controller
 - Theorem 2.16: Des-1 and Des-2 is a correct design wrt. Req
 - Lemma 2.19: Des-1 and Des-2 imply a simplified requirement Req-1
 - Some Laws of the DC Integral Operator
 - Lemma 2.17: Req-1 implies Req
- Obstacles (in a Non-Ideal World)
 - requirements may be unrealisable without considering plant assumptions
 - intermediate design levels
 - different observables
 - proving correctness may be difficult
- If time permits: A Calculus for DC

Methodology (in an ideal world)

In order to prove a controller design correct wrt. a specification:

- Choose observables: Obs.
- Formalise the requirements Req as a conjunction of DC formulae (over Obs).
- Formalise a controller design Ctrl as a conjunction of DC formulae (over Obs).
- We say 'Ctrl' is correct (wrt. Req) iff

$$\models_0 \text{Ctrl} \Rightarrow \text{Req},$$
 so "just" prove $\models_0 \text{Ctrl} \Rightarrow \text{Req}.$

Real-Time Systems

Lecture 5: Duration Calculus

2017-11-09

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Content

Introduction

- Observables and Evolutions
- Duration Calculus (DC) ✓
- Semantical Correctness Proofs S
 - DC Decidability ϵ/ℓ^2
 - DC Implementables
 - PLC Automata
- Timed Automata (TA), Uppaal
 - Networks of Timed Automati
 - Region/Zone-Abstraction
 - TA model-checking
 - Extended Timed Automata
 - Undecidability Results

obs : Time $\rightarrow \mathcal{D}(obs)$

$(obs_0, obs_1) \xrightarrow{\Delta} (obs_1, obs_1)$

- Automatic Verification – whether a TA satisfies a DC formula, observer-based
- Recent Results
- Timed Sequence Diagrams, or Quasi-equal Clocks, or Automatic Code Generation, or ...

Specification and Semantics-based Correctness I of Real-Time Systems with DC

Gas Burner Revisited



gas valve

flame

input

output

- (i) Choose observables:
- $F : \{0, 1\}$ value 1 mode1 "flame sensed now"
 - $G : \{0, 1\}$ value 1 mode1 "gas valve is open now"
 - define $L := G \wedge \neg F$ to model leakage

(ii) Formalise the requirement:

$$\text{Req} := \Box(\ell \geq 60 \implies 20 \cdot f \cdot L \leq \ell)$$

"in each interval of length at least 60 time units, at most 5% of the tm

(iii) Formalise controller design ideas:

- Des-1 := $\Box(L \implies \ell \leq 1)$
- "make leakage phases last for at most one time unit"
- Des-2 := $\Box(L; \neg L; L) \implies \ell > 30$
- "ensure non-leakage phases between two leakage phases last at ea

(iv) Prove correctness, i.e. prove $\models (\text{Des-1} \wedge \text{Des-2} \implies \text{Req})$.

(Or do we want " $\models_{\text{no}} \dots$ ")

Lemma 2.17

Claim:

$$\models \Box(\ell \leq 30 \implies f \cdot L \leq 1) \implies \Box(\ell \geq 60 \implies 20 \cdot f \cdot L$$

Proof:

- Assume that Req-1 holds.
- Let L_I be any interpretation of L , and $[b, e]$ an interval with $e - b \geq$
- We need to show that

$$20 \cdot f \cdot L \leq \ell$$

evaluates to 'tt' on interval $[b, e]$ under interpretation \mathcal{I} and any \mathbf{vr}

- We have

$$\int_b^e 20 \cdot f \cdot L \leq \int_b^e \ell \quad \mathbf{by} \text{ (i)}$$

\iff (by DC semantics)

$$20 \cdot \int_b^e L_I(t) dt \leq (e - b)$$

Gas Burner Revisited



gas valve

flame

input

output

- (i) Choose observables:
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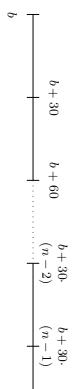
- Des-1 := $\Box(L \implies \ell \leq 1)$
- "make leakage phases last for at most one time unit"
- Des-2 := $\Box(L; \neg L; L) \implies \ell > 30$



Lemma 2.17 Cont'd

Set $n := \lfloor \frac{e-b}{30} \rfloor$, i.e. $n \in \mathbb{N}$ with $n - 1 < \frac{e-b}{30} \leq n$,

and split the interval as follows:



$$20 \int_b^e L_I(t) dt$$

$$= 20 \left(\sum_{k=0}^{n-2} \int_{b+30k}^{b+30(k+1)} L_I(t) dt + \int_{b+30(n-1)}^e L_I(t) dt \right)$$

$$\stackrel{[Req-1]}{\leq} 20 \sum_{k=0}^{n-2} 1 + 20 \cdot 1 = 20 \cdot n$$

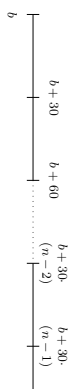
$$\{(*)\} < 20 \cdot \left(\frac{e-b}{30} + 1 \right) = \frac{2}{3}(e-b) + 20$$

$$\{e-b \geq 60\} \leq e-b$$

Lemma 2.17 Cont'd

Set $n := \lfloor \frac{e-b}{30} \rfloor$, i.e. $n \in \mathbb{N}$ with $n - 1 < \frac{e-b}{30} \leq n$,

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$$\stackrel{[Req-1]}{\leq} 20 \sum_{k=0}^{n-2} 1 + (20 \cdot 1)$$

$$\leq e-b$$

Lemma 2.19

$$\begin{aligned} \text{(i)} & \models fP \leq \ell, & \text{(ii)} & \models \neg fP \\ \text{(iii)} & \models (fP = r_1) \vee (fP = r_2) & \text{(iv)} & \models \neg fP = 0. \end{aligned}$$

Claim: $\models \underbrace{(\Box[D] \Rightarrow r \leq D) \wedge \underbrace{\Box[D] : \neg \Delta : [D] \Rightarrow f > 300}}_{\text{Des-2}} \Rightarrow \underbrace{\Box(\ell \leq \cdot)}_{\text{Des-1}}$

Proof: $\ell \leq 300$
 $\forall [D] : (\neg \vee \neg \Delta)$
 $\forall \neg \Delta : (\neg \vee [D])$
 $\forall \neg \Delta : [D] : \neg \Delta$
 $\ell = 23$

Some Laws of the DC Integral Operator

Theorem 2.18.

For all state assertions P and all real numbers $r_1, r_2 \in \mathbb{R}$,

- (i) $\models fP \leq \ell,$
- (ii) $\models (fP = r_1) ; (fP = r_2) \Rightarrow (fP = (r_1 + r_2))$
- (iii) $\models \neg fP \Rightarrow fP = 0,$
- (iv) $\models \neg \cdot \Rightarrow fP = 0.$

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 $\forall \neg \Delta : [D] : \neg \Delta$
 $\{ \text{Des-1} \} \Rightarrow \neg$
 $\vee (\ell \leq D) ; (\neg \vee \neg \Delta)$
 $\vee \neg \Delta : (\neg \vee (\ell \leq D))$
 $\vee \neg \Delta : (\ell \leq D) ; \neg \Delta$
 $\{ \text{(i)} \} \Rightarrow \neg$
 $\vee (fL \leq D) ; (\neg \vee \neg \Delta)$
 $\vee \neg \Delta : (\neg \vee (fL \leq D))$
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- **Semantics-based Correctness Proofs**
 - Example: Gas Burner Controller
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 - **different observables**
 - **proving correctness** may be difficult
- If time permits:
A Calculus for DC

Lemma 2.19

$\{0\} \models J P \leq t$	$\{00\} \models \neg P$
$\{00\} \models (J P = t) \wedge (J P = \tau)$	
$\{00\} \models \perp \implies J P = 0$	

Claim: $\models (\underbrace{(\perp \mid D)}_{\text{Des-1}} \implies \underbrace{t \leq 1} \wedge \underbrace{(\perp \mid D) : \neg L : \mid D}_{\text{Des-2}} \implies t > 300) \implies \perp \{Q \leq t\}$

Proof:

$\{ \text{Des-2} \} \implies \perp$ $\forall \mid D : (\perp \vee \neg L)$ $\forall \neg L : (\perp \vee \mid D)$ $\forall \neg L : \mid D : \neg L$ $\{ \text{Des-1} \} \implies \perp$ $\forall (t \leq 1) : (\perp \vee \neg L)$ $\forall \neg L : (\perp \vee (t \leq 1))$ $\forall \neg L : (t \leq 1) : \neg L$ $\{0\} \implies \perp$ $\forall (J L \leq 1) : (\perp \vee \neg L)$ $\forall \neg L : (\perp \vee (J L \leq 1))$ $\forall \neg L : (J L \leq 1) : \neg L$	$\{00\} \implies J L = 0$ $\forall (J L \leq 1) : J L$ $\forall J L = 0 : J L = 0$ $\{00\} \implies J L = 0$ $\forall J L \leq 1 + 0$ $\forall J L = 0 + 1$ $\forall J L \leq 0 + 1 + 1$ $\implies J L \leq 1$
---	--

Methodology: The World is Not Ideal...

- (i) Choose a collection of **observables** Obs;
- (ii) Provide specification Req (conjunction of DC formulae over C
- (iii) Provide a description Ctrl of the **controller** DC formula over C
- (iv) Prove Ctrl **correct** (wrt Req), i.e. prove $\models \text{Ctrl} \implies \text{Req}$.

That looks **too simple to be practical**.

Typical obstacles:

- (i) It may be **impossible** to realise Req if it doesn't consider properties of the plant
- (ii) There are typically **intermediate design levels** between Req and Ctrl
- (iii) Req and Ctrl may use **different observables**.
- (iv) Proving validity of the implication is **not trivial**.

(ii) Intermediate Design Levels

- A **top-down development approach** may involve
 - Req – **specification/requirements**
 - Des – **design**
 - Ctrl – **implementation**
- Then **correctness** is established by proving validity of

$$\text{Ctrl} \implies \text{Des}$$

and

$\text{Des} \implies \text{Req}$
(and then concluding Ctrl \implies Req by transitivity).

- Any preference on the order (of (i) and (2))?

(i) Assumptions As A Form of Plant Model

- Often the controller will (or can) operate correctly only under some
- For instance, with a **level crossing**
 - we may assume an **upper bound on the speed of approaching t** (otherwise we'd need to close the gates arbitrarily fast)
 - we may assume that trains are **not arbitrarily slow** in the crossing (otherwise we can't make promises to the road traffic)
- We shall **specify such assumptions** as a DC formula Asm on the **input observables** and verify correctness of Ctrl wrt Req by proving validity (from 0

$$\text{Ctrl} \wedge \text{Asm} \implies \text{Req}$$
- Shall we **care** whether Asm is satisfiable?

Obstacles in Non-Ideal World

Obstacle (iv): How to Prove Correctness?

Main options:

- by hand on the basis of DC semantics (as demonstrated before),
- using proof rules from a calculus (\rightarrow later),
- sometimes a general theorem may fit (e.g. cycle times of PLC auto algorithms as in Uppaal (\rightarrow later))

References

(iii): Different Observables

- Assume, 'Req' uses **more abstract observables** Obs_A and 'Ctrl' **more concrete observables** Obs_C:

For instance:

- In Obs_A: only consider **one gas valve, open or closed** - $G_1 : \{0, 1\}$
 - In Obs_C: may consider **two valves and intermediate positions**, for instance, to react to different heating requests - $G_1 : \{0, 1, 2\}$
 - **To prove correctness**,
 - we need information **how the observables are related**,
 - an **invariant** which links the data values of Obs_A and Obs_C.
- If we're given the linking invariant as a DC formula, say 'Link_{C,A}', the correctness of 'Ctrl' wrt. 'Req' amounts to proving

$$\models_0 \text{Ctrl} \wedge \text{Link}_{C,A} \implies \text{Req}$$

- For instance, $\text{Link}_{C,A} := \prod \forall [G \iff (G_1 > 0 \vee G_2 > 0)]$.

Tell Them What You've Told Them...

- **Design ideas** for the behaviour of real-time system control can also be described using DC formulae.
- The **correctness** of a design idea wrt. requirements can **judiciously be proven** "on foot" (using the DC semantics and analysis results).
- This approach is not limited to over-simplified (?) **gas burner** controllers:
- Consider **plant assumptions**.
- Use **intermediate designs** in a step-by-step development
- Link **different observables** by invariants
- Consider other **proof techniques**.

References

Olaeag, E.-R. and Dierks, H. (2008). *Real-Time Systems - Formal Specification and Autor* Cambridge University Press.