

Content

- **RDC** ( $\ell = \exists X.Y.F$ ) in Continuous Time
  - ↳ Outline of the proof
  - ↳ Recall: two-counter machines (2-CM)
  - ↳ states and commands (syntax)
  - ↳ configurations and computations (semantics)
  - ↳ Encoding configurations in DC
  - ↳ Initial configuration of a 2-CM
  - ↳ Encoding transitions in DC
  - ↳ increment counter
  - ↳ decrement counter
  - ↳ and some integer formulae
  - ↳ **Satisfiability and Validity**
  - ↳ Discussion

Decidability Results for Realisability: Overview

Fragment	Discrete Time	Continuous Time
RDC	decidable ✓	decidable
$RDC + \ell = r$	decidable for $r \in \mathbb{N}$	undecidable for $r \in \mathbb{R}^+$
$RDC + \ell \wedge F_1 = \ell \wedge F_2$	undecidable	undecidable
$RDC + \ell = x.Yx$	undecidable	undecidable ⚠
DC	— " —	— " —

Recall: Restricted DC (RDC)

$$F ::= [P] \mid \neg F_1 \mid F_1 \vee F_2 \mid F_1 \wedge F_2$$

where  $P$  is a state assertion with boolean observables only.

From now on, "RDC" =  $\ell = x.Yx$

$$F ::= [P] \mid \neg F_1 \mid F_1 \vee F_2 \mid F_1 \wedge F_2 \mid \ell = 1 \mid \ell = x \mid \forall x.X \mid \bullet F_1$$

Undecidability of Satisfiability/Realisability from 0

**Theorem 3.10.**  
The realisability from 0 problem for DC with continuous time is undecidable, not even semi-decidable.

**Theorem 3.11.**  
The satisfiability problem for DC with continuous time is undecidable.

Decidability Results for RDC in Continuous Time

Sketch: Proof of Theorem 3.10

- Reduce divergence of two-counter machines to realizability from O.
- Given a two-counter machine  $M$  with final state  $q_{fin}$ .
- construct a DC formula  $F(M) := encoding(M)$
- such that

$$M \text{ diverges} \iff \text{if and only if the DC formula}$$

$$F(M) \wedge \neg \Delta [q_{fin}]$$

is realizable from O.

- If realizability from O was (semi-)decidable, divergence of two-counter machines would be (which it isn't)

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Two-Counter Machines

2CM Configurations and Computations

- a configuration of  $M$  is a triple  $K = (q, n_1, n_2) \in \mathbb{Q} \times \mathbb{N}_0 \times \mathbb{N}_0$ .
- The transition relation  $\tau$ , on configurations is defined as follows:

Command	Semantics $K' \vdash K''$
$q : inc_1 : q'$	$(q, n_1, n_2) \vdash (q', n_1 + 1, n_2)$
$q : dec_1 : q'$	$(q, 0, n_2) \vdash (q', 0, n_2)$ $(q, n_1 + 1, n_2) \vdash (q', n_1, n_2)$
$q : inc_2 : q'$	$(q, n_1, n_2) \vdash (q', n_1, n_2 + 1)$
$q : dec_2 : q'$	$(q, n_1, 0) \vdash (q', n_1, 0)$ $(q, n_1, n_2 + 1) \vdash (q', n_1, n_2)$

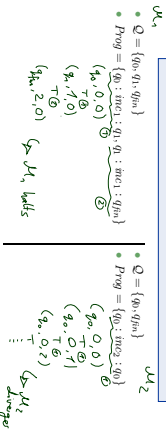
- The () computation of  $M$  is a finite sequence of the form  $K_0 = (q_0, 0, 0) \vdash K_1 \vdash K_2 \vdash \dots \vdash (q_{fin}, n_1, n_2)$  or an infinite sequence of the form  $K_0 = (q_0, 0, 0) \vdash K_1 \vdash K_2 \vdash \dots$

[“M halts”]  
[“M diverges”]

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2CM Example

$M = \{Q, q_0, q_{fin}, Prog\}$	
commands of the form $q : inc_i : q'$ and $q : dec_i : q'$ , $i \in \{1, 2\}$	
configuration $K = (q, n_1, n_2) \in \mathbb{Q} \times \mathbb{N}_0 \times \mathbb{N}_0$	
Command	Semantics $K' \vdash K''$
$q : inc_1 : q'$	$(q, n_1, n_2) \vdash (q', n_1 + 1, n_2)$
$q : dec_1 : q'$	$(q, 0, n_2) \vdash (q', 0, n_2)$ $(q, n_1 + 1, n_2) \vdash (q', n_1, n_2)$
$q : inc_2 : q'$	$(q, n_1, n_2) \vdash (q', n_1, n_2 + 1)$
$q : dec_2 : q'$	$(q, n_1, 0) \vdash (q', n_1, 0)$ $(q, n_1, n_2 + 1) \vdash (q', n_1, n_2)$



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Recall: Two-counter machines

A two-counter machine is a structure  $M = (Q, q_0, q_{fin}, Prog)$

- where
- $Q$  is a finite set of states,
- comprising the initial state  $q_0$  and the final state  $q_{fin}$ .
- $Prog$  is the machine program, i.e. a finite set of commands of the form

$$q : inc_i : q' \quad \text{and} \quad q : dec_i : q', q'' \quad i \in \{1, 2\}$$

$$q' : x_i := x_i + 1, \quad q'' : x_i := x_i - 1, \quad q'' : x_i = 0$$

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$$q' : x_i := x_i + 1, \quad q'' : x_i = 0$$

- We assume deterministic 2CM, for each  $q \in Q$  at most one command starts in  $q$ , and  $q_{fin}$  is the only state where no command starts.

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Reduction to 2-CM: Idea

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2CM,  $\mathcal{M}$  diverges

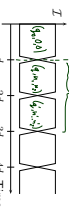
iff

exists  $\pi : K_0 \vdash K_1 \vdash \dots$

iff

- $F(\mathcal{M})$  inductively specifies:
  - $[0, d]$  encodes  $(q_0, 0, 0)$ ,
  - each  $[n \cdot d, (n+1) \cdot d]$  encodes a configuration,
  - $[n \cdot d, (n+1) \cdot d]$  and  $[(n+1) \cdot d, (n+2) \cdot d]$  are in  $\vdash$ -relation,
  - if  $q_m$  is reached, we stay there

Encoding Configurations



" $\mathcal{Z}$  describes  $\pi$ "

$\mathcal{I} \models_{\mathcal{Q}} F(\mathcal{M}) \wedge \neg \Diamond [q_m]$

$\cdot (q, a, z)$

- A single configuration  $K$  of  $\mathcal{M}$  can be encoded in an interval of length  $d$ : being an encoding interval can be characterised by a DC formula.

- An interpretation on Time encodes the computation of  $\mathcal{M}$  if

- each interval  $[3n, 4(n+1)]$ ,  $n \in \mathbb{N}_0$ , encodes a configuration  $K_n$ ,

- each two subsequent intervals

$$[3n, 4(n+1)] \text{ and } [4(n+1), 4(n+2)], n \in \mathbb{N}_0$$

- encode configurations  $K_n \vdash K_{n+1}$  in transition relation,

- Being an encoding of the run can be characterised by a DC formula  $F(\mathcal{M})$ .

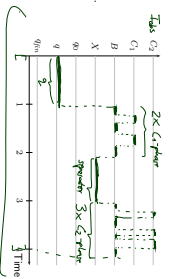
- Then,  $\mathcal{M}$  diverges iff and only if  $F(\mathcal{M}) \wedge \neg \Diamond [q_m]$  is realisable from  $\mathbf{0}$ .

Encoding Configurations

• We use Obs = {obs} with

$\mathcal{D}(\text{obs}) = \mathcal{Q}_{\mathcal{M}} \cup \{C_1, C_2, B, X\}$

Asynchronous



Examples

•  $K = (q, 2, 3)$

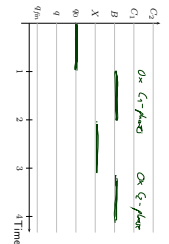
$$\left( \begin{array}{c} [a] \\ \Delta \\ [x] \\ \Delta \\ [b] \end{array} \right)_{t=1} : \left( [B] : [C_1] : [B] : [C_1] : [B] \right)_{t=1} : \left( [X] \right)_{t=1} : \left( [B] : [C_1] : [B] : [C_1] : [B] : [C_1] : [B] \right)_{t=1}$$

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$$\left( \begin{array}{c} [a] \\ \Delta \\ [x] \\ \Delta \\ [b] \end{array} \right)_{t=1} : \left( [B] : [C_1] : [B] : [C_1] : [B] \right)_{t=1} : \left( [X] \right)_{t=1} : \left( [B] : [C_1] : [B] : [C_1] : [B] : [C_1] : [B] \right)_{t=1}$$

- $K_0 = (q_0, 0, 0)$

$$\left( \begin{array}{c} [a] \\ \Delta \\ [x] \\ \Delta \\ [b] \end{array} \right)_{t=1} : \left( [B] \right)_{t=1} : \left( [X] \right)_{t=1} : \left( [B] \right)_{t=1}$$

or using abbreviations,  $[a]^1 : [B]^1 : [X]^1 : [B]^1$

Formula Construction for Given 2-CM

Construction of  $F(\mathcal{M})$

In the following, we give DC formulae describing

- the initial configuration:  $init$ ,
- the general form of configurations:  $keep$ ,
- the transitions between configurations:  $F(q, true, q')$  and  $F(q, dec, q')$ ,
- the handling of the final state.

$F(\mathcal{M})$  is the conjunction of all these formulae:

$$F(\mathcal{M}) = init \wedge keep \wedge \dots$$

$$\wedge \bigwedge_{q, true, q' \in \text{Prog}} F(q, true, q')$$

$$\wedge \bigwedge_{q, dec, q' \in \text{Prog}} F(q, dec, q')$$

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$q : inc_1 : q'$  (Increment)

(0) Change state

$$\Box([q] \wedge [B \vee C_1] \wedge [X] \wedge [B \vee C_2] \wedge t = 4 \implies t = 4; [q'] \wedge true)$$

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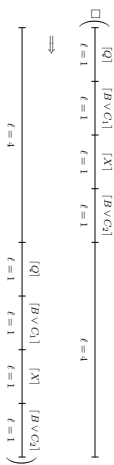
Initial and General Configurations

$$init := (t \geq 4 \implies [a] \wedge [B] \wedge [X] \wedge [B] \wedge true)$$

$$keep := \Box([q] \wedge [B \vee C_1] \wedge [X] \wedge [B \vee C_2] \wedge t = 4)$$

$$\implies Q = 4; [q] \wedge [B \vee C_1] \wedge [X] \wedge [B \vee C_2]$$

where  $Q := \neg(X \vee C_1 \vee C_2 \vee B)$ .

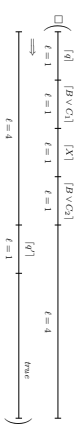


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$q : inc_1 : q'$  (Increment)

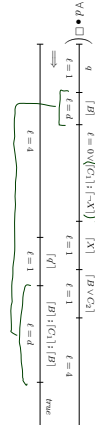
(0) Change state

$$\Box([q] \wedge [B \vee C_1] \wedge [X] \wedge [B \vee C_2] \wedge t = 4 \implies t = 4; [q'] \wedge true)$$



(0) Increment counter

$$\forall d \bullet \Box([q] \wedge [B] \wedge t = 0 \vee [C_1] \wedge \neg [X] \wedge [X] \wedge [B \vee C_2] \wedge t = 4 \implies t = 4; [q'] \wedge ([B] \wedge t = d) \wedge true)$$



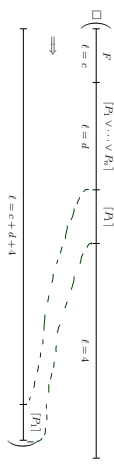
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Auxiliary Formula Pattern copy

$$\text{copy}(F, (P_1, \dots, P_n)) := \Box(F \wedge t = 0) \wedge (P_1 \vee \dots \vee P_n) \wedge t = 0; [P_1] \wedge t = 4$$

$$\forall c, d \bullet \Box(F \wedge t = 0) \wedge (P_1 \vee \dots \vee P_n) \wedge t = 0; [P_1] \wedge t = 4$$

$$\implies (t = c + d + 4; [P_1])$$



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$q : inc_1 : q'$  (Increment)

(0) Keep rest of first counter

$$\text{copy}([q] \wedge [B \vee C_1] \wedge [C_1] \wedge [B, C_1])$$

(0) Leave second counter unchanged

$$\text{copy}([q] \wedge [B \vee C_1] \wedge [X] \wedge [B, C_2])$$

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$q : dec_C : q', q' \text{ (Decrement)}$

(i) If zero  $\square([q] : \{B\}^+ ; \{X\}^+ ; \{B \vee C\}^+ ; t = 4 \Rightarrow t = 4; [q']^+ ; \{B\}^+ ; t = 0)$



(ii) Decrement counter  $\forall d \bullet \square([q] : \{B\}^+ ; \{C\}^+ \wedge t = d; \{B\}^+ ; \{B \vee C\}^+ ; \{X\}^+ ; \{B \vee C\}^+ ; t = 4 \Rightarrow t = 4; [q']^+ ; \{B\}^+ ; t = 0)$

(iii) Keep rest of first counter  $copy([q] : \{B\}^+ ; \{C\}^+ ; \{B, C\}^+)$

(iv) Leave second counter unchanged  $copy([q] : \{B \vee C\}^+ ; \{X\}^+ ; \{B, C\}^+)$

Final State

$copy([q_m] : \{B \vee C\}^+ ; \{X\}^+ ; \{B \vee C\}^+ ; [q_m, B, X, C_1, C_2])$

*M diverges*  
 $\neg(F(x) \wedge \neg \exists y. F(y))$   
 is not derivable from 0

Satisfiability

- Following Chaochen and Hansen (2004) we can observe that  $M$  halts if and only if the DC formula  $F(x) \wedge \exists! [q_m]$  is satisfiable. This yields

**Theorem 3.11.** The satisfiability problem for DC with continuous time is undecidable. (It is semi-decidable.)

- Furthermore, by taking the contraposition, we see  $M$  diverges if and only if  $M$  does not halt if and only if  $\neg(F(x) \wedge \exists! [q_m])$  is not satisfiable.
- Thus whether a DC formula is not satisfiable is not decidable, not even semi-decidable.

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Validity

- By Remark 2.13,  $F$  is valid iff  $\neg F$  is not satisfiable, so

**Corollary 3.12.** The validity problem for DC with continuous time is undecidable, not even semi-decidable.

- This provides us with an alternative proof of Theorem 2.23 (There is no sound and complete proof system for DC).
- Suppose there were such a calculus  $C$ .
- By Lemma 2.22 it is semi-decidable whether a given DC formula  $F$  is a theorem in  $C$ .
- By the soundness and completeness of  $C$ ,  $F$  is a theorem in  $C$  if and only if  $F$  is valid.
- Thus it is semi-decidable whether  $F$  is valid. **Contradiction.**

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Satisfiability / Validity

Discussion

- Note the DC fragment defined by the following grammar is sufficient for the reduction  $F ::= [P] \mid \neg F_1 \mid F_1 \vee F_2 \mid F_1 ; F_2 \mid \ell = 1 \mid \ell = x \mid \forall x \bullet F_1$ ,  $P$  a state assertion,  $x$  a global variable.
- Formulae used in the reduction are abbreviations:  $\ell = 4 \iff \ell = 1; \ell = 1; \ell = 1; \ell = 1$   $\ell \geq 4 \iff \ell = 4; true$   $\ell = x + y + 4 \iff \ell = x; \ell = y; \ell = 4$
- Length 1 is not necessary – we can use  $\ell = z$  instead, with fresh  $z$ .
- This is RDC augmented by “ $\ell = x$ ” and “ $\forall x, x$ ” which we denote by RDC +  $\ell = x; \forall x$ .

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- RDC  $\{f = x, Vx\}$  in Continuous Time
  - ↳ Outline of the proof
  - ↳ Recall: two-counter machines (2-CM)
  - ↳ states and demands (syntax)
  - ↳ configurations and configurations (semantics)
  - ↳ Encoding configurations in DC
  - ↳ Initial configuration of a 2-CM
  - ↳ Encoding transitions in DC
  - ↳ Invariant counter
  - ↳ decrement counter
  - ↳ and some helper formulae
- **Satisfiability and Validity**
- **Discussion**

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Tell Them What You've Told Them...

- For Restricted DC plus  $f = x$  and  $Vx$  in continuous time:
  - satisfiability is undecidable
  - Proof Idea: reduce to halting problem of two-counter machines
  - For full DC: it doesn't get better.

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Content

- Introduction
  - Observables and Evolving
  - Duration Calculus (DC)
  - Semantical Correspondence Proofs
  - DC Decidability
  - DC Implementables
  - PLC Automata
- **Time  $\rightarrow \mathcal{P}(Obs)$** 
  - **Timed Automata (TA)** Uppaal
  - Networks of Timed Automata
  - Region/Zone Abstraction
  - TA model-checking
  - Extended Timed Automata
  - Undecidability Results
- **Automatic Verification**
  - whether a TA satisfies a DC formula, observer-based
  - Recent Results
  - Timed Sequence Diagrams or Quasi-equal Clocks, or Automatic Code Generation, or ...

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References

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Chaochen, Z. and Hansen, M. R. (2004). Duration Calculus: A Formal Approach to Real-Time Systems. Monograph in Theoretical Computer Science. Springer-Verlag. An EATCS Series.

Ollinger, E.-R. and Dierks, H. (2008). Real-Time Systems - Formal Specification and Automatic Verification. Cambridge University Press.

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