

# Real-Time Systems

## Lecture 13: Location Reachability

(or: The Region Automaton)

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Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

### Content

- Introduction
- Observables and Evolutions
- Duration Calculus (DC)
- Semantical Correctness Proofs
- DC Decidability
- DC Implementables
- PLC Automata
- Automatic Verification... whether a TA satisfies a DC formula, observer-based
- Recent Results:
  - Timed Sequence Diagrams or Quasi-equal Clocks, or Automatic Code Generation, or ...
- Timed Automata (TA), Uppaal ✓
- Networks of Timed Automata ✓
- Model/Zone-Abstraction ✓
- TA model-checking
- Extended Timed Automata } 24
- Undecidability Results
- obs. : Time  $\rightarrow \mathcal{D}(obs)$
- $(obs_0, t_0) \xrightarrow{\Delta_0} (obs_1, t_1), \dots$

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### Content

- The Location Reachability Problem
  - ... is decidable for TA
  - Normalized Constants
  - Time Abstract Transition System
  - Regions
  - Equivalence Classes of Clock Valuations
  - The Region Automaton
    - ... is finite
    - ... and effectively constructible
- The Constraint Reachability Problem
  - ... is decidable as well

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### The Location Reachability Problem

### The Location Reachability Problem

Given: A timed automaton  $A$  and one of its locations  $l$ .  
 Question: Is  $l$  reachable?  
 That is, is there a transition sequence of the form  
 $(l_{in}, t_0) \xrightarrow{\Delta_1} (l_1, t_1) \xrightarrow{\Delta_2} (l_2, t_2) \xrightarrow{\Delta_3} \dots \xrightarrow{\Delta_n} (l_n, t_n)$  with  $l_n = l$   
 in the labelled transition system  $T(A)$ ?

- Note: Decidability is not so obvious, recall that
- clocks range over real numbers, thus infinitely many configurations,
- at each configuration, uncountably many transitions  $\rightarrow$  may originate
- Consequence: The timed automata as we consider them here cannot encode a 2-counter machine, and they are strictly less expressive than DC

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### Decidability of Location Reachability for TA

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### Decidability of The Location Reachability Problem

Claim: (Theorem 4.33)

The location reachability problem is **decidable** for timed automata.



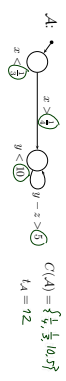
- Approach:** Constructive proof
- Observe clock constraints are simple
    - w.l.o.g. assume constants  $c \in \mathbb{N}_0$ .
  - Def. 4.19: **time-abstract transition system**  $(\mathcal{L}, \mathcal{A})$  – abstracts from uncountably many delay transitions, still infinite-state.
  - Lemma 4.20: location reachability of  $\mathcal{A}$  is preserved in  $(\mathcal{L}, \mathcal{A})$ .
  - Def. 4.29: **region automaton**  $\mathcal{R}(\mathcal{A})$  – equivalent configurations collapse into regions
  - Lemma 4.32: location reachability of  $(\mathcal{L}, \mathcal{A})$  is preserved in  $\mathcal{R}(\mathcal{A})$ .
  - Lemma 4.28:  $\mathcal{R}(\mathcal{A})$  is finite.

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### Without Loss of Generality: Natural Constants

**Recall:**  $\varphi ::= x \sim c \mid x - y \sim c \mid \varphi \wedge \varphi, x, y \in X, c \in \mathbb{Q}_0^+, \text{ and } \sim \in \{<, >, \leq, \geq\}$ .

- Let  $C(\mathcal{A}) = \{c \in \mathbb{Q}_0^+ \mid c \text{ appears in } \mathcal{A}\}$  –  $C(\mathcal{A})$  is finite (Why?)
- Let  $\iota_A$  be the **least common multiple** of the denominators in  $C(\mathcal{A})$ .
- Let  $\{\iota_A \cdot \mathcal{A}\}$  be the TA obtained from  $\mathcal{A}$  by multiplying all constants by  $\iota_A$ .

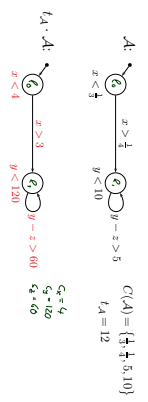


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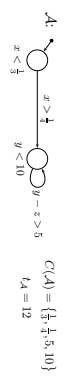


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- Approach:** Constructive proof
- Observe: clock constraints are simple
    - w.l.o.g. assume constants  $c \in \mathbb{N}_0$ .
  - Def. 4.19: **time-abstract transition system**  $(\mathcal{L}, \mathcal{A})$  – abstracts from uncountably many delay transitions, still infinite-state.
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**Recall:**  $\varphi ::= x \sim c \mid x - y \sim c \mid \varphi \wedge \varphi, x, y \in X, c \in \mathbb{Q}_0^+, \text{ and } \sim \in \{<, >, \leq, \geq\}$ .

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- Let  $\iota_A$  be the **least common multiple** of the denominators in  $C(\mathcal{A})$ .
- Let  $\{\iota_A \cdot \mathcal{A}\}$  be the TA obtained from  $\mathcal{A}$  by multiplying all constants by  $\iota_A$ .
- Then:
  - $C(\iota_A \cdot \mathcal{A}) \subset \mathbb{N}_0$ .
  - Allocation  $f$  is reachable in  $\iota_A \cdot \mathcal{A}$  if and only if  $f$  is reachable in  $\mathcal{A}$ .
- That is:** we can, **without loss of generality**, in the following consider only timed automata  $\mathcal{A}$  with  $C(\mathcal{A}) \subset \mathbb{N}_0$ .

**Definition:** Let  $\sigma$  be a dock of timed automaton  $\mathcal{A}$  with  $C(\mathcal{A}) \subset \mathbb{N}_0$ . We denote by  $c_{\sigma} \in \mathbb{N}_0$  the **largest time constant**  $c$  that appears together with  $x$  in a constraint of  $\mathcal{A}$ .

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Recall:  $T(A) = (Conf(A), Time \cup B_1, \{\xrightarrow{\lambda}\} \lambda \in Time \cup B_1), C_{init}$

- Note: The  $\xrightarrow{\lambda}$  are binary relations on configurations.  $\begin{matrix} C_1 \subseteq C_2 \subseteq C_3 \\ C_1 \subseteq C_2 \subseteq C_3 \\ C_1 \subseteq C_2 \subseteq C_3 \end{matrix}$

Definition: Let  $A$  be a TA. For all  $\langle l_1, v_1 \rangle, \langle l_2, v_2 \rangle \in Conf(A)$ ,

$$\langle l_1, v_1 \rangle \xrightarrow{\lambda_1} \langle l_2, v_2 \rangle$$

if and only if there exists some  $\langle l', v' \rangle \in Conf(A)$  such that

$$\langle l_1, v_1 \rangle \xrightarrow{\lambda_1} \langle l', v' \rangle \text{ and } \langle l', v' \rangle \xrightarrow{\lambda_2} \langle l_2, v_2 \rangle.$$

Remark: The following property of time additivity holds.

$$\forall l_1, l_2 \in Time: \xrightarrow{\lambda_1} \circ \xrightarrow{\lambda_2} = \xrightarrow{\lambda_1 + \lambda_2}$$

Definition 4.19 [Time-abstract transition system]

Let  $A$  be a timed automaton. The time-abstract transition system  $\underline{U}(A)$  is obtained from  $T(A)$  (Def 4.4) by taking

$$U(A) = (Conf(A), B_{init}, \{\xrightarrow{\alpha}\} \alpha \in B_{init}), C_{init}$$

where  $B_{init} \subseteq Conf(A) \times Conf(A)$

is defined as follows: Let  $\langle l, v \rangle, \langle l', v' \rangle \in Conf(A)$  be configurations of  $A$  and  $\alpha \in B_{init}$  an action. Then

$$\langle l, v \rangle \xrightarrow{\alpha} \langle l', v' \rangle$$

if and only if there exists  $t \in Time$  such that

$$\langle l, v \rangle \xrightarrow{t} \langle l', v' \rangle.$$

Location Reachability is preserved in  $U(A)$

Lemma 4.20 For all locations  $l$  of a given timed automaton  $A$ , the following holds:

$l$  is  $(\xrightarrow{\lambda})$ -reachable in  $T(A)$  if and only if  $l$  is  $(\xrightarrow{\alpha})$ -reachable in  $U(A)$ .

Proof:

- " $\Leftarrow$ " easy

" $\Rightarrow$ "  $l$  is reachable in  $T(A)$

$$\langle l_0, v_0 \rangle \xrightarrow{t_1} \langle l_1, v_1 \rangle \xrightarrow{t_2} \dots \xrightarrow{t_n} \langle l, v \rangle$$

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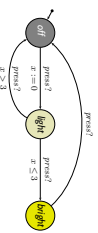
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Example

$$\langle l, v \rangle \xrightarrow{\alpha} \langle l', v' \rangle \text{ iff } \exists t \in Time: \langle l, v \rangle \xrightarrow{t} \langle l', v' \rangle$$



- $\langle left, x = 0 \rangle \xrightarrow{right} \langle left, x = 27 \rangle$  YES with  $t = 27$  we have  $\langle 0 \rangle \xrightarrow{27} \langle 27 \rangle$
- $\langle left, x = 4 \rangle \xrightarrow{right} \langle left, x = 0 \rangle$  YES, any  $t \in \mathbb{R}^+$  works
- $\langle left, x = 4 \rangle \xrightarrow{right} \langle left, x = 1 \rangle$  NO (0, 4)  $\not\xrightarrow{t} (0, 3)$  implies  $t = 0$
- $\langle left, x = 0 \rangle \xrightarrow{right} \langle left, x = 5 \rangle$  NO no  $\alpha \leq (0, 5) \xrightarrow{t} (0, 5)$
- $\langle left, x = 0 \rangle \xrightarrow{right} \langle right, x = 5 \rangle$  NO, needs two actions
- $\langle left, x = 1 \rangle \xrightarrow{right} \langle right, x = 1 \rangle$  YES with  $t = 0$

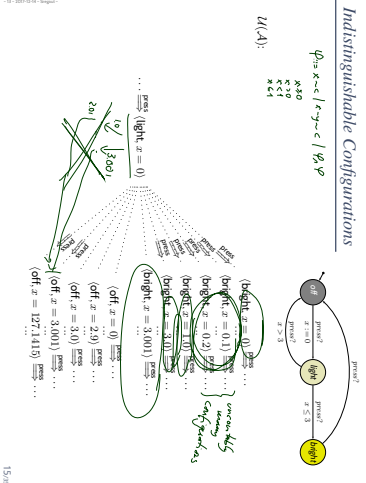
Decidability of The Location Reachability Problem

Claim: (Theorem 4.33)

The location reachability problem is decidable for timed automata.

- Approach: Constructive proof
- Observe: clock constraints are simple
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- Def 4.19: time-abstract transition system  $U(A)$  induces finitely many delay transitions, still infinite-state
- Lemma 4.20: location reachability of  $A$  is preserved in  $U(A)$ .
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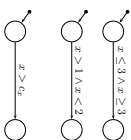
### Indistinguishable Configurations



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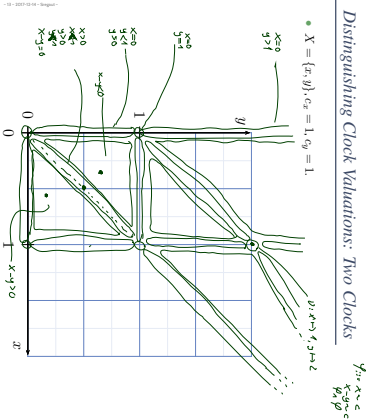
### Distinguishing Clock Valuations: One Clock

- Assume  $A$  with only a single clock, i.e.  $X = \{x\}$  (recall  $C(A) \subset \mathbb{N}$ )
- $A$  could detect for a given  $v_1$  whether  $v_2(x) \in \{0, \dots, c_2\}$ .
- $A$  cannot distinguish  $v_1$  and  $v_2$  if  $v_1(x) \in \{k, k+1\}$ ,  $k \in \mathbb{Z}$  and  $k \in \{0, \dots, c_2 - 1\}$ .
- $A$  cannot distinguish  $v_1$  and  $v_2$  if  $v_1(x) > c_2 - 1 = 1, 2$ .
- If  $c_2 \geq 1$ , there are  $(2c_2 + 2)$  equivalence classes:  $\{(0), (0, 1), \{1\}, (1, 2), \dots, \{c_2\}, (c_2, \infty)\}$



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### Distinguishing Clock Valuations: Two Clocks



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### Helper: Floor and Fraction

- Recall: Each  $q \in \mathbb{R}^+$  can be split into
  - floor  $\lfloor q \rfloor \in \mathbb{N}_0$  and
  - fraction  $frac(q) \in [0, 1)$

$$q = \lfloor q \rfloor + frac(q)$$

$$\lfloor 3.14 \rfloor = 3$$

$$frac(3.14) = 0.14$$

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### An Equivalence-Relation on Valuations

- Definition. Let  $X$  be a set of clocks,  $c_2 \in \mathbb{N}_0$  for each clock  $x \in X$ , and  $v_1, v_2$  clock valuations of  $X$ .
- We set  $v_1 \approx v_2$  if and only if the following four conditions are satisfied:
- For all  $x \in X$ ,  $\lfloor v_1(x) \rfloor = \lfloor v_2(x) \rfloor$  or both  $v_1(x) > c_2$  and  $v_2(x) > c_2$ .
  - For all  $x \in X$  with  $v_1(x) \leq c_2$ ,  $frac(v_1(x)) = 0$  if and only if  $frac(v_2(x)) = 0$ .
  - For all  $x, y \in X$ ,  $\lfloor v_1(x) - v_1(y) \rfloor = \lfloor v_2(x) - v_2(y) \rfloor$  or both  $\lfloor v_1(x) - v_1(y) \rfloor > c_2$  and  $\lfloor v_2(x) - v_2(y) \rfloor > c_2$ .
  - For all  $x, y \in X$  with  $\lfloor v_1(x) - v_1(y) \rfloor \leq c_2$ ,  $frac(v_1(x) - v_1(y)) = 0$  if and only if  $frac(v_2(x) - v_2(y)) = 0$ .
- Where  $c = \max\{c_2, c_3\}$ .

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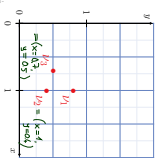
### Example: Regions

- $\forall x \in X \forall v_1(v_2) = \lfloor v_1(x) \rfloor \vee (v_1(x) > c \wedge v_2(x) > c)$
  - $\forall x \in X \forall v_1(v_2) \leq c \implies frac(v_1(x)) = 0 \iff frac(v_2(x)) = 0$
  - $\forall x, y \in X \forall v_1(v_2) = \lfloor v_1(x) - v_1(y) \rfloor = \lfloor v_2(x) - v_2(y) \rfloor$  or both  $\lfloor v_1(x) - v_1(y) \rfloor > c$  and  $\lfloor v_2(x) - v_2(y) \rfloor > c$
  - $\forall x, y \in X \forall v_1(v_2) = \lfloor v_1(x) - v_1(y) \rfloor \leq c \implies frac(v_1(x) - v_1(y)) = 0 \iff frac(v_2(x) - v_2(y)) = 0$
- $v_1 \approx v_2$  because
- $\lfloor v_1(x) \rfloor = \lfloor 1 \rfloor = 1 = \lfloor v_2(x) \rfloor$
  - $\lfloor v_1(y) \rfloor = \lfloor 0.8 \rfloor = 0 = \lfloor v_2(y) \rfloor$
  - $\lfloor v_1(0) \rfloor = \lfloor 0.8 \rfloor = 0 = \lfloor v_2(0) \rfloor$
  - $frac(v_1(x)) = 0 = frac(v_2(x))$
  - $frac(v_1(y)) = frac(0.8) = 0.8 \neq 0$
  - $frac(v_1(0)) = frac(0) = 0.4 \neq 0$
  - $\lfloor v_1(x) - v_1(y) \rfloor = \lfloor 1 - 0.8 \rfloor = 0$
  - $\lfloor v_1(x) - v_1(0) \rfloor = \lfloor 1 - 0.8 \rfloor = 0$
  - $\lfloor v_1(y) - v_1(0) \rfloor = \lfloor 0.8 - 0.4 \rfloor = 0.4 \neq 0$

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### Example: Regions

- (1)  $\forall x \in X \bullet |v_1(x)| = |v_2(x)| \vee (v_1(x) > c_1 \wedge v_2(x) > c_1)$
- (2)  $\forall x \in X \bullet |v_1(x)| \leq c_1 \iff |v_2(x)| \leq c_1 \iff f_{v_1}(v_1(x)) = 0$
- (3)  $\forall x, y \in X \bullet |v_1(x) - v_1(y)| = |v_2(x) - v_2(y)|$   
 $\vee (|v_1(x) - v_1(y)| > c_1 \wedge |v_2(x) - v_2(y)| < c_1)$
- (4)  $\forall x, y \in X \bullet -c_1 \leq v_1(x) - v_1(y) \leq c_1 \iff f_{v_1}(v_1(x) - v_1(y)) = 0$   
 $\iff |f_{v_1}(v_1(x) - v_1(y))| = 0$



- $v_1 \cong v_2$  because
  - $|v_1(x)| = |1| = 1 = |1| = |v_2(x)|$
  - $|v_1(y)| = |0.8| = 0 = |0.4| = |v_2(y)|$
  - $f_{v_1}(v_1(x)) = 0 = f_{v_2}(v_2(x))$
  - $f_{v_1}(v_1(y)) = f_{v_2}(v_2(y)) = 0.8 \neq 0$
  - $f_{v_1}(v_1(0)) = f_{v_2}(v_2(0)) = 0.4 \neq 0$
  - $|v_1(x) - v_1(y)| = |1 - 0.8| = 0$
  - $= |1 - 0.4| = |v_2(x) - v_2(y)|$
- $v_1 \not\cong v_2$  because
  - $|v_1(x)| = |1| = 1 = |1| = |v_2(x)|$
  - $|v_1(y)| = |0.8| = 0 = |0.4| = |v_2(y)|$
  - $|v_1(x) - v_1(y)| = |1 - 0.8| = 0$
  - $\neq |1 - 0.4| = |v_2(x) - v_2(y)|$

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### Regions

Proposition:  $\cong$  is an equivalence relation.

Definition 4.27: For a given evaluation  $v$ , we denote by  $[v]$  the equivalence class of  $v$ . We call the equivalence classes of  $\cong$  regions.

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### The Region Automaton

Definition 4.29 [Region Automaton] The region automaton  $\mathcal{R}(\mathcal{A})$  of the timed automaton  $\mathcal{A}$  is the labelled transition system

$$\mathcal{R}(\mathcal{A}) = (\text{Conj}(\mathcal{R}(\mathcal{A})), B_{in}, \{\xrightarrow{\alpha} R(\mathcal{A}) \mid \alpha \in B_{in}\}, C_{init})$$

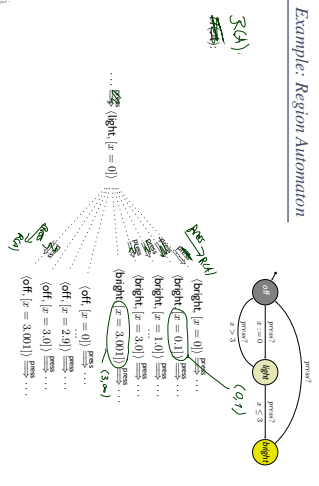
where

- $\text{Conj}(\mathcal{R}(\mathcal{A})) = \{ \langle \ell, [v] \rangle \mid \ell \in L, v: X \rightarrow \text{Time}, v \models \ell(t) \}$
- for each  $\alpha \in B_{in}$ ,
  - $\langle \ell, [v] \rangle \xrightarrow{\alpha} \langle \ell', [v'] \rangle$  if and only if  $\langle \ell, v \rangle \xrightarrow{\alpha} \langle \ell', v' \rangle$  in  $\mathcal{U}(\mathcal{A})$ , and
  - $C_{init} = \{ \langle \ell_{init}, [v_{init}] \rangle \} \cap \text{Conj}(\mathcal{R}(\mathcal{A}))$  with  $v_{init}(X) = \{0\}$ .

Proposition: The transition relation of  $\mathcal{R}(\mathcal{A})$  is well-defined, that is, independent of the choice of the representative  $v$  of a region  $[v]$ .

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### Example: Region Automaton



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### Remark

Remark 4.30 A configuration  $\langle \ell, [v] \rangle$  is reachable in  $\mathcal{R}(\mathcal{A})$  if and only if all  $\langle \ell', v' \rangle$  with  $v' \in [v]$  are reachable. In other words, it is possible to enter the configuration  $\langle \ell, v \rangle$  with an action transition (possibly some delay before). The clock values reachable by staying / letting time pass in  $\ell$  are not explicitly represented by the regions of  $\mathcal{R}(\mathcal{A})$ .

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### Decidability of The Location Reachability Problem

Claim: (Theorem 4.33)

The location reachability problem is decidable for timed automata.

- Approach: Constructive proof
- Observe: clock constants are simple
  - w.l.o.g. assume constants  $c \in \mathbb{N}$ .
- Def. 4.19: time-abstract transition system  $\mathcal{U}(\mathcal{A})$ : abstracts from (possibly many) delay transitions, still infinite-state
- Lemma 4.20: location reachability of  $\mathcal{A}$  is preserved in  $\mathcal{U}(\mathcal{A})$ .
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- Lemma 4.28:  $\mathcal{R}(\mathcal{A})$  is finite.

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**Lemma 4.32 [Correctness]**  
 For all locations  $l$  of a given timed automaton  $\mathcal{A}$ , the following holds:  
 $l$  is reachable in  $l(\mathcal{A})$  if and only if  $l$  is reachable in  $\mathcal{R}(\mathcal{A})$ .

For the proof:

$$\begin{matrix} c \\ \vdots \\ 1 \\ \vdots \\ c \end{matrix} \xrightarrow{\text{}} c' \Rightarrow \begin{matrix} 3d' \\ \vdots \\ 3d' \\ \vdots \\ 3d' \end{matrix} \xrightarrow{\text{}} \begin{matrix} c \\ \vdots \\ c \\ \vdots \\ c \end{matrix}$$

**Definition 4.21 [Bisimulation]** An equivalence relation  $\sim$  on valuations is a (strong) bisimulation if and only if, whenever  
 $v_1 \sim v_2$  and  $(t, v_1) \xrightarrow{a} (t', v'_1)$   
 then there exists  $v'_2$  with  $v'_1 \sim v'_2$  and  $(t, v_2) \xrightarrow{a} (t', v'_2)$ .

**Lemma 4.26 [Bisimulation]**  $\cong$  is a strong bisimulation.

**Claim: (Theorem 4.33)**

The location reachability problem is decidable for timed automata

**Approach:** Constructive proof.

- ✓ Observe clock constraints are simple
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- ✓ **Lemma 4.20:** location reachability of  $\mathcal{A}$  is preserved in  $l(\mathcal{A})$
- ✓ **Def. 4.29:** region automaton  $\mathcal{R}(\mathcal{A})$  – equivalent configurations collapse into regions
- ✓ **Lemma 4.32:** location reachability of  $l(\mathcal{A})$  is preserved in  $\mathcal{R}(\mathcal{A})$
- ✗ **Lemma 4.28:**  $\mathcal{R}(\mathcal{A})$  is finite

**Lemma 4.28** Let  $X$  be a set of clocks,  $c_i \in \mathbb{N}$ , the maximal constant for each  $x \in X$ , and  $c = \max\{c_x \mid x \in X\}$ . Then  
 $(2c + 2)^{|X|} \cdot (4c + 3)^{\frac{1}{2}|X|(|X|-1)}$   
 is an upper bound on the number of regions.

**Proof:** Olderog and Dierks (2008)

$$\text{Conf}(\mathcal{R}(\mathcal{A})) = \underbrace{L \times \prod_{c \in C} \mathcal{U}_c}_{\text{regions}} \cdot \mathcal{D}$$

**Lemma 4.28** Let  $X$  be a set of clocks,  $c_x \in \mathbb{N}$ , the maximal constant for each  $x \in X$ , and  $c = \max\{c_x \mid x \in X\}$ . Then  
 $(2c + 2)^{|X|} \cdot (4c + 3)^{\frac{1}{2}|X|(|X|-1)}$   
 is an upper bound on the number of regions.

**Proof:** Olderog and Dierks (2008)

- **Lemma 4.28** in particular tells us that each timed automaton (in our definition) has **finitely many** regions.
- Note: the upper bound is a worst case / upper bound, not an exact number.

**Claim: (Theorem 4.33)**

The location reachability problem is decidable for timed automata

**Approach:** Constructive proof.

- ✓ Observe clock constraints are simple
- w.l.o.g. assume constants  $c \in \mathbb{N}$ .
- ✓ **Def. 4.19:** time-abstract transition system  $l(\mathcal{A})$  – abstracts from uncountably many delay transitions, still infinite-state
- ✓ **Lemma 4.20:** location reachability of  $\mathcal{A}$  is preserved in  $l(\mathcal{A})$
- ✓ **Def. 4.29:** region automaton  $\mathcal{R}(\mathcal{A})$  – equivalent configurations collapse into regions
- ✓ **Lemma 4.32:** location reachability of  $l(\mathcal{A})$  is preserved in  $\mathcal{R}(\mathcal{A})$
- ✓ **Lemma 4.28:**  $\mathcal{R}(\mathcal{A})$  is finite

- Let  $\mathcal{A} = (L, B, X, E, t_{init})$  be a timed automaton and  $l \in L$  a location.
- $\mathcal{R}(\mathcal{A})$  can be constructed effectively.
  - There are **finitely many** locations in  $L$  (by definition).
  - There are **finitely many** regions by Lemma 4.28.
  - So  $\text{Conf}(\mathcal{R}(\mathcal{A}))$  is **finite** (by construction).
  - It is **decidable** whether there exists a sequence  $(t_{init}, [v_{init}]) \xrightarrow{a_1} R_1(\mathcal{A}) \langle t_1, [v_1] \rangle \xrightarrow{a_2} R_2(\mathcal{A}) \dots \xrightarrow{a_n} R_n(\mathcal{A}) \langle t_n, [v_n] \rangle$  such that  $t_n = l$  (reachability in graphs).

Thus we have just shown:

**Theorem 4.33 [Decidability]**  
 The location reachability problem for timed automata is decidable.

The Constraint Reachability Problem [Lecture 18, 19, 20, 21]

- Given: Timed automaton  $\mathcal{A}$ , one of its locations  $l$ , and a clock constraint  $\varphi$ .
- Question:** Is a configuration  $(l, \nu)$  *reachable* where  $\nu \models \varphi$ , i.e. is there a transition sequence of the form
 
$$(l_{\text{init}}, \nu_{\text{init}}) \xrightarrow{\Delta_1} (l_1, \nu_1) \xrightarrow{\Delta_2} (l_2, \nu_2) \xrightarrow{\Delta_3} \dots \xrightarrow{\Delta_n} (l_n, \nu_n) = (l, \nu)$$
 in the labelled transition system  $T(\mathcal{A})$  with  $\nu \models \varphi$ ?
- Note: we just observed that  $R(\mathcal{A})$  loses some information about the clock valuations that are possible in  $\gamma$  from a region.

**Theorem 4.34**  
The constraint reachability problem for timed automata is decidable.

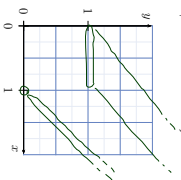
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References

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The Delay Operation

- Let  $|y|$  be a clock region.
- We set  $\text{delay}(|y|) := \{y' + t \mid y' \models y \text{ and } t \in \text{Time}\}$ .



- Note:  $\text{delay}(|y|)$  can be represented as a finite union of regions.
- For example, with our two-clock example we have

$$\text{delay}(|x = y = 0|) = \{(x, y) \in \mathbb{R}^2 \mid x = y = 0\} \cup \{(x, y) \in \mathbb{R}^2 \mid x = y = t\} \cup \dots$$

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References

Olderogge, E.-R. and Dierks, H. (2008). *Real-time Systems - Formal Specification and Automatic Verification*. Cambridge University Press.

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Tell Them What You've Told Them...

- Location Reachability Problem:** is location  $l$  reachable in  $\mathcal{A}^?$
- Decidability proof: [Lecture 18]
- normalise constants.
- construct the Time Abstract Transition System
  - get rid of 'delay transitions'.
  - still *unconcretely* many configurations
- collapse equivalent clock valuations into **regions**
- obtain finitely many abstract configurations
- still *unconcretely* many configurations
- construct the Region Automaton
  - it is finite
  - and preserves location reachability from  $\mathcal{A}$  to  $\mathcal{A}^?$
- Thus: there are chances to get automatic verification for TA.
- Result can easily be lifted to constraint reachability.

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