Software Design, Modelling and Analysis in UML

Lecture Constructive Behaviour, State Machines Overview

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Prof. Dr. Andreas Podelski, Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

Stocktaking...

Have: Means to model the structure of the system.

- Class diagrams graphically, concisely describe sets of system states.
- · OCL expressions logically state constraints/invariants on system states.

Want: Means to model behaviour of the system.

Means to describe how system states evolve over time that is, to describe sets of sequences

 $\sigma_0, \sigma_1, \dots \in \Sigma^{\omega}$

NOT pool-fines clisticle time

of system states.

Contents & Goals

Last Lecture:

Completed discussion of modelling structure.

This Lecture:

- Educational Objectives: Capabilities for following tasks/questions.
- Discuss the style of this class diagram)
- What's the difference between reflective and constructive descriptions of behaviour?
- What's the purpose of a behavioural model?
- What does this State Machine mean? What happens if I inject this event?
- Can you please model the following behaviour.

• Content:

Course Map

- Purposes of Behavioural Models
- Constructive vs. Reflective
- UML Core State Machines (first half)

Modelling Behaviour

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Constructive UML

UML provides two visual formalisms for constructive description of behaviours:

- Activity Diagrams
- State-Machine Diagrams

We (exemplary) focus on State-Machines because

- somehow "practice proven" (in different flavours),
- · prevalent in embedded systems community,
- $\bullet\,$ indicated useful by [Dobing and Parsons, 2006] survey, and
- Activity Diagram's intuition changed (between UML 1.x and 2.x) from transition-system-like to petri-net-like...
- Example state machine:



I .

G = (N, E, f) Mathematics

UML State Machines: Overview

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UML State Machines



Brief History:

- Rooted in Moore/Mealy machines, Transition Systems
- [Harel, 1987]: Statecharts as a concise notation, introduces in particular hierarchical states.
- Manifest in tool Statemate [Harel et al., 1990] (simulation, code-generation); nowadays also in Matlab/Simulink, etc.
- From UML 1.x on: State Machines (not the official name, but understood: UML-Statecharts)
- Late 1990's: tool Rhapsody with code-generation for state machines.

Note: there is a common core, but each dialect interprets some constructs subtly different [Crane and Dingel, 2007]. (Would be too easy otherwise...)

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UML State Machines

Roadmap: Chronologically

- (i) What do we (have to) cover?
- UML State Machine Diagrams Syntax.
- (ii) Def.: Signature with signals.
- (iii) Def.: Core state machine.
- (iv) Map UML State Machine Diagrams to core state machines

The Basic Causality Model

(v) Def.: Ether (aka. event pool)



- (vi) Def.: System configuration. (vii) Def.: Event.
- (viii) Def.: Transformer. (ix) Def.: Transition system, computation.
- (x) Transition relation induced by core state ma-
- (xi) Def.: step, run-to-completion step.
- (xii) Later: Hierarchical state machines.

Roadmap: Chronologically

(i) What do we (have to) cover? UML State Machine Diagrams Syntax (ii) Def.: Signature with signals.

 $r = (\mathcal{T}, \mathcal{C}, V, atr), SM$

(Σ2. A...

- (iii) Def.: Core state machine.
- (iv) Map UML State Machine Diagrams to core state machines

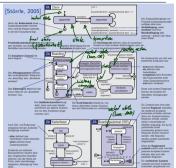
Semantics:

- The Basic Causality Model
- (v) Def.: Ether (aka. event pool)-(vi) Def.: System configuration
- (vii) Def.: Event.
- (viii) Def.: Transformer. (ix) Def.: Transition system, computation.
- (x) Transition relation induced by core state machine.
- (xi) Def.: step. run-to-completion step.
- (xii) Later: Hierarchical state machines.

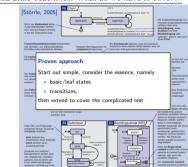
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UML State Machines: Syntax

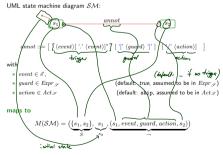
UML State-Machines: What do we have to cover?



UML State-Machines: What do we have to cover?



From UML to Core State Machines: By Example

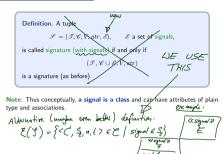


Core State Machine dispoint mia: - should not already be in E (otherwise rename first) Definition. A core state made e over signature $\mathscr{S} = (\mathscr{T},\mathscr{C},V,atr,\mathscr{E})$ is a tuple $M = (S, s_0, \rightarrow)$ where / i-empty, finite set of (basic) states, • S is a not and

We assume a set $Expr_{\mathscr{S}}$ of boolean expressions over \mathscr{S} (for instance OCL, may be something else) and a set $Act_{\mathscr{S}}$ of actions.

is a labelled transition relation.

Signature With Signals



Annotations and Defaults in the Standard

Reconsider the syntax of transition annotations:

```
annot ::= [ [\langle event \rangle [ `.' \langle event \rangle]^*] [ `[' \langle guard \rangle `]' ] [ `/' [\langle action \rangle]] ]
                                                              (81)—
and let's play a bit with the defaults:
                                         ~ (s1, -, true, slip, s2)
~ (s1. -, true, slip, s2)
          (emply dunot:)
                              E / ~ (s, E, tru, stip 152)

/ act ~ (s, _, tru, act, s)
                              E / act - (s1, E, +ne, act, s2)
                        E[e]/act (s,, E, e, act, s2)
```

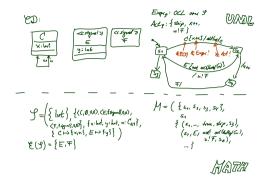
In the standard, the syntax is even more elaborate: (us doil discuss flost)

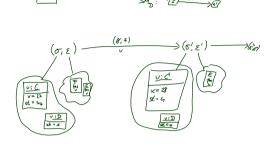
- \bullet E(v) when consuming E in object u, attribute v of u is assigned the corresponding attribute of $E \in \mathcal{E}$
- $E(v:\tau)$ similar, but v is a local variable, scope is the transition

we view as an abbrev.

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References

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References

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[Harel et al., 1990] Harel, D., Lachover, H., et al. (1990). Statemate: A working environment for the development of complex reactive systems. IEEE Transactions on Software Engineering, 16(4):403-414.

[OMG, 2007a] OMG (2007a). Unified modeling language: Infrastructure, version 2.1.2. Technical Report formal/07-11-04.

[OMG, 2007b] OMG (2007b). Unified modeling language: Superstructure, version 2.1.2. Technical Report formal/07-11-02.

State-Machines belong to Classes

- \bullet In the following, we assume that a UML models consists of a set \mathscr{CD} of class diagrams and a set ${\mathscr {SM}}$ of state chart diagrams (each comprising one state machines SM)
- \bullet Furthermore, we assume each that each state machine $\mathcal{SM}\in\mathscr{SM}$ is associated with a class $C_{SM} \in \mathcal{C}(\mathcal{S}) \setminus \mathcal{E}(\mathcal{G})$
- associated with a class $C_{SM} \in v(\mathcal{F}) \setminus CO_J$ For simplicity, we even assume a bijection, i.e. we assume that each class $C \in \mathscr{C}(\mathcal{F})$ has a state machine SM_C and that its class C_{SM_C} is C. If not explicitly given, then this one:

$$SM_0 := (\{s_0\}, s_0, (s_0, \bot, true, skip, s_0)).$$

We'll see later that, semantically, this choice does no harm.

• Intuition 1: \mathcal{SM}_C describes the behaviour of the instances of class C. Intuition 2: Each instance of class C executes, SMc.

Note: we don't consider multiple state machines per class. Because later (when we have AND-states) we'll see that this case can be viewed as

a single state machine with as many AND-states.