

# Real-Time Systems

## Lecture 7: DC Properties II

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### Contents & Goals

#### Last Lecture:

- RDC in discrete time
- Stated: Satisfiability and realizability from 0 is decidable for RDC in discrete time

#### This Lecture:

- Educational Objectives: Capabilities for following tasks/questions
- Facts: (un)decidability properties of DC in discrete/continuous time.
  - What's the idea of the considered (un)decidability proofs?
- Content:
  - Complete: Satisfiability and realizability from 0 is decidable for RDC in discrete time
  - Undecidable problems of DC in continuous time

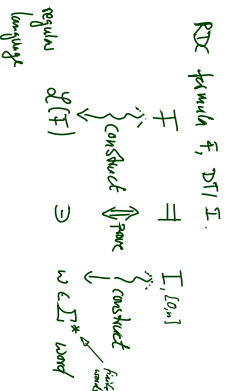
### RDC in Discrete Time Cont'd

### Recall: Decidability of Satisfiability/Realizability from 0

Theorem 3.6. The satisfiability problem for RDC with discrete time is decidable.

Theorem 3.9. The realizability problem for RDC with discrete time is decidable.

### Recall: Proof Sketch

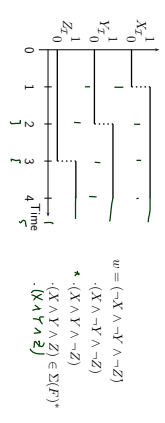


### Sketch: Proof of Theorem 3.6

- give a procedure to construct, given a formula  $F$ , a regular language  $L(F)$  such that
  - $\exists I_{[0,n]} \models F$  if and only if  $w \in L(F)$
 where word  $w$  describes  $I$  on  $[0,n]$  (satisfiability of the procedure: Lemma 3.4)
- then  $F$  is satisfiable in discrete time if and only if  $L(F)$  is not empty (Lemma 3.5)
- Theorem 3.6 follows because
  - $L(F)$  can effectively be constructed,
  - the emptiness problem is decidable for regular languages.

Construction of  $\mathcal{L}(F)$

- **Idea:**
  - alphabet  $\Sigma(F)$  consists of basic conjuncts of the state variables in  $F$ .
  - a letter corresponds to an interpretation on an interval of length 1.
  - a word of length  $n$  describes an interpretation on interval  $[0, n]$ .
- **Example:** Assume  $F$  contains exactly state variables  $X, Y, Z$ ; then
 
$$\Sigma(F) = \{X \wedge Y \wedge Z, X \wedge Y \wedge \neg Z, X \wedge \neg Y \wedge Z, X \wedge \neg Y \wedge \neg Z, \neg X \wedge Y \wedge Z, \neg X \wedge Y \wedge \neg Z, \neg X \wedge \neg Y \wedge Z, \neg X \wedge \neg Y \wedge \neg Z\}$$



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Sketch: Proof of Theorem 3.9

**Theorem 3.9**  
The realizability problem for RDC with discrete time is decidable.

- $\text{kernel}(L)$  contains all words of  $L$  whose prefixes are again in  $L$ .
  - if  $L$  is regular, then  $\text{kernel}(L)$  is also regular.
  - $\text{kernel}(\mathcal{L}(F))$  can effectively be constructed.
  - We have
- Lemma 3.8.** For all RDC formulae  $F$ ,  $F$  is realizable from 0 in discrete time if and only if  $\text{kernel}(\mathcal{L}(F))$  is infinite.
- Infinity of regular languages is decidable.

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Construction of  $\mathcal{L}(F)$  more Formally

**Definition 3.2.** A word  $w = a_1 \dots a_n \in \Sigma(F)^*$  with  $n \geq 0$  describes a discrete interpretation  $I$  on  $[0, n]$  if and only if

$$\forall j \in \{1, \dots, n\} \forall i \in \{j-1, j\} : \mathbb{I}[a_i][j] = 1.$$

For  $n = 0$  we put  $w = \varepsilon$ .

- Each state assertion  $P$  can be transformed into an equivalent disjunctive normal form  $\bigvee_{i=1}^k a_i$  with  $a_i \in \Sigma(F)$ .
- Set  $\text{DNF}(P) := \{a_1, \dots, a_k\} (\subseteq \Sigma(F))$ .
- Define  $\mathcal{L}(F)$  inductively:

$$\begin{aligned} \mathcal{L}(\neg P) &= \text{DNF}(P)^c && \text{find mode, negate at least one} \\ \mathcal{L}(P_1 \wedge P_2) &= \mathcal{L}(P_1) \wedge \mathcal{L}(P_2) && \text{(regular language)} \\ \mathcal{L}(P_1 \vee P_2) &= \mathcal{L}(P_1) \vee \mathcal{L}(P_2) && \text{(spec. regular)} \\ \mathcal{L}(F_1; F_2) &= \mathcal{L}(F_1) \cdot \mathcal{L}(F_2) && \text{(concatenation)} \end{aligned}$$

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Lemma 3.4

**Lemma 3.4.** For all RDC formulae  $F$ , discrete interpretations  $I$ ,  $n \geq 0$ , and all words  $w \in \Sigma(F)^*$  which describe  $I$  on  $[0, n]$ ,  $I, [0, n] \models F$  if and only if  $w \in \mathcal{L}(F)$ .

**Goal:** Structural induction.  
**Base:**  $F = \text{True}$ . assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models \text{True}$  because  $\mathbb{I}[a_i] \in \{1, 0\}$ .  
 For  $n \geq 1$  and  $F = X \wedge Y$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models X \wedge Y$  iff  $\mathbb{I}[a_1] = 1$  and  $\mathbb{I}[a_2] = 1$ .  
 For  $n \geq 1$  and  $F = X \wedge \neg Y$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models X \wedge \neg Y$  iff  $\mathbb{I}[a_1] = 1$  and  $\mathbb{I}[a_2] = 0$ .  
 For  $n \geq 1$  and  $F = \neg X \wedge Y$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models \neg X \wedge Y$  iff  $\mathbb{I}[a_1] = 0$  and  $\mathbb{I}[a_2] = 1$ .  
 For  $n \geq 1$  and  $F = \neg X \wedge \neg Y$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models \neg X \wedge \neg Y$  iff  $\mathbb{I}[a_1] = 0$  and  $\mathbb{I}[a_2] = 0$ .  
**Inductive step:** Assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 For  $F = P_1 \wedge P_2$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_1 \wedge P_2$  iff  $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_1$  and  $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_2$ .  
 For  $F = P_1 \vee P_2$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_1 \vee P_2$  iff  $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_1$  or  $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_2$ .  
 For  $F = P_1; P_2$ , assume  $w = a_1 \dots a_n$ , describes  $I$  on  $[0, n]$ .  
 $\mathbb{I}[a_1] \dots \mathbb{I}[a_n] \models P_1; P_2$  iff  $\mathbb{I}[a_1] \dots \mathbb{I}[a_{i-1}] \models P_1$  and  $\mathbb{I}[a_i] \dots \mathbb{I}[a_n] \models P_2$ .  
**Sketch:**  $\rightarrow \mathbb{I}$ ,  $\mathbb{I} \rightarrow \mathbb{I}$ ,  $\mathbb{I} \rightarrow \mathbb{I}$ ,  $\mathbb{I} \rightarrow \mathbb{I}$ .

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Variants of) RDC in Continuous Time

Recall: Restricted DC (RDC)

$F ::= [P] \mid \neg F_1 \mid F_1 \vee F_2 \mid F_1; F_2$   
 where  $P$  is a state assertion, but with boolean observables only.  
 From now on:  $\text{RDC} + \ell = x, \forall x^r$   
 $F ::= [P] \mid \neg F_1 \mid F_1 \vee F_2 \mid F_1; F_2 \mid \ell = 1 \mid \ell = x \mid \forall x \bullet F_1$

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**Theorem 3.10.**  
The realisability from 0 problem for DC with continuous time is undecidable, not even semi-decidable.

**Theorem 3.11.**  
The satisfiability problem for DC with continuous time is undecidable.

Reduce divergence of two-counter machines to realisability from 0:

- Given a two-counter machine  $M$  with final state  $q_{fin}$
- construct a DC formula  $F(M) := encoding(M)$
- such that

$M$  diverges **if and only if** the DC formula  $F(M) \wedge \neg \exists [q_{fin}]$  is realisable from 0.

- If realisability from 0 was (semi-)decidable,
- divergence of two-counter machines would be (which it isn't).

ZCM Configurations and Computations

- a configuration of  $M$  is a triple  $K = (q, n_1, n_2) \in \mathbb{Q} \times \mathbb{N}_0 \times \mathbb{N}_0$
- The transition relation " $\vdash$ " on configurations is defined as follows:

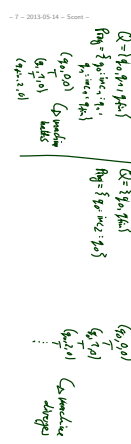
Command	Semantics: $K' \vdash K$
$q : inc_1 : q'$	$(q, n_1, n_2) \vdash (q', n_1 + 1, n_2)$
$q : dec_1 : q'$	$(q, 0, n_2) \vdash (q', 0, n_2)$
$q : dec_1 : q'$	$(q, n_1 + 1, n_2) \vdash (q', n_1, n_2)$
$q : inc_2 : q'$	$(q, n_1, n_2) \vdash (q', n_1, n_2 + 1)$
$q : dec_2 : q'$	$(q, n_1, 0) \vdash (q', n_1, 0)$
$q : dec_2 : q'$	$(q, n_1, n_2 + 1) \vdash (q', n_1, n_2)$

- The (1) computation of  $M$  is a finite sequence of the form ("M halts")  
 $K_0 = (q_0, 0, 0) \vdash K_1 \vdash K_2 \vdash \dots \vdash (q_{fin}, n_1, n_2)$
- or an infinite sequence of the form ("M diverges")  
 $K_0 = (q_0, 0, 0) \vdash K_1 \vdash K_2 \vdash \dots$

ZCM Example

- $M = \{Q, \{q_0, q_{fin}, prog\}$
- commands of the form  $q : inc_i : q'$  and  $q : dec_i : q'$ ,  $i \in \{1, 2\}$
- configuration  $K = (q, n_1, n_2) \in \mathbb{Q} \times \mathbb{N}_0 \times \mathbb{N}_0$

Command	Semantics: $K' \vdash K$
$q : inc_1 : q'$	$(q, n_1, n_2) \vdash (q', n_1 + 1, n_2)$
$q : dec_1 : q'$	$(q, 0, n_2) \vdash (q', 0, n_2)$
$q : dec_1 : q'$	$(q, n_1 + 1, n_2) \vdash (q', n_1, n_2)$
$q : inc_2 : q'$	$(q, n_1, n_2) \vdash (q', n_1, n_2 + 1)$
$q : dec_2 : q'$	$(q, n_1, 0) \vdash (q', n_1, 0)$
$q : dec_2 : q'$	$(q, n_1, n_2 + 1) \vdash (q', n_1, n_2)$



A two-counter machine is a structure

- $M = (\mathbb{Q}, q_0, q_{fin}, Prog)$
- where
- $Q$  is a finite set of states,
- comprising the initial state  $q_0$  and the final state  $q_{fin}$
- $Prog$  is the machine program, i.e. a finite set of commands of the form

$q : inc_i : q'$  and  $q : dec_i : q'$ ,  $i \in \{1, 2\}$ ,  $q, q' \in Q$

- We assume deterministic ZCM: for each  $q \in Q$ , at most one command starts in  $q$ , and  $q_{fin}$  is the only state where no command starts.

Reducing Divergence to DC realisability: Idea in Pictures



Reducing Divergence to DC realizability: Idea

- A single configuration  $K$  of  $\mathcal{M}$  can be encoded in an interval of length 4: Being an encoding interval can be characterized by a DC formula.
- An interpretation on 'Time' encodes a configuration of  $\mathcal{M}$  if
  - each interval  $[n, 4(n+1)]$ ,  $n \in \mathbb{N}_0$ , encodes a configuration  $K_n$ ,
  - each two subsequent intervals  $[n, 4(n+1)]$  and  $[4(n+1), 4(n+2)]$ ,  $n \in \mathbb{N}_0$ , encode configurations  $K_n, K_{n+1}$  in transition relation.
- Being encoding of the run can be characterized by DC formula  $F(\mathcal{M})$ .
- Then  $\mathcal{M}$  diverges if and only if  $F(\mathcal{M}) \wedge \neg \exists! q_{fin}$  is realisable from 0.

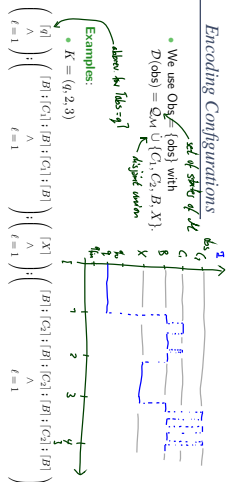
Initial and General Configurations

$$init := \exists t \geq 4 \implies [q_0]^{-1}; [B]^{-1}; [X]^{-1}; [B]^{-1}; true$$

$$keep := \exists t \geq 4 \implies \Box([Q]^{-1}; [B \vee G_1]^{-1}; [X]^{-1}; [B \vee G_2]^{-1}; t = 4 \implies t = 4; [Q]^{-1}; [B \vee G_1]^{-1}; [X]^{-1}; [B \vee G_2]^{-1})$$

where  $Q := \neg(X \vee G_1 \vee G_2 \vee B)$ .

Encoding Configurations



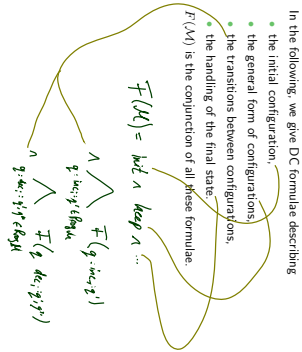
Auxiliary Formula Pattern copy

$$copy(q_i, \{P_1, \dots, P_n\}) := \exists c, d \bullet \Box((P \wedge t = c) \implies ([B \vee \dots \vee P_n] \wedge t = d); [P_1]; t = 4 \implies t = c + d + 4; [P_1])$$

$$\dots$$

$$\wedge \forall c, d \bullet \Box((P \wedge t = c) \implies ([B \vee \dots \vee P_n] \wedge t = d); [P_n]; t = 4 \implies t = c + d + 4; [P_n])$$

Construction of  $F(\mathcal{M})$



(i)  $q_i : inc_{q_i} : [Q]$  (Increment)  $\in R_{q_i, K}$

(i) Change state

$$\Box([q]^{-1}; [B \vee G_1]^{-1}; [X]^{-1}; [B \vee G_2]^{-1}; t = 4 \implies t = 4; [q]^{-1}; true)$$

$$\Box([q]^{-1}; [B, G_1]^{-1}; [X]^{-1}; [B, G_2]^{-1}; t = 4 \implies t = 4; [q]^{-1}; ([B]; [G_1]; [B]); t = d; true)$$

(ii) Increment counter

$$\forall d \bullet \Box([q]^{-1}; [B]^{-1}; t = 0 \vee [G_1]^{-1}; \neg X); [X]^{-1}; [B \vee G_1]^{-1}; t = 4 \implies t = 4; [q]^{-1}; ([B]; [G_1]; [B]); t = d; true$$

$q : m_1 : q'$  (Increment)

- (i) Keep rest of first counter  

$$\text{copy}([q]^{-1} : [B \vee C_1] : [C_1], \{B, C_1\})$$

$$\underbrace{\text{copy}([q]^{-1} : [B \vee C_1] : [X]^{-1}, \{B, C_2\})}_{\text{true}}$$
- (ii) Leave second counter unchanged  

$$\underbrace{\text{copy}([q]^{-1} : [B \vee C_1] : [X]^{-1}, \{B, C_2\})}_{\text{true}}$$

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$q : dec_1 : q', q''$  (Decrement)

- (i) If zero  

$$\Box([q]^{-1} : [B]^{-1} : [X]^{-1} : [B \vee C_2]^{-1} : \ell = 4 \implies \ell = 4 : [q]^{-1} : [B]^{-1} : true)$$
- (ii) Decrement counter  

$$\forall \ell \bullet \Box([q]^{-1} : [B]^{-1} : [C_1] \wedge \ell = 0) : [B]^{-1} : [B \vee C_1] : [X]^{-1} : [B \vee C_2]^{-1} : \ell = 4$$

$$\implies \ell = 4 : [q]^{-1} : [B]^{-1} : true$$

- (iii) Keep rest of first counter  

$$\text{copy}([q]^{-1} : [B]^{-1} : [C_1] : [B_1] : [B, C_1])$$
- (iv) Leave second counter unchanged  

$$\text{copy}([q]^{-1} : [B \vee C_1] : [X]^{-1}, \{B, C_2\})$$

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Final State

$$\text{copy}([q_{\text{final}}]^{-1} : [B \vee C_1]^{-1} : [X]^{-1} : [B \vee C_2]^{-1} : \{q_{\text{final}}, B, X, C_1, C_2\})$$

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### Satisfiability

- Following [Chaochen and Hansen, 2004] we can observe that  $\mathcal{M}$  halts if and only if the DC formula  $F(\mathcal{M}) \wedge \neg \langle q_{\text{final}} \rangle$  is satisfiable. This yields

**Theorem 3.11.** The satisfiability problem for DC with continuous time is undecidable.

(It is semi-decidable.)

- Furthermore, by taking the contraposition, we see  $\mathcal{M}$  diverges if and only if  $\mathcal{M}$  does not halt if and only if  $F(\mathcal{M}) \wedge \neg \langle q_{\text{final}} \rangle$  is not satisfiable.
- Thus whether a DC formula is not satisfiable is not decidable, not even semi-decidable.

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### Validity

- By Remark 2.13,  $F$  is valid iff  $\neg F$  is not satisfiable, so

**Corollary 3.12.** The validity problem for DC with continuous time is undecidable, not even semi-decidable.

- This provides us with an alternative proof of Theorem 2.23 ("there is no sound and complete proof system for DC").
- Suppose** there were such a calculus  $C$ .
- By Lemma 2.22 it is semi-decidable whether a given DC formula  $F$  is a theorem in  $C$ .
- By the soundness and completeness of  $C$ ,  $F$  is a theorem in  $C$  if and only if  $F$  is valid.
- Thus it is semi-decidable whether  $F$  is valid. **Contradiction.**

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### Discussion

- Note: the DC fragment defined by the following grammar is sufficient for the reduction  

$$F ::= [P]^{-1} \neg F_1 \mid F_1 \vee F_2 \mid F_1 : F_2 \mid \ell = 1 \mid \ell = x \mid \forall x \bullet F_1,$$

$$P \text{ a state assertion, } x \text{ a global variable.}$$

- Formulae used in the reduction are abbreviations:

$$\ell = 4 \iff \ell = 1; \ell = 1; \ell = 1; \ell = 1; \ell = 1$$

$$\ell \geq 4 \iff \ell = 4; true$$

$$\ell = x + y + 4 \iff \ell = x; \ell = y; \ell = 4$$

- Length 1 is not necessary — we can use  $\ell = x$  instead, with fresh  $z$ .
- This is RDC augmented by " $\ell = x$ " and " $\forall x, x$ ", which we denote by **RDC +  $\ell = x, \forall x$** .

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## References

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- ### References
- [Chaochen and Hansen, 2004] Chaochen, Z. and Hansen, M. R. (2004). *Duration Calculus: A Formal Approach to Real-Time Systems*. Monographs in Theoretical Computer Science. Springer-Verlag. An EATCS Series.
- [Olderog and Dierks, 2008] Olderog, E.-R. and Dierks, H. (2008). *Real-Time Systems - Formal Specification and Automatic Verification*. Cambridge University Press.

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