

Real-Time Systems

Lecture 14: Extended Timed Automata

2013-06-25

Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

Contents & Goals

Last Lecture:

- Decidability of the location reachability problem:
- region automaton
- zones

This Lecture:

- **Educational Objectives:** Capabilities for following tasks/ questions:
 - By what are TA extended? Why/ is that useful?
 - What's an urgent/committed location? What's the difference?
 - What's an urgent channel?
 - Where has the notion of "input action" and "output action" correspondences in the formal semantics?

Content:

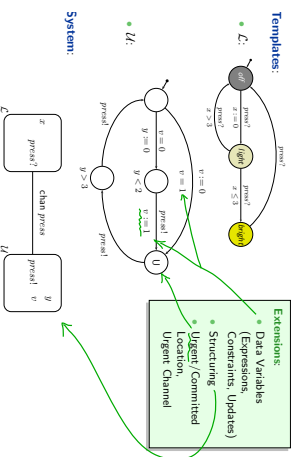
- Extended TA.
- Data Variables
- Structuring Facilities
- Restriction of Non-Determinism
- The Logic of Uppaal

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Extended Timed Automata

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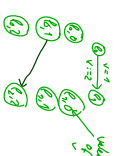
Example (Partly Already Seen in Uppaal Demo)



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Data Variables

- When modelling controllers as timed automata, it is sometimes desirable to have (local and shared) variables.
 - E.g. count number of open doors, or intermediate positions of gas valve.
- Adding variables with **finite range** (possibly grouped into finite arrays) to any finite-state automata concept is straightforward:
 - if we have control locations $L_n = \{l_1, \dots, l_n\}$,
 - and want to model, e.g., the value range as a variable v with $D(v) = \{0, \dots, 2\}$,
 - then just use $L = L_n \times D(v)$ as control locations, i.e. encode the current value of v in the control location, and consider updates of v in the Δ .
- L is still finite, so we still have a proper TA.



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- When modelling controllers as timed automata, it is sometimes desirable to have (local and shared) variables.
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 - then just use $L = L_n \times D(v)$ as control locations, i.e. encode the current value of v in the control location, and consider updates of v in the Δ .
- L is still finite, so we still have a proper TA.
- But: writing Δ is tedious.
- So have variables as "first class citizens" and let compilers do the work.
- **Interestingly**, many examples in the literature live without variables: the more abstract the model is, i.e., the fewer information it keeps track of (e.g. in data variables), the easier the verification task.

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Data Variables and Expressions

- Let $(v, w) \in V$ be a set of (integer) variables.
 $(\psi_{int} \in \mathcal{W}(V))$: Integer expressions over V using func. symb. $+, -, \dots$
 $(\varphi_{int} \in \mathcal{W}(V))$: Integer (or data) constraints over V using integer expressions, predicate symbols $=, <, \leq, \dots$, and boolean logical connectives. $(\text{and } \forall, \neg, \vee, \Rightarrow, \Leftarrow, \dots)$
- Let $(r, g) \in X$ be a set of clocks.
 $(\varphi \in \mathcal{W}(X, V))$: (extended) guards, defined by
 $\varphi ::= \varphi_{int} \wedge \varphi_{int} \mid \varphi_1 \wedge \varphi_2$
 where $\varphi_{int} \in \mathcal{W}(V)$ is a simple clock constraint (as defined before) and $\varphi_{int} \in \mathcal{W}(V)$ an integer (or data) constraint.

Examples: Extended guard or not extended guard? Why?

(a) $x < y \wedge v > z$	(b) $x < y \vee v > z$	(c) $v < 1 \vee v > 2$	(d) $\frac{x < y}{x < y}$
$\in \mathcal{W}(X)$	$\in \mathcal{W}(V)$	$\in \mathcal{W}(V)$	$\in \mathcal{W}(X)$
\checkmark	\checkmark	\checkmark	\checkmark

Handwritten notes: (a) is not a guard, (b) is a guard, (c) is a guard, (d) is a guard. (a) is not a guard because it contains variables from both X and V. (b) is a guard because it contains only variables from V. (c) is a guard because it contains only constants. (d) is a guard because it contains only variables from X.

Modification or Reset Operation

- New: a modification or reset (operation)** is
 $x := 0$, $x \in X$,
 or
 $v := \psi_{int}, v \in V, \psi_{int} \in \mathcal{W}(V)$.
- By $R(X, V)$ we denote the set of all resets.
- By r we denote a finite list $(r_1, \dots, r_n), n \in \mathbb{N}_0$ of reset operators $r_i \in R(X, V)$.
 $\langle \rangle$ is the empty list.
- By $R(X, V)^*$ we denote the set of all such lists of reset operators.

Examples: Modification or not? Why?

(a) $x := 0$	(b) $x := x$	(c) $x := x$	(d) $v := w$	(e) $v := 0$
\checkmark	\checkmark	\checkmark	\checkmark	\checkmark

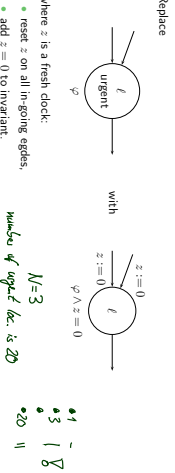
Handwritten notes: (a) is a reset, (b) is not a reset, (c) is not a reset, (d) is not a reset, (e) is a reset. (a) is a reset because it sets x to 0. (b) is not a reset because it does not change the value of x. (c) is not a reset because it does not change the value of x. (d) is not a reset because it does not change the value of v. (e) is a reset because it sets v to 0.

Restricting Non-determinism

- Urgent locations** — enforce local immediate progress.
- Committed locations** — enforce atomic immediate progress.
- Urgent channels** — enforce cooperative immediate progress.

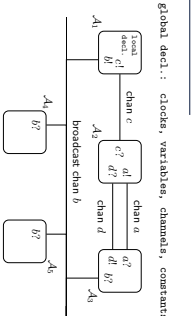


Urgent Locations: Only an Abbreviation...



Question: How many fresh clocks do we need in the worst case for a network of N extended timed automata?

Structuring Facilities



- Global declarations of clocks, data variables, channels, and constants.
- Binary and broadcast channels: chan c and broadcast chan b .
- Templates of timed automata.
- Instantiation of templates (instances are called **process**).
- System definition: list of processes.

Extended Timed Automata

Definition 4.29: An extended timed automaton is a structure $\mathcal{A} = (L, C, B, U, X, V, L, E, I, \text{inv})$ where L, B, X, L, inv are as in Def. 4.28 except that location invariants in I are downward closed and where

- $C \subseteq L$: committed locations,
- $U \subseteq B$: urgent channels,
- V : a set of data variables,
- $E \subseteq L \times B \times \mathcal{W}(X, V) \times R(X, V)^* \times L$: a set of directed edges such that

$(l, \alpha, \varphi, r, l') \in E \wedge (\text{chan}(\alpha) \in U \Rightarrow \varphi = \text{true})$
 Edges $(l, \alpha, \varphi, r, l')$ from location l to l' are labelled with an action α , a guard φ , and a list r of reset operations.

Handwritten notes: φ is dc, $V \cup X \rightarrow \mathbb{R}_{>0}$, $\Rightarrow \mathcal{P} \subseteq \mathcal{P}$, $(\forall l \in L, \text{inv}(l) \subseteq \mathbb{R}_{>0})$, \mathcal{P} is dc, $\mathcal{P} \subseteq \mathcal{P}$ is dc.

Operational Semantics of Networks

Definition 4.40. Let $\mathcal{A}_i = (L_i, C_i, B_i, E_i, X_i, V_i, I_i, B_i, \delta_{int,i})$, $1 \leq i \leq n$, be extended timed automata with pairwise disjoint sets of clocks X_i .

The operational semantics of $C(\mathcal{A}_1, \dots, \mathcal{A}_n)$ (closed) is the labelled transition system

$$\mathcal{T}_c(C(\mathcal{A}_1, \dots, \mathcal{A}_n)) \\ = (Conf, Time \cup \{\tau\}, \{\frac{\cdot}{\cdot}\} \cup \{\lambda \in Time \cup \{\tau\}\}, C_{int})$$

where

- $X = \bigcup_{i=1}^n X_i$ and $V = \bigcup_{i=1}^n V_i$.
 - $Conf = \{(l, v) \mid l_i \in L_i, v : X \cup V \rightarrow Time, v \models \bigwedge_{i=1}^n I_i(l_i)\}$
 - $C_{int} = \{(l_{int}, v_{int})\} \cap Conf$.
- and the transition relation consists of transitions of the following three types.

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Helpers: Extended Valuations and Timeshift

- Now:** $v : X \cup V \rightarrow Time \cup DV$
- Canonically extends to $v : \Psi(V) \rightarrow D$ (valuation of expression).
- " \models " extends canonically to expressions from $\Psi(X, V)$.

$$v_i := v \upharpoonright \{l_i, v_i, \dots, v_i\}$$

$$Assume \quad \mathbb{I}(t) : \mathbb{Z}^n \rightarrow \mathbb{Z}$$

$$\mathbb{I}(l_i) := v_i(l_i) \in \mathbb{Q}(V)$$

$$\mathbb{I}(f(v_1, \dots, v_i, l_i)) := \mathbb{I}(f)(\mathbb{I}(v_1), \dots, \mathbb{I}(v_i), \mathbb{I}(l_i))$$

$$\mathbb{I}(v_i) := \{v_i\}$$

$$\mathbb{I}(t) := \{t\}$$

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Helpers: Extended Valuations and Timeshift

- Now:** $v : X \cup V \rightarrow Time \cup DV$
- Canonically extends to $v : \Psi(V) \rightarrow D$ (valuation of expression).
- " \models " extends canonically to expressions from $\Psi(X, V)$.
- Extended timeshift $\mathcal{C} \pm t, t \in Time$, applies to clocks only.
- $(v \pm t)(c) := v(c) + t, c \in X$.
- $(v \pm t)(v) := v(v), v \in V$.

- Effect of modification** $r \in R(X, V)$ on v , denoted by $v[r]$:

$$v[r] := \mathbf{0}(v) := \begin{cases} 0, & \text{if } a = x, \\ v(a), & \text{otherwise} \end{cases}$$

$$v[a] := \Psi_{int}(v) := \begin{cases} v(a), & \text{if } a = v_i, \\ v(a), & \text{otherwise} \end{cases}$$

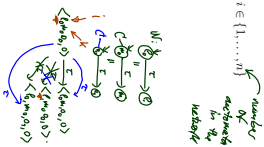
- We set $v[r_1, \dots, r_n] := v[r_1] \dots [r_n] = ((v[r_1])[r_2]) \dots [r_n]$

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Operational Semantics of Networks: Internal Transitions

- An **internal transition** $(l^i, v^i) \xrightarrow{\tau} (l^j, v^j)$ occurs if there is $i \in \{1, \dots, n\}$ such that

- there is a τ -edge $(l_i, r_i, \sigma_i, \tau_i(l_i)) \in E_i$.
- $v \models \sigma_i$.
- $v^i = \mathbb{I}(l_i) \upharpoonright X_i$.
- $v^j = v[r_i]$.
- $v^j \models I_i(l_i^j)$.
- \clubsuit if $l_k \in C_k$ for some $k \in \{1, \dots, n\}$ then $l_i \in C_i$.



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Operational Semantics of Networks: Synchronisation Transitions

- A **synchronisation transition** $(l^i, v^i) \xrightarrow{\tau} (l^j, v^j)$ occurs if there are $k, j \in \{1, \dots, n\}$ with $i \neq j$ such that

- there are edges $(l_i, l_i, \sigma_i, \tau_i(l_i)) \in E_i$ and $(l_j, l_j, \sigma_j, \tau_j(l_j)) \in E_j$.
- $v \models \sigma_i \wedge \sigma_j$.
- $v^i = \mathbb{I}(l_i) \upharpoonright X_i$.
- $v^j = v[r_i^j] \upharpoonright X_j$.
- $v^i \models I_i(l_i^i) \wedge I_j(l_j^j)$.
- $v^j \models I_i(l_i^j) \wedge I_j(l_j^j)$.
- \clubsuit if $l_k \in C_k$ for some $k \in \{1, \dots, n\}$ then $l_i \in C_i$ or $l_j \in C_j$.

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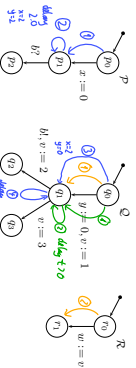
Operational Semantics of Networks: Delay Transitions

- A **delay transition** $(l^i, v^i) \xrightarrow{\delta} (l^i, v^i + t)$ occurs if

- $v + t \models \bigwedge_{i=1}^n I_i(l_i)$.
- \clubsuit there are no $k, j \in \{1, \dots, n\}$ and $b \in U$ with $(l_i, l_i, \sigma_i, \tau_i(l_i)) \in E_i$ and $(l_j, l_j, \sigma_j, \tau_j(l_j)) \in E_j$.
- \spadesuit there is no $i \in \{1, \dots, n\}$ such that $l_i \in C_i$.

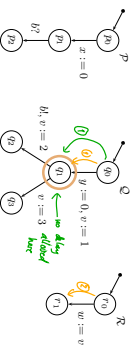
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Restricting Non-determinism: Example



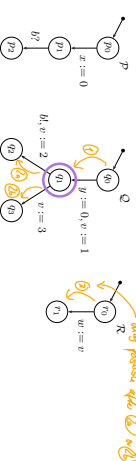
	Property 1	Property 2	Property 3
$\exists v w = 1$	$\forall \square Q \wedge 1$	$\forall \square Q \wedge 1 \implies y \leq 0$	$\forall \square (P \wedge Q \wedge 1 \implies (x \geq y \implies y \leq 0))$
$N := P \parallel Q \parallel R$	\checkmark	\times	\times
N, a urgent			
N, a comm.			
N, b urgent			

Restricting Non-determinism: Urgent Location



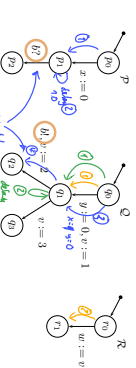
	Property 1	Property 2	Property 3
$\exists v w = 1$	\checkmark	\times	\times
$N := P \parallel Q \parallel R$	\checkmark	\checkmark	\checkmark
N, a urgent			
N, a comm.			
N, b urgent			

Restricting Non-determinism: Committed Location



	Property 1	Property 2	Property 3
$\exists v w = 1$	$\forall \square Q \wedge 1$	$\forall \square Q \wedge 1 \implies y \leq 0$	$\forall \square (P \wedge Q \wedge 1 \implies (x \geq y \implies y \leq 0))$
$N := P \parallel Q \parallel R$	\checkmark	\times	\times
N, a urgent	\checkmark	\checkmark	\checkmark
N, a comm.	\times	\checkmark	\checkmark
N, b urgent			

Restricting Non-determinism: Urgent Channel



	Property 1	Property 2	Property 3
$\exists v w = 1$	$\forall \square Q \wedge 1$	$\forall \square Q \wedge 1 \implies y \leq 0$	$\forall \square (P \wedge Q \wedge 1 \implies (x \geq y \implies y \leq 0))$
$N := P \parallel Q \parallel R$	\checkmark	\times	\times
N, a urgent	\checkmark	\checkmark	\checkmark
N, a comm.	\times	\checkmark	\checkmark
N, b urgent	\checkmark	\times	\checkmark

Extended vs. Pure Timed Automata

Extended vs. Pure Timed Automata

- $\mathcal{A} = (L, C, B, U, X, I, E, I_{init})$
- $(\ell, \alpha, \varphi, F, \rho) \in L \times B \times U \times \Phi(X, V) \times R(X, V) \times L$
- vs.
- $\mathcal{A} = (L, B, X, I, E, I_{init})$
- $(\ell, \alpha, \varphi, Y, \rho) \in L \times B \times X \times \Phi(X) \times 2^X \times L$
- \mathcal{A} is in fact (or specialises to) a pure timed automaton if
 - $C = \emptyset$
 - $U = \emptyset$
 - $V = \emptyset$
- for each $F = \{r_1, \dots, r_n\}$, every r_i is of the form $x := 0$ with $x \in X$.
- $I(\ell, \varphi \in \Phi(X))$ is then a consequence of $V = \emptyset$.

Theorem 4.41. If A_1, \dots, A_n specialise to pure timed automata, then the operational semantics of $C(A_1, \dots, A_n)$ and $\text{chan}_{h_1, \dots, h_n} \bullet (A_1 \parallel \dots \parallel A_n)$ where $\{h_1, \dots, h_n\} = \bigcup_{i=1}^n B_i$, coincide, i.e. $T(C(A_1, \dots, A_n)) = T(\text{chan}_{h_1, \dots, h_n} \bullet (A_1 \parallel \dots \parallel A_n))$.

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Reachability Problems for Extended Timed Automata

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Theorem 4.33. [Location Reachability] The location reachability problem for pure timed automata is decidable.

Theorem 4.34. [Constraint Reachability] The constraint reachability problem for pure timed automata is decidable.

• And what about “w” extended timed automata?

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References

References

[Deog and Dieks, 2008] Olderg, E.-R. and Dieks, H. (2008). Real-Time Systems - Formal Specification and Automatic Verification. Cambridge University Press.

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