

## Real-Time Systems

### Lecture 11: Networks of Timed Automata

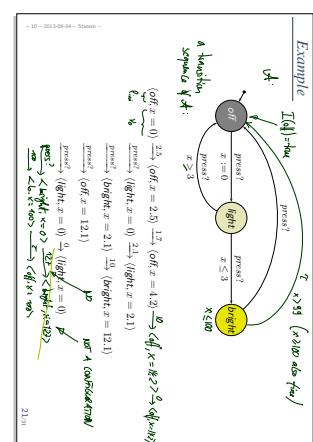
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## Contents & Goals

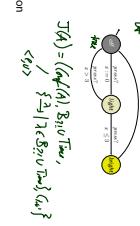
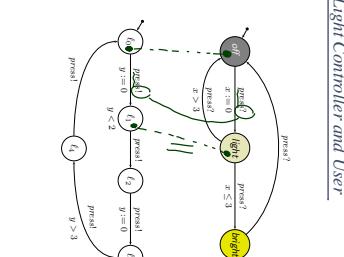
### Last Lecture:

- Timed automata syntax
- TA operational semantics
- Def. TA
- Pure TA operational semantics
- clock valuation, time shift, modification
- operational semantics
- transition sequence, computation path, run
- network of TA
- parallel composition (syntactical)
- restriction
- n network of TA semantics
- Uppaal demo
- parallel composition of TA
- what's the (syntactical) parallel composition of TA?
- Content:
- Educational Objectives: Capabilities for following tasks/questions.



## Recall: Plan

- Pure TA syntax
- channels, actions
- (simple) clock constraints
- Def. TA
- Pure TA operational semantics
- clock valuation, time shift, modification
- operational semantics
- transition sequence, computation path, run
- network of TA
- parallel composition (syntactical)
- restriction
- n network of TA semantics
- Uppaal Demo, part 1
- Extended timed automata



## Parallel Composition

### Helper: Action Complementation

**Definition 4.12.**

The parallel composition  $\mathcal{A}_1 \parallel \mathcal{A}_2$  of two timed automata

$$\mathcal{A}_i = (L_i, B_i, X_i, I_i, E_i, (\ell_{ini}, \ell_{fini}), i = 1, 2,$$

with disjoint sets of clocks  $X_1$  and  $X_2$ , yields the timed automaton

$$\mathcal{A} = (L_1 \times L_2, B_1 \cup B_2, X_1 \cup X_2, I, E, (\ell_{ini,1}, \ell_{fin,2}))$$

where

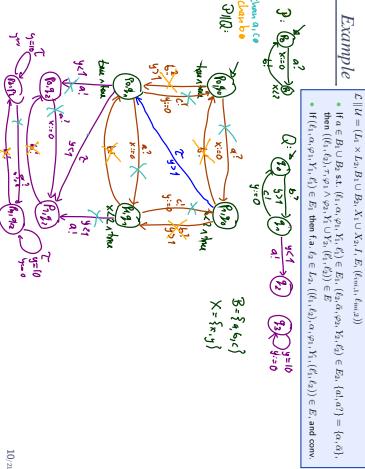
- $I(\ell_1, \ell_2) := I'_1(\ell_1) \cup I'_2(\ell_2)$ , and

- $E$  consists of handshaking and asynchronous communication.

(→ next slide)

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\* The complementation function

$$\overline{-} : \text{Act} \rightarrow \text{Act}$$

is defined pointwise as

- $\overline{a!} = a?$
- $\overline{a?} = a!$
- $\overline{\tau} = \tau$
- Note:  $\overline{\overline{\alpha}} = \alpha$  for all  $\alpha \in \text{Act}$ .

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**Parallel Composition: Handshake and Asynchrony**

$\mathcal{A}_1 \parallel \mathcal{A}_2 = (L_1 \times L_2, B_1 \cup B_2, X_1 \cup X_2, I, E, (\ell_{ini,1}, \ell_{fin,2}))$  with

If there is  $\alpha \in B_1 \cup B_2$  such that

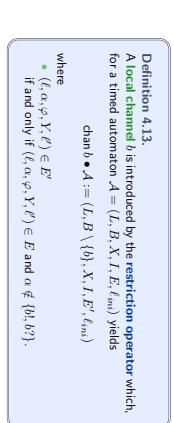
$$(f_1, \alpha, \varphi_1, Y_1, \ell'_1) \in E_1 \text{ and } (f_2, \alpha, \varphi_2, Y_2, \ell'_2) \in E_2,$$

and  $\{\alpha, \alpha'\} = \{\alpha, \alpha'\}$ , then

$$((\ell_1, \ell_2), \alpha, \varphi_1 \wedge \varphi_2, Y_1 \cup Y_2, (\ell'_1, \ell'_2)) \in E.$$

- **Handshake:** If  $(\ell_1, \alpha, \varphi_1, Y_1, \ell'_1) \in E_1$ , then for all  $\ell_2 \in L_2$ ,
- **Asynchrony:** If  $(\ell_2, \alpha, \varphi_2, Y_2, \ell'_2) \in E_2$ , then  $((\ell_1, \ell_2), \alpha, \varphi_1, Y_1, (\ell'_1, \ell_2)) \in E$ .
- If  $(\ell_2, \alpha, \varphi_2, Y_2, \ell'_2) \in E_2$ , then for all  $\ell_1 \in L_1$ ,
- If  $(\ell_1, \ell_2), \alpha, \varphi_1 \wedge \varphi_2, Y_1 \cup Y_2, (\ell'_1, \ell'_2) \in E$ .

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## Networks of Timed Automata

- A timed automaton  $\mathcal{N}$  is called network of timed automata if and only if it is obtained as

$$\text{chan } b_1 \dots b_m \bullet (\mathcal{A}_1 \parallel \dots \parallel \mathcal{A}_n)$$

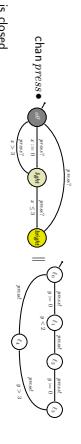
- Abbreviation:

$$\text{chan } b_1 \dots b_m \bullet \mathcal{A} := \text{chan } b_1 \bullet \dots \bullet \text{chan } b_m \bullet \mathcal{A}$$

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## Closed Networks

- A network  $\mathcal{N} = \text{chan } b_1 \dots b_m \bullet (\mathcal{A}_1 \parallel \dots \parallel \mathcal{A}_n)$  is called **closed** if and only if  $\{b_1, \dots, b_m\} = \bigcup_{i=1}^n B_i$ .
- Then, by Lemma 4.16 (later), local transitions don't occur (since  $B = \emptyset$ ). Transitions are thus either internal actions  $\tau$  or delay transitions.



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## Operational Semantics of Networks

**Lemma 4.16.** Let  $\mathcal{A} = (U, B, X, I, E, f_{\text{inv}, i})$  with  $i = 1, \dots, n$  be a net of timed automata with disjoint clocks. Then the operational semantics of the network  $\text{chan } b_1 \dots b_m \bullet (\mathcal{A}_1 \parallel \dots \parallel \mathcal{A}_n)$  yields the labelled transition system

- ( $\text{Conf}(\mathcal{A}), \text{Time} \cup B_n, \{\overset{\lambda}{\rightarrow}\}_{\lambda \in \text{Time} \cup B_n}, C_{\text{ini}})$
- $X = \bigcup_{i=1}^n X_i$
  - $B = \bigcup_{i=1}^n B_i \cup \{b_1, \dots, b_m\}$
  - $\text{Conf}(\mathcal{A}) = \{\langle \vec{t}, \nu \rangle \mid \vec{t} \in L_1 \times \dots \times L_n \wedge \nu : X \rightarrow \text{Time} \wedge \nu \models \bigwedge_{i=1}^n I_k(t_k)\}$
  - $C_{\text{ini}} = \{\langle \langle \ell_{\text{ini}, 1}, \dots, \ell_{\text{ini}, n} \rangle, \nu_{\text{ini}} \rangle\} \cap \text{Conf}(\mathcal{A})$  where  $\nu_{\text{ini}}(x) = 0$  for all  $x \in X$ .
  - and three types of transition relations ( $\rightarrow$  next slides)

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## Operational Semantics of Networks

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## Operational Semantics of Networks: Local Transitions

For each  $\lambda \in \text{Time} \cup B_n$ , the transition relation  $\overset{\lambda}{\subseteq} \text{Conf}(\mathcal{A}) \times \text{Conf}(\mathcal{A})'$  has one of the following three types:

### (i) Local transition:

- if there is  $i \in \{1, \dots, n\}$  such that  $\langle \vec{t}, \nu \rangle \overset{\lambda}{\rightarrow} \langle \vec{t}', \nu' \rangle$
- $\langle \ell_i, \alpha, \varphi, Y, \ell'_i \rangle \in E_i$ ,  $\alpha \in B_i$ , ( $i$ -th automaton has corresp. edge)
- $\nu \models \varphi$ , ( $i$ -th location is satisfied)
- $\vec{t}' = \vec{t}[t_i := \ell'_i]$ , (only  $i$ -th location changes)
- $\nu' = \nu[Y_i \cup \{j := 0\}]$ , and (destination invariant holds)
- $\nu' \models I_i(\ell'_i)$ .

local  
transition

## Operational Semantics of Networks: Synchronisation

### (ii) Synchronisation transition:

- $\langle \vec{t}, \nu \rangle \overset{\lambda}{\rightarrow} \langle \vec{t}', \nu' \rangle$  if there are  $i, j \in \{1, \dots, n\}$ ,  $i \neq j$ , and  $b \in B_i \cap B_j$ , such that

- $\langle \ell_i, b, \varphi_i, Y, \ell'_i \rangle \in E_i$  and  $\langle \ell_j, b, \varphi_j, Y, \ell'_j \rangle \in E_j$ ,
- $\nu \models \varphi_i \wedge \varphi_j$ ,
- $\vec{t}' = \vec{t}[t_i := \ell'_i, t_j := \ell'_j]$ ,
- $\nu' = \nu[Y_i \cup \{j := 0\}]$ , and
- $\nu' \models I_i(\ell'_i) \wedge I_j(\ell'_j)$ .

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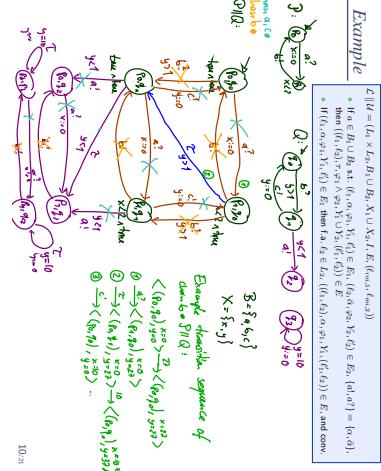
## Operational Semantics of Networks: Delay

### (iii) Delay transition:

- $\langle \vec{t}, \nu \rangle \overset{\lambda}{\rightarrow} \langle \vec{t}', \nu + t \rangle$  if for all  $t' \in [0, t]$ ,

- $\nu + t' \models \bigwedge_{k=1}^n I_k(t_k)$

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*Example*

- $L_1 = \{L_0 \cup L_2, B_1 \cup B_2, X_1 \cup X_2, Y_1, E, (f_{\min}, f_{\max}), \alpha\}$
- If  $a \in B_1 \cup B_2$ , s.t.  $(l_1, \alpha, \varphi_1, Y_1, f_1) \in L_1$ ,  $(l_2, \beta, \varphi_2, Y_2, f_2) \in L_2$ ,  $\{\alpha, \beta\} = \{\varphi_1, \varphi_2\}$ , then  $(l_1, l_2, \alpha, \varphi_1, \varphi_2, Y_1, f_1, Y_2, f_2) \in E_2$ .
- If  $(l_1, \alpha, \varphi_1, Y_1, f_1) \in E_1$ , then  $l_2 \in L_2$ ,  $(l_2, \beta, \varphi_2, Y_2, f_2) \in E_2$  and conv

Demo, Vol. I

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Upper Architecture

```

graph TD
    XML[XML] --> Simulation[Simulation]
    XML --> Validation[Validation]
    XML --> Analysis[Analysis]
    XML --> Reporting[Reporting]
    
    Simulation --> JUnit[JUnit]
    Simulation --> Sim[Sim]
    Validation --> JUnit
    Validation --> Sim
    Analysis --> JUnit
    Analysis --> Sim
    Reporting --> JUnit
    Reporting --> Sim
    
    JUnit --> JUnitOutput[JUnit Output]
    Sim --> SimOutput[Sim Output]
    JUnitOutput --- SimOutput
  
```

The diagram illustrates the Upper Architecture flow:

- Input:** XML
- Stages:**
  - Simulation:** Produces JUnit and Sim outputs.
  - Validation:** Produces JUnit and Sim outputs.
  - Analysis:** Produces JUnit and Sim outputs.
  - Reporting:** Produces JUnit and Sim outputs.
- Outputs:**
  - JUnit Output:** Summarizes JUnit results.
  - Sim Output:** Summarizes Sim results.
  - JUnit Output** and **Sim Output** are connected, indicating they are produced simultaneously.

Annotations in the diagram include:

- JUnit:** A bracket above the JUnit output boxes.
- Sim:** A bracket above the Sim output boxes.
- Validation:** A bracket above the Validation stage.
- Analysis:** A bracket above the Analysis stage.
- Reporting:** A bracket above the Reporting stage.
- XML:** A bracket above the XML input.
- plat-form, design, business:** A bracket above the Simulation stage.
- JUnit:** A label next to the JUnit output box.
- Sim:** A label next to the Sim output box.
- Validation:** A label next to the Validation stage.
- Analysis:** A label next to the Analysis stage.
- Reporting:** A label next to the Reporting stage.
- XML:** A label next to the XML input.
- plat-form, design, business:** A label next to the Simulation stage.
- JUnit** and **Sim** labels are placed above their respective output boxes.
- is G ready?** A question mark inside a box near the bottom right.

Uppaal Architecture

## References

- [Boehmann et al., 2004] Boehmnn, G., David, A., and Lusen, K. G. (2004). A tutorial on uppaal 2004-11-17. Technical report, Aalborg University, Denmark.

[Larsen et al., 1991] Larsen, K. G., Pettersson, P., and Yi, W. (1997). UPPAAL in a nutshell. *International Journal on Software Tools for Technology Transfer*, 1(1):154–152.

[Odeberg and Diers, 2008] Odeberg, E. R. and Diers, H. (2008). *Real-Time Systems [Formal Specification and Automatic Verification]*. Cambridge University Press.

### References