

Interpolation

Seminar Slides

Albert-Ludwigs-Universität Freiburg



UNI
FREIBURG

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27th June 2015

```
program add(int a, int b) {  
  var x,i : int;  
   $l_0$    assume(b  $\geq$  0);  
   $l_1$    x := a;  
   $l_2$    i := 0;  
  while(i < b) {  
     $l_3$      x := x + 1;  
     $l_4$      i := i + 1;  
  }  
  assert (x == a + b);  
}
```

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program add(int a, int b) {
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  var x,i : int;
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  assume(b ≥ 0);
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  x := a;
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l_err  assert (x != a + b);
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Prove correctness (CEGAR approach)

Idea: Show that all traces from l_0 to l_{err} are infeasible.

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  }
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```
  l_err assert (x != a + b);
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Prove correctness (CEGAR approach)

Idea: Show that all traces from l_0 to l_{err} are infeasible.

- 1 Choose an error trace τ .
- 2 Show that τ is infeasible.
- 3 Compute interpolants for τ .

A bit of history

Interpolation

What is an interpolant?

Interpolation in Propositional Logic

Interpolation in First-Order Logic

Conclusion

References

- W. Craig (1957), Linear reasoning. A new form of the Herbrand-Gentzen theorem

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- K. L. McMillan (2003), Interpolation and SAT-Based Model Checking

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- A. Cimatti et al. (2007), Efficient Interpolant Generation in SMT

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- $I \wedge B$ is unsatisfiable
- $I \preceq A$ and $I \preceq B$ (symbol condition)

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Ingredients

- 1 a pair of unsatisfiable formulae A, B
- 2 a resolution proof of their unsatisfiability

Interpolation in Propositional Logic

Resolution



Prove unsatisfiability of $\overbrace{P \wedge (\neg P \vee R)}^A \wedge \overbrace{\neg R}^B$

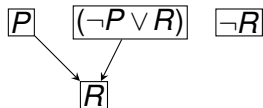
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Interpolation in Propositional Logic

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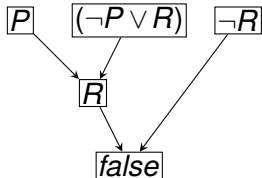


Interpolation in Propositional Logic

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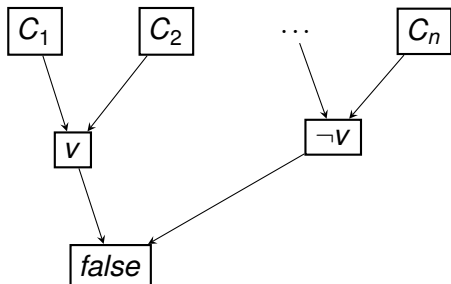


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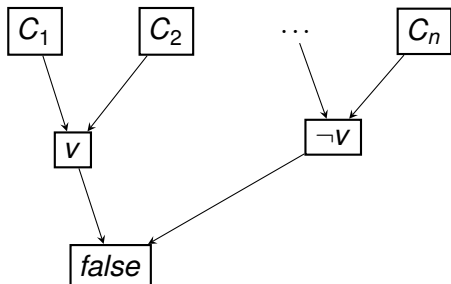
Interpolation in Propositional Logic

Given: unsatisfiable formulae A, B and a proof of unsatisfiability.



Interpolation in Propositional Logic

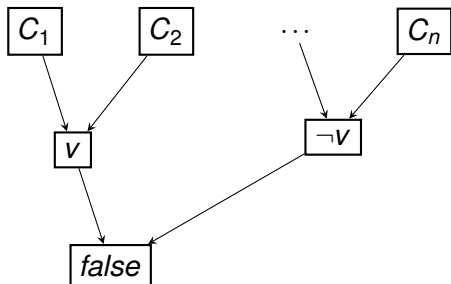
Given: unsatisfiable formulae A, B and a proof of unsatisfiability. For every vertex v of the proof define the interpolant $ITP(v)$ as follows:



Interpolation in Propositional Logic

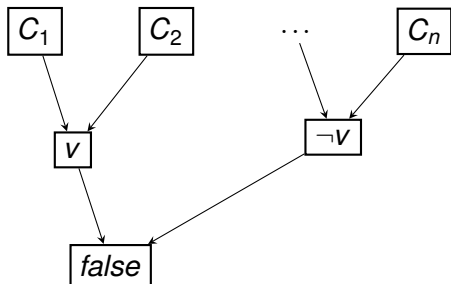
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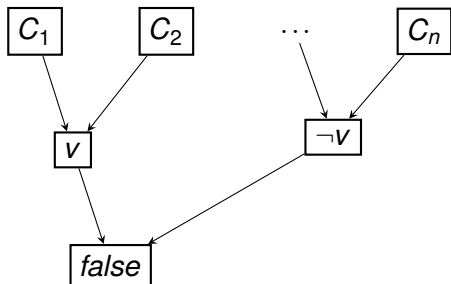
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- if v is an input node
 - 1 if $v \in A$ then $ITP(v) = \mathbf{global_literals}(v)$
 - 2 else $ITP(v) = \mathbf{true}$



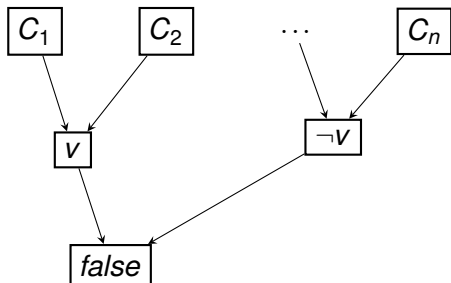
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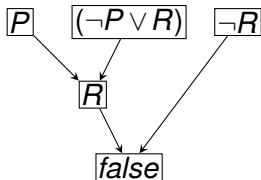
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- else v must have two predecessors v_1, v_2 and p_v is the pivot variable.
 - 1 if p_v is *local* to A , then
$$ITP(v) = ITP(v_1) \vee ITP(v_2)$$
 - 2 else $ITP(v) = ITP(v_1) \wedge ITP(v_2)$



Interpolation in Propositional Logic

Example

$$\text{Formula: } \overbrace{P \wedge (\neg P \vee R)}^A \wedge \overbrace{\neg R}^B$$



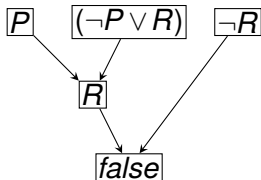
Interpolation in Propositional Logic

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$$\text{Formula: } \overbrace{P \wedge (\neg P \vee R)}^A \wedge \overbrace{\neg R}^B$$

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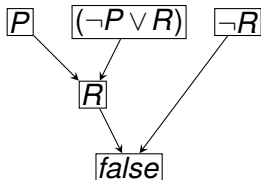
Interpolation in Propositional Logic

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$$\text{Formula: } \overbrace{P \wedge (\neg P \vee R)}^A \wedge \overbrace{\neg R}^B$$

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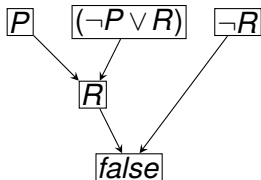
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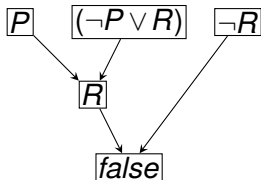
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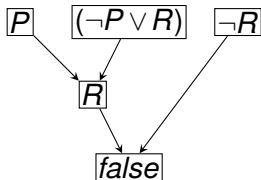


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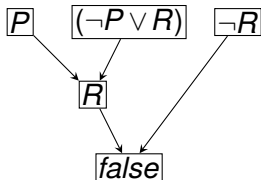
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Interpolation in Propositional Logic

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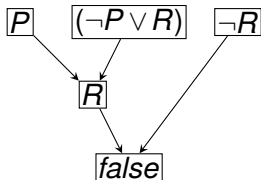


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The resulting interpolant:

$$ITP(false) = (FALSE \vee R) \wedge TRUE = R$$

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Interesting theories in practice



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- Linear Integer Arithmetic
- Presburger Arithmetic
- Equality Theory with Uninterpreted Functions
- Theory of Arrays
- Theory of Lists



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Requirements

- SAT-Solver (lazy)
- a theory solver (T -Solver)

SMT: Satisfiability Modulo Theory



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- 4 If a truth value is assigned to all variables \implies **SAT**
- 5 If no assignment left \implies **UNSAT**

SMT-SAT (lazy approach)

Illustration

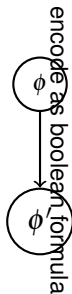


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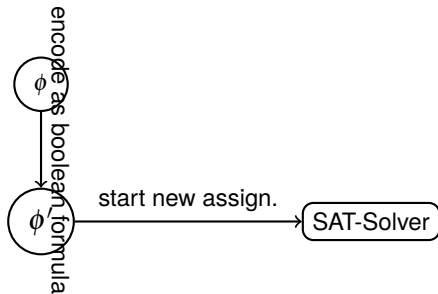
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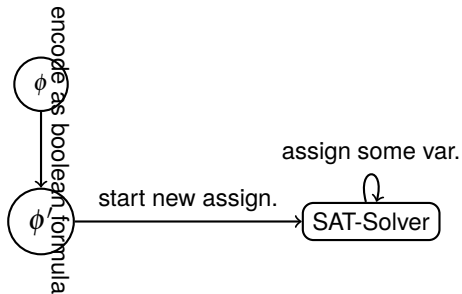
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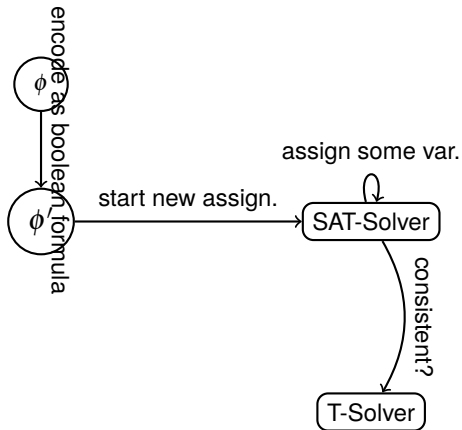
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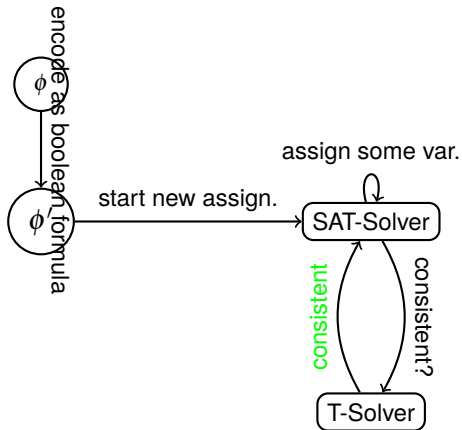
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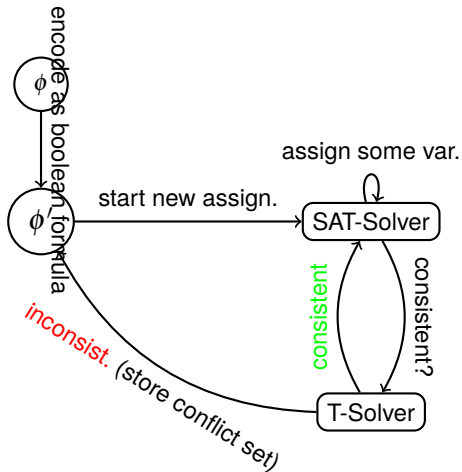
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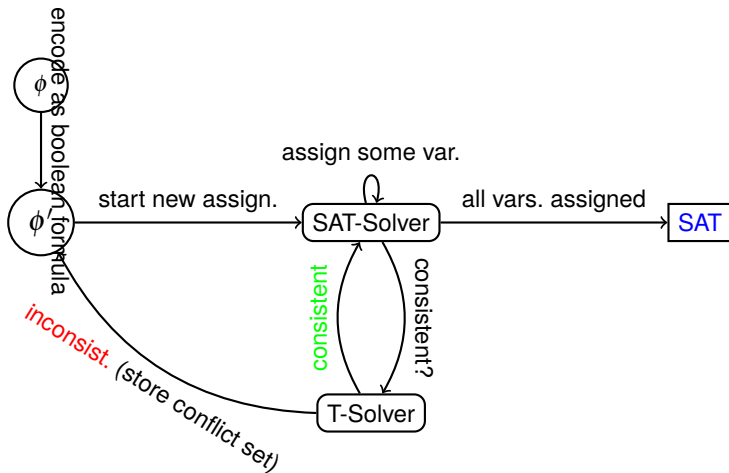
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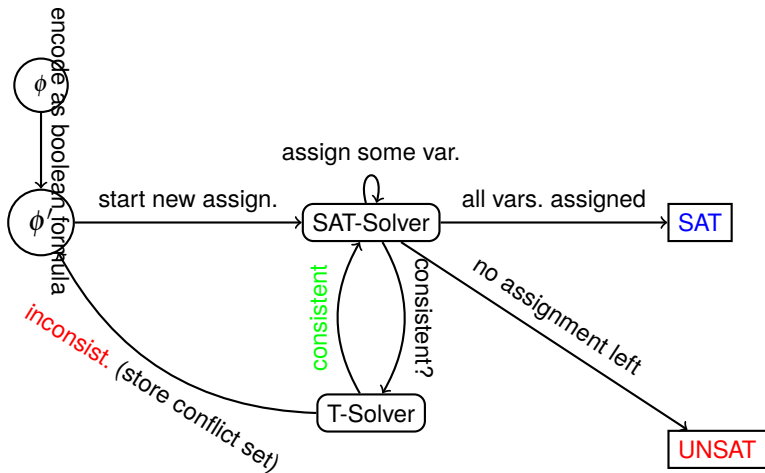
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Given two formulae $c_1 = \neg x_1 \vee x_2 \vee \neg x_3$ and $c_2 = x_2 \vee x_3$

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- $c_1 \downarrow c_2 = x_2 \vee \neg x_3$
- $c_1 \setminus c_2 = \neg x_1$

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- Output the interpolant at the root node, namely I_{\perp}

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- research in progress for other theories

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- Propositional Logic: resolution proof

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How does it work?

- Propositional Logic: resolution proof
- First-Order Logic: Resolution proof, Theory interpolation

A theory where no efficient interpolation algorithm exists

- theory of non-linear integer arithmetic (e.g. $x^2 + y^2 = 1$)

-  [A. Cimatti, A. Griggio, R. Sebastiani.](#)
Efficient Interpolant Generation in SMT.
-  [K.L.McMillan.](#)
Interpolation and SAT-based Model Checking.
-  [Philipp Rümmer](#)
Craig Interpolation in SAT and SMT
http://satsmt2014.forsyte.at/files/2014/01/interpolation_philipp.pdf
-  [D. Kroening, G. Weissenbacher .](#)
Lifting Propositional Interpolants to the Word-Level.



Wikipedia

Satisfiability Modulo Theories.

https://en.wikipedia.org/wiki/Satisfiability_Modulo_Theories