

NEXT TUTORIAL: MONDAY

Softwaretechnik / Software-Engineering

Lecture 13: Behavioural Software Modelling

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Communicating Finite Automata
presentation follows (Ollenberg and Dierks, 2009)

Contents of the Block "Design"

- (i) Introduction and Vocabulary
(a) Principles of Design
1) modularity
2) separation of concerns
3) information hiding and data encapsulation
4) abstract data types, object orientation
(ii) Software Modelling
1) state and transitions, the A-11 ship
2) model-driven/based software engineering
3) Unified Modeling Language (UML)
4) modelling structure
5) simplified class diagrams
6) simplified object diagrams
7) simplified object constraint logic (OCL)
(iii) modelling behaviour
1) communicating finite automata
2) signal hand shakings
3) handshake automata
4) an outlook on hierarchical state-machines
(iv) Design Patterns

Table with 2 columns: Topic and Page/Time. Topics include Introduction, Development Process, Metrics, Requirements Engineering, Architecture & Design, Software Modelling, Quality Assurance, Invited Talks, and Workshop.

Contents & Goals

- Last Lecture:
- Class diagrams, object diagrams, (Pre-)OCL
This Lecture:
- Educational Objectives: Capabilities for following tasks/questions
- What is a communicating finite automaton?
- Which two kind of transitions are considered in the CFA semantics?
- Given a network of CFA, what are its computation paths?
- Is this configuration / location reachable in the given CFA?
Content:
- Networks of Communicating Finite Automata
- Uppaal Demo
- Implementable CFA

Channel Names and Actions

To define communicating finite automata, we need the following sets of symbols:

- A set (alpha, beta, gamma) Chan of channel names or channels.
For each channel alpha in Chan, two visible actions:
alpha^? and alpha^! denote input and output on the channel (alpha^?, alpha^! not in Chan)
alpha not in Chan represents an internal action, not visible from outside.
(alpha, beta, gamma) Act := {alpha^? | alpha in Chan} union {alpha^! | alpha in Chan} union {tau} is the set of actions.

- An alphabet B is a set of channels, i.e. B subseteq Chan.
For each alphabet B, we define the corresponding action set
B^pi := {alpha^? | alpha in B} union {alpha^! | alpha in B} union {tau}.
Note: Chan^pi = Act.

Integer Variables and Expressions, Resets

- Let (v1, v2, ..., vn) V be a set of ((finite domain) integer) variables.
By (var in V) W(V) we denote the set of Integer expressions over V using function symbols +, -, ..., <, >, <=, >=, ...
A modification on v is
v := var1, ..., varn v in V, var in W(V).

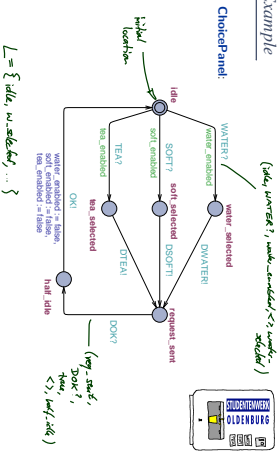
- By R(V) we denote the set of all modifications.
By F we denote a finite list (r1, ..., rn), n in N0, of modifications
r1 in R(V), empty list (n = 0)
By R(V)* we denote the set of all such finite lists of modifications.

Definition: A communicating finite automaton is a structure $A = (L, B, V, E, l_{in})$ where

- $(\emptyset \neq L)$ is a finite set of locations (or control states),
- $B \subseteq \text{Chan}$,
- V : a set of data variables,
- $E \subseteq L \times B \times \Phi(V) \times R(V)^* \times L$: a set of directed edges such that $(\alpha, \varphi, \tau, \beta) \in E \wedge \text{Chan}(\alpha) \in U \implies \varphi = \text{true}$

Edges $(\alpha, \varphi, \tau, \beta)$ from location α to β are labeled with an action α, β guarded φ , and a list τ of modifications.

l_{in} is the initial location.



Helpers: Extended Valuations and Effect of Resets

- $v : V \rightarrow \mathcal{D}(V)$ is a valuation of the variables.
- A valuation v of the variables canonically assigns an integer value $v(i)$ to each integer expression $\varphi \in \Phi(V)$.
- $\models \subseteq (V \rightarrow \mathcal{D}(V)) \times \Phi(V)$ is the canonical satisfaction relation between valuations and integer expressions from $\Phi(V)$.

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- Effect of modification $r \in R(V)$ on v , denoted by $v[r]$:**

$$v[r](i) := \mathcal{D}(i) = \begin{cases} v(i), & \text{if } i = i_1 \\ v(x_1 + 1), & \text{if } i = x_1 \\ v(x_1 - 1), & \text{if } i = x_2 \\ v(i), & \text{otherwise} \end{cases}$$
- We set $v[(r_1, \dots, r_n)] := v[r_1] \dots [r_n] = (((v[r_1])[r_2]) \dots [r_n])$. That is, modifications are executed sequentially from left to right.

Definition. Let $A_i = (L_i, B_i, V_i, E_i, l_{in,i}), 1 \leq i \leq n$, be communicating finite automata.

The operational semantics of the network of FCA $\mathcal{C}(A_1, \dots, A_n)$ is the labelled transition system **configuration nodes** **labelled transitions** $\mathcal{T}(\mathcal{C}(A_1, \dots, A_n)) = (\text{Conf}, \text{Chan} \cup \{\tau\}, \{\Delta\} \times \text{Chan} \cup \{\tau\}, C_{in})$ where

- $V = \bigcup_{i=1}^n V_i$ is a variable set V
- $\text{Conf} = \{(L, v) \mid L_i \in L_i, v : V \rightarrow \mathcal{D}(V)\}$, **configuration**
- $C_{in} = \{l_{in,1}, v_{in,1}\}$ with $v_{in,i}(v) = 0$ for all $v \in V$.

The transition relation consists of transitions of the following two types.

Operational Semantics of Networks of FCA

- An internal transition** $(\vec{l}, v) \xrightarrow{\tau} (\vec{l}', v')$ occurs if there exists $i \in \{1, \dots, n\}$ and
 - there is a τ -edge $(l_i, \tau, \varphi, \tau', \beta_i) \in E_i$ such that
 - $v \models \varphi$, **same valuation, satisfies guard**
 - $\tau' = \beta_i$, **substitution, i changes location**
 - $v' = v[\beta_i]$, **"v" is v modified by \beta_i**
 - A synchronisation transition** $(\vec{l}, v) \xrightarrow{\tau} (\vec{l}', v')$ occurs if there are $i, j \in \{1, \dots, n\}$ with $i \neq j$ and
 - there are edges $(l_i, \tau, \varphi, \beta_i, l'_i) \in E_i$ and $(l_j, \tau', \varphi', \beta_j, l'_j) \in E_j$ such that
 - $v \models \varphi \wedge \varphi'$,
 - $\tau' = \beta_i$, **same edge, this sync**
 - $v' = v[\beta_i][\beta_j]$,
 - $v' = v[\beta_i][\beta_j]$.
- This style of communication is known under the names "rendezvous", "synchronisation", "blocking" communication (and possibly many others).

Transition Sequences, Reachability

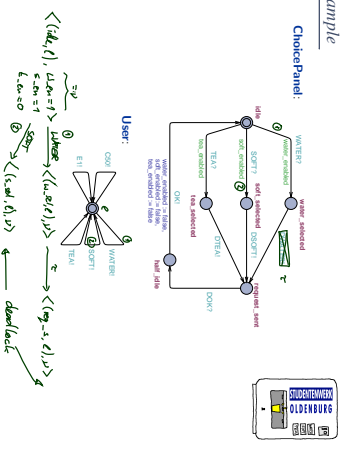
- A **transition sequence** of $C(A_1, \dots, A_n)$ is any (in)finite sequence of the form

$$\langle \ell_0, \nu_0 \rangle \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle \xrightarrow{\lambda_2} \langle \ell_2, \nu_2 \rangle \xrightarrow{\lambda_3} \dots$$
 with
 - $\langle \ell_0, \nu_0 \rangle = C_{init}$
 - for all $i \in \mathbb{N}$, there is $\lambda_{i+1} \in \mathcal{T}(C(A_1, \dots, A_n))$ with $\langle \ell_i, \nu_i \rangle \xrightarrow{\lambda_{i+1}} \langle \ell_{i+1}, \nu_{i+1} \rangle$
- A configuration $\langle \ell, \nu \rangle$ is called **reachable** (in $C(A_1, \dots, A_n)$) if and only if there is a transition sequence of the form

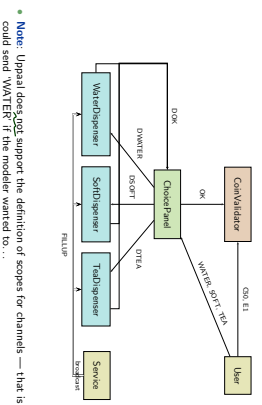
$$\langle \ell_0, \nu_0 \rangle \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle \xrightarrow{\lambda_2} \langle \ell_2, \nu_2 \rangle \xrightarrow{\lambda_3} \dots \xrightarrow{\lambda_n} \langle \ell_n, \nu_n \rangle = \langle \ell, \nu \rangle$$
- A location ℓ is called **reachable** if and only if any configuration $\langle \ell, \nu \rangle$ is reachable, i.e. there exists a valuation ν such that $\langle \ell, \nu \rangle$ is reachable.
- The network $C(A_1, \dots, A_n)$ is said to have a **deadlock** if and only if there is a configuration $\langle \ell, \nu \rangle$ such that

$$\exists \lambda \in \mathcal{T}(C(A_1, \dots, A_n)), \langle \ell, \nu \rangle \in \text{Conf} \bullet \langle \ell, \nu \rangle \xrightarrow{\lambda} \langle \ell, \nu \rangle$$
 (i.e. $\langle \ell, \nu \rangle$ is a **deadlock**)

Example



Model Architecture — Who Talks What to Whom



Note: Uppaal does *not* support the definition of scopes for channels — that is, 'Service' could send 'WAITER' if the modeller wanted to...

A CPA Model Is Software

Definition. Software is a finite description S of a (possibly infinite) set $[S]$ of (finite or infinite) computation paths of the form

$$r_0 \xrightarrow{\alpha_1} r_1 \xrightarrow{\alpha_2} r_2 \dots$$

where

- $r_i \in \mathcal{S}$, i.e. R_0 , is called **state** (for configuration), and
- $\alpha_i \in A$, i.e. R_0 , is called **action** (for event).

The (possibly partial) function $[\cdot] : S \rightarrow [S]$ is called **interpretation** of S .

- Let $C(A_1, \dots, A_n)$ be a network of CPA.
- $\mathcal{S} = \text{Conf}$
- $A = \text{Chan} \cup \{ \tau \}$
- $[C] = \{ \pi = \langle \ell_0, \nu_0 \rangle \xrightarrow{\lambda_1} \langle \ell_1, \nu_1 \rangle \xrightarrow{\lambda_2} \langle \ell_2, \nu_2 \rangle \dots \mid \pi \text{ is a computation path of } C \}$
- Note: the structural model just consists of the set of variables and the locations of C .

Uppaal

(Larsen et al., 1997; Behrmann et al., 2004)

CPA Model-Checking

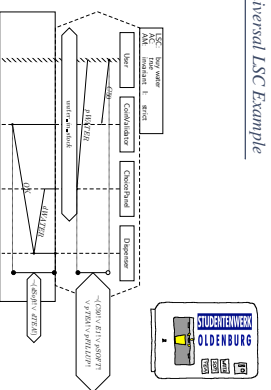
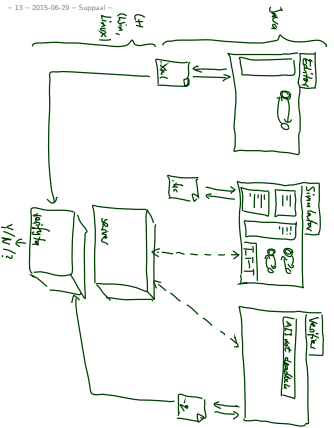
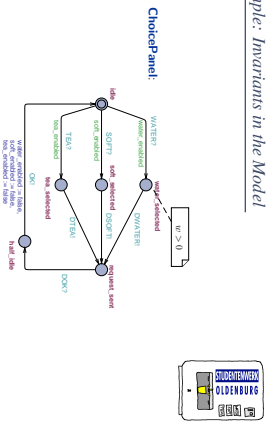
Definition. The **model-checking problem** for a network C of communicating finite automata and a query F is to decide whether

$$\langle C, F \rangle \in \mathbb{F}$$

$$C \models F$$

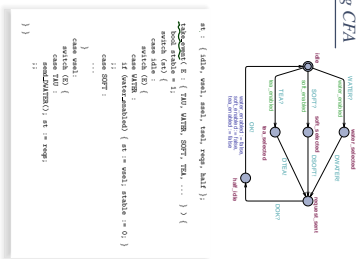
$$\forall F \in \mathcal{F} \bullet \bullet \bullet$$

Proposition. The model-checking problem for communicating finite automata is decidable.

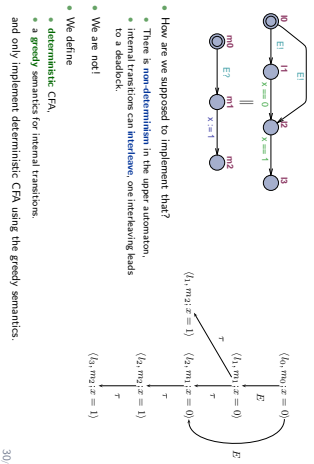


Implementing Communicating Finite Automata

Implementing CFA



Would be Too Easy...

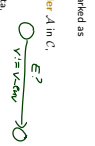


- How are we supposed to implement that?
- There is non-determinism in the upper automaton,
- internal transitions can interfere, one interfering leads to a deadlock.
- We are not!
- We define
- deterministic CFA,
- a greedy semantics for internal transitions,
- and only implement deterministic CFA using the greedy semantics.

- The communicating finite automaton $\mathcal{A} = (L, B, V, E, f_{in})$ is called **deterministic** if and only if
 - for each location l ,
 - either all edges with l as source location have pairwise different input actions,
 - or there is no edge with an input action starting at l ,
 - and all edges starting at l have pairwise (logically) disjoint guards.



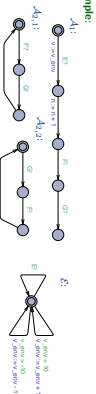
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 - for each location l ,
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 - or there is no edge with an input action starting at l ,
 - and all edges starting at l have pairwise (logically) disjoint guards.
 - Let each automaton in the network $(\mathcal{A}_1, \dots, \mathcal{A}_n)$ be marked as either **environment** or **controller**.
- We call \mathcal{C} **implementable** if and only if, for each controller \mathcal{A} in \mathcal{C} ,
- \mathcal{A} is deterministic;
 - \mathcal{A} has no self-loops, the local variables $V_{\mathcal{A}}$ and the set of variables written by **environment** automata, but only in modification vectors of edges with input synchronisation;
 - \mathcal{A} is **locally deadlock free**, i.e. enabled edges with output actions are not blocked forever.
- **Note:** implementable (i) and (ii) can be checked syntactically. Property (iii) is a property of the whole network. Can be checked with Uppaal: $(\mathcal{A} \wedge \mathcal{C}) \rightarrow (\mathcal{A})$



Model vs. Implementation

- Now an implementable model $(\mathcal{A}_1, \dots, \mathcal{A}_n)$ has **two semantics**:
 - $[C]_{ind}$ — standard semantics
 - $[C]_{ind}$ — greedy semantics
- Are they **related** in any way?

- **Greedy semantics**:
 - each input synchronisation transition (give system start) of automaton \mathcal{A} is followed by a maximal sequence of internal transitions or output transitions of \mathcal{A} .
 - **Maximal**: cannot be extended by an internal transition.
- There may still be interleaving of the internal transitions, but (by forbidding shared variables for controllers) cannot be observed outside of an automaton.



References

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