

Softwaretechnik / Software-Engineering

Lecture 15: Software Quality Assurance

2015-07-09

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Contents of the Block “Quality Assurance”

(i) **Introduction and Vocabulary**

- correctness illustrated
- vocabulary: fault, error, failure
- three basic approaches

(ii) **Formal Verification**

- Hoare calculus
- Verifying C Compiler (VCC)
- over- / under-approximations

(iii) **(Systematic) Tests**

- systematic test vs. experiment
- classification of test procedures
- model-based testing
- glass-box tests: coverage measures

(iv) **Runtime Verification**

(v) **Review**

(vi) **Concluding Discussion**

- Dependability

Introduction	L 1:	20.4., Mo
Development Process, Metrics	T 1:	23.4., Do
Requirements Engineering	L 2:	27.4., Mo
	L 3:	30.4., Do
	L 4:	4.5., Mo
	T 2:	7.5., Do
	L 5:	11.5., Mo
	-	14.5., Do
	L 6:	18.5., Mo
	L 7:	21.5., Do
	-	25.5., Mo
	-	28.5., Do
Architecture & Design, Software Modelling	T 3:	1.6., Mo
	-	4.6., Do
	L 8:	8.6., Mo
	L 9:	11.6., Do
	L 10:	15.6., Mo
Quality Assurance	T 4:	18.6., Do
	L 11:	22.6., Mo
	L 12:	25.6., Do
	L 13:	29.6., Mo
	L 14:	2.7., Do
Invited Talks	T 5:	6.7., Mo
	L 15:	9.7., Do
	L 16:	13.7., Mo
	L 17:	16.7., Do
Wrap-Up	T 6:	20.7., Mo
	L 18:	23.7., Do

Contents & Goals

Last Lecture:

- Completed the block “Architecture & Design”

This Lecture:

- **Educational Objectives:** Capabilities for following tasks/questions.

- When do we call a software correct?
- What is fault, error, failure? How are they related?
- What is ~~format~~^{total} and partial correctness?
- What is a Hoare triple (or correctness formula)?
- Is this program (partially) correct?
- Prove the (partial) correctness of this WHILE-program using PD.
- What can we conclude from the outcome of tools like VCC?

- **Content:**

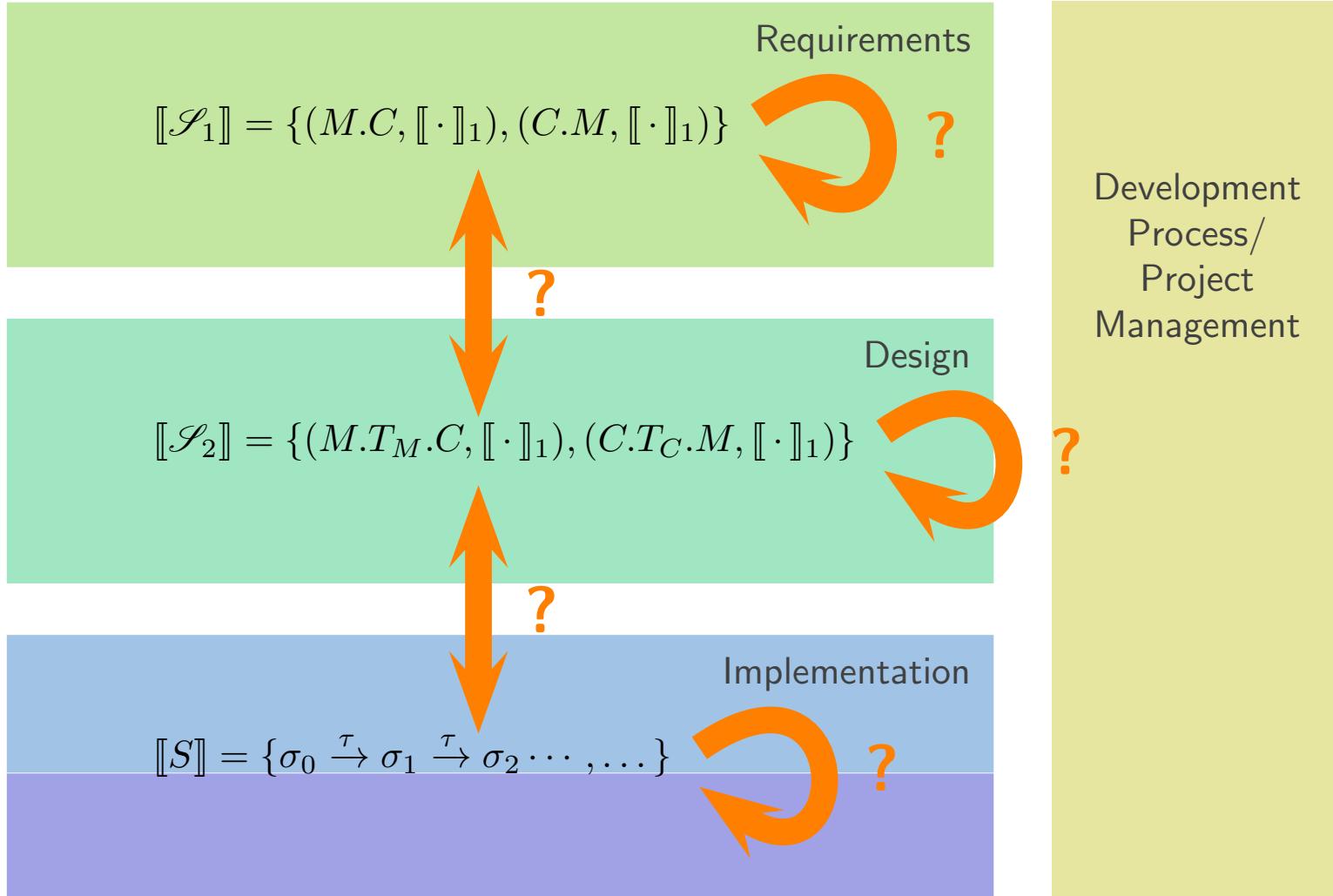
- Introduction, Vocabulary
- WHILE-program semantics, partial & total correctness
- Correctness proofs with the calculus PD.
- The Verifying C Compiler (VCC)

Introduction

Recall: Formal Software Development



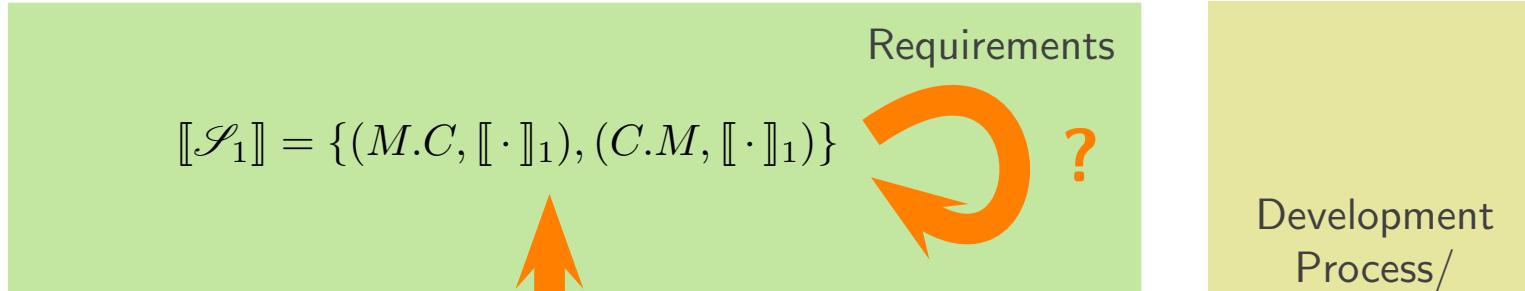
Mmmh,
Software!



Recall: Formal Software Development



Mmmh,
Software!



validation The process of evaluating a system or component during or at the end of the development process to determine whether it satisfies specified requirements. Contrast with: **verification**.

IEEE 610.12 (1990)

verification

- (1) The process of evaluating a system or component to determine whether the products of a given development phase satisfy the conditions imposed at the start of that phase. Contrast with: **validation**.
- (2) Formal proof of program correctness.

IEEE 610.12 (1990)

Recall: Formal Software Development

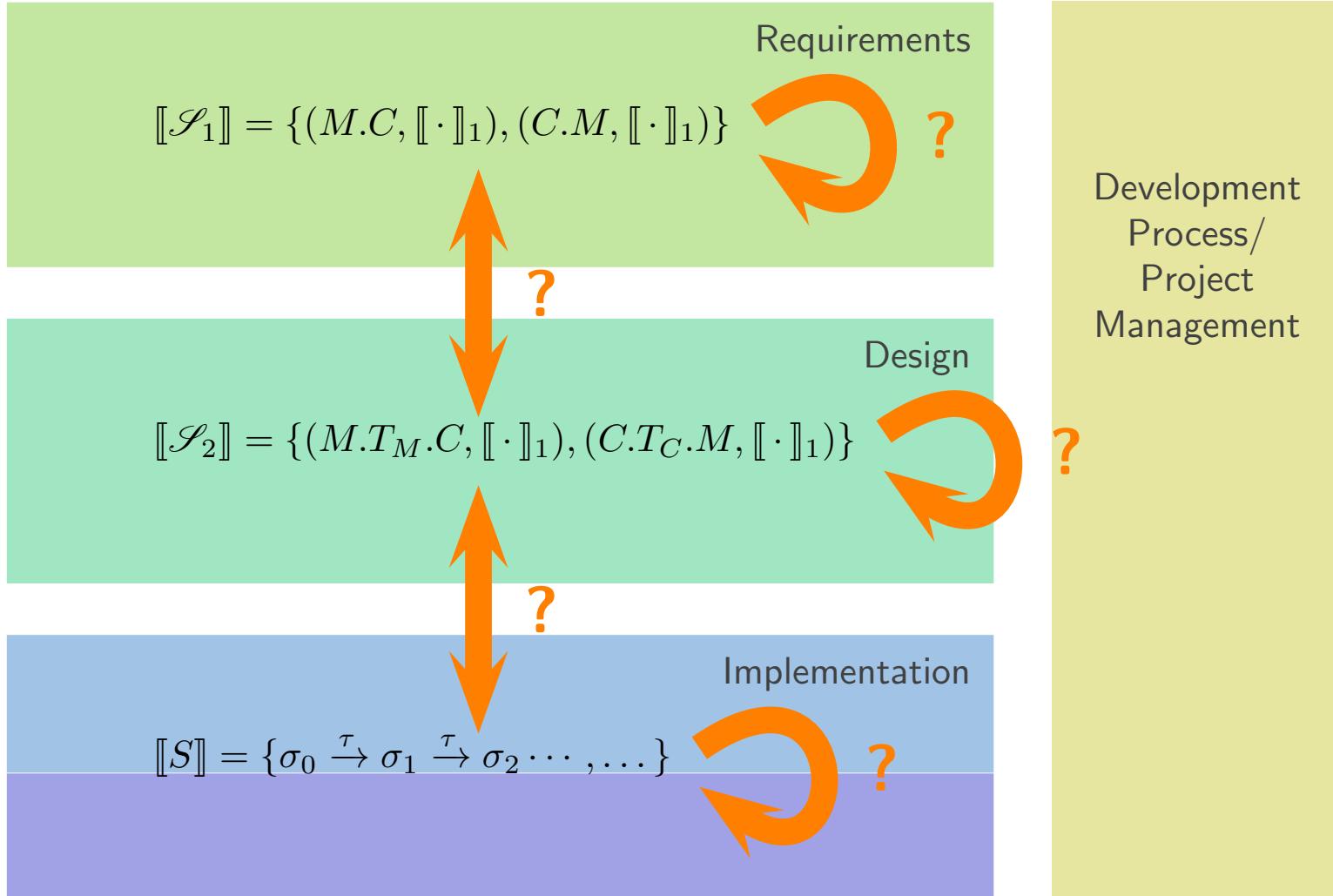


Mmmh, Software!

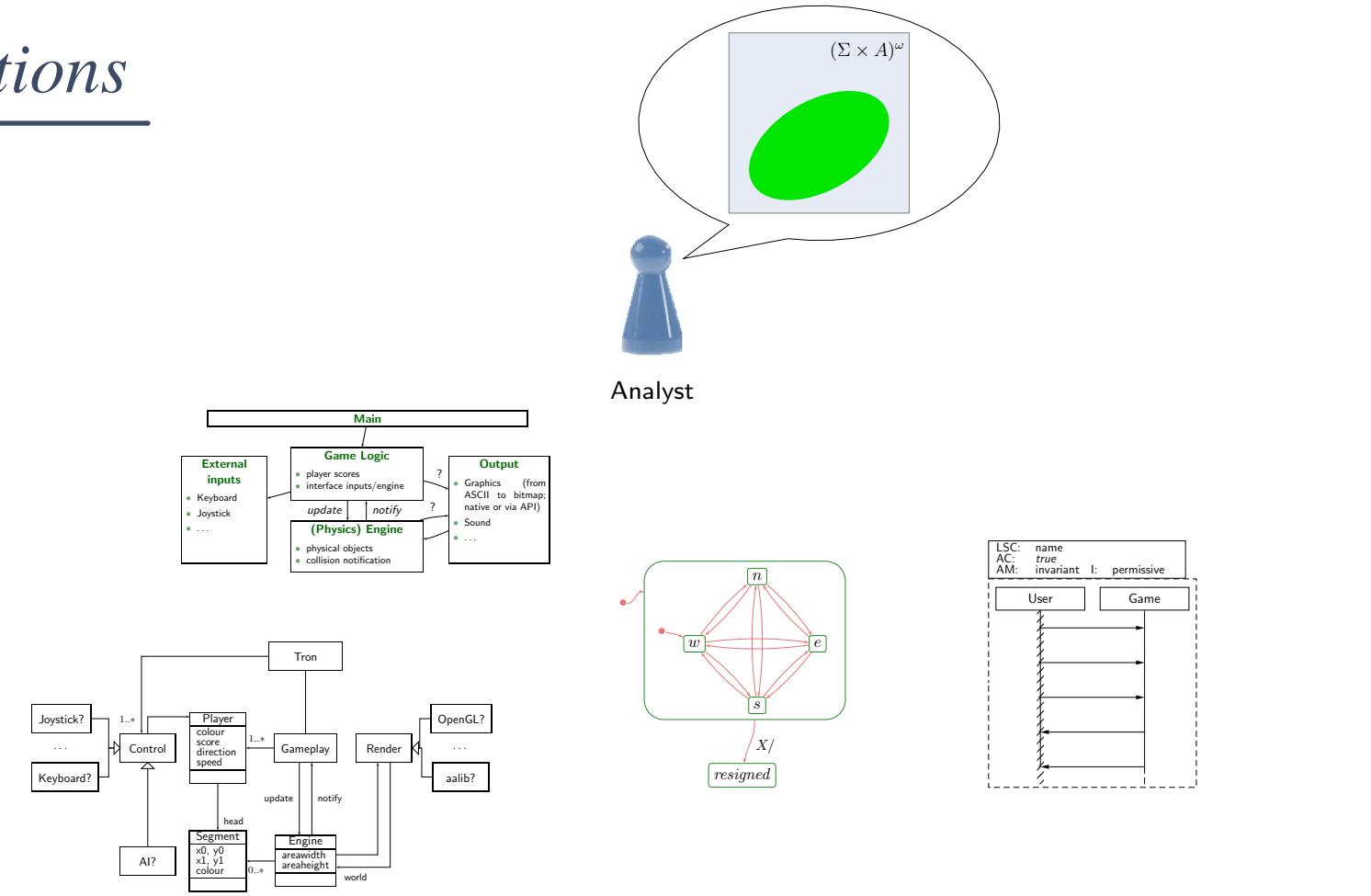
validation The process of evaluating a system or component during or at the end of the development process to determine whether it satisfies specified requirements. Contrast with: **verification**.
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verification

(1) The process of evaluating a system or component to determine whether the products of a given development phase satisfy the conditions imposed at the start of that phase. Contrast with: **validation**.
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IEEE 610.12 (1990)



Big Questions



Is the implementation “correct”? And “correct” in what sense?

Definition. A **software specification** is a finite description \mathcal{S} of a (possibly infinite) set $\llbracket \mathcal{S} \rrbracket$ of softwares, i.e.

$$\llbracket \mathcal{S} \rrbracket = \{(S_1, \llbracket \cdot \rrbracket_1), \dots\}.$$

The (possibly partial) function $\llbracket \cdot \rrbracket : \mathcal{S} \mapsto \llbracket \mathcal{S} \rrbracket$ is called **interpretation** of \mathcal{S} .

We define:

Software S is **correct** wrt. **software specification** \mathcal{S} if and only if $(S, \llbracket \cdot \rrbracket) \in \llbracket \mathcal{S} \rrbracket$.

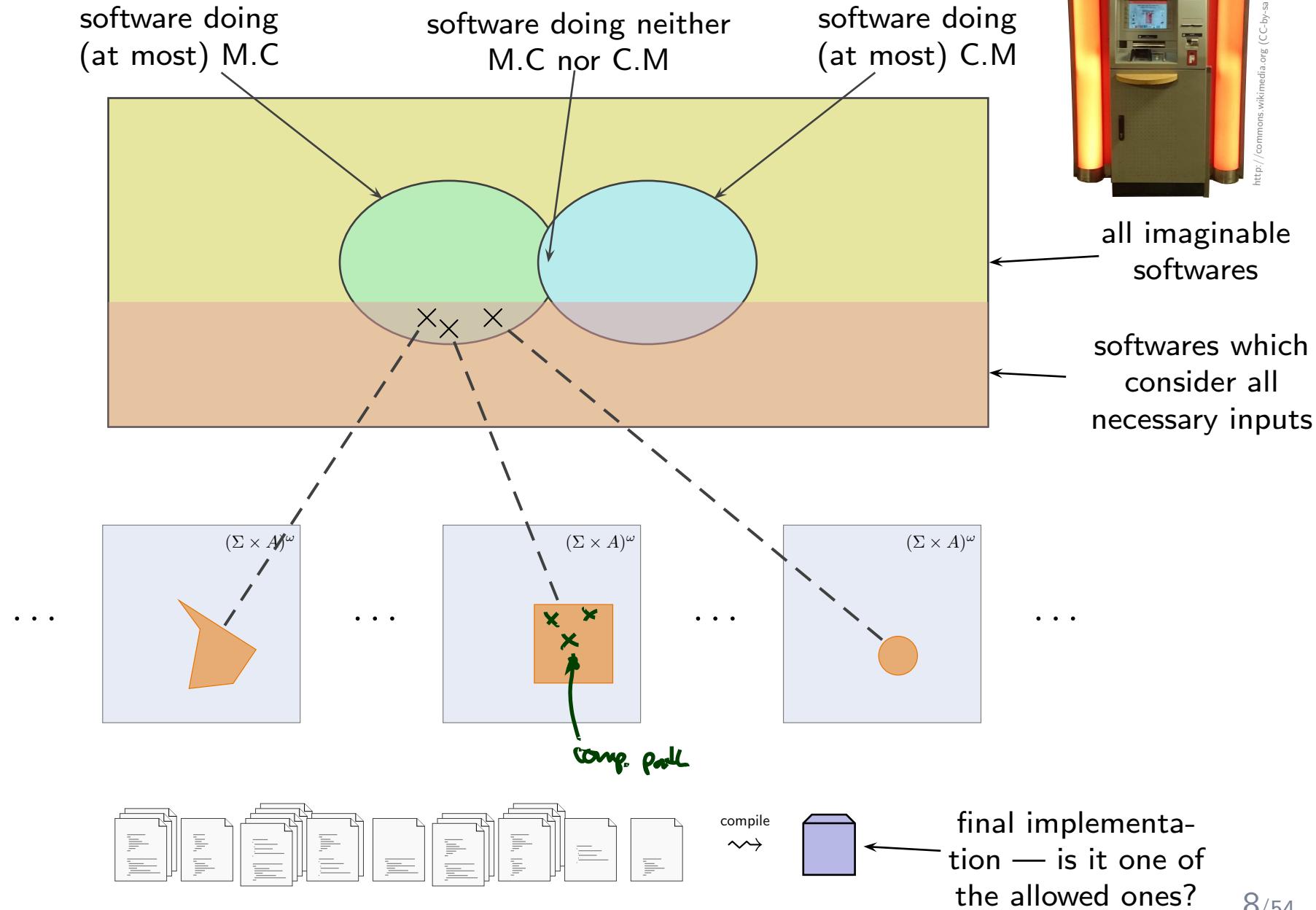
- **Note:** no specification, no correctness. Without specification, S is neither correct nor not correct — it's just some software then.

Correctness Illustrated



http://commons.wikimedia.org (CC-by-sa 4.0, Dirk Ingó Franke)

$$\mathcal{S} = (M.C) \text{ or } (C.M)$$



Vocabulary

software quality assurance — See: quality assurance. **IEEE 610.12 (1990)**

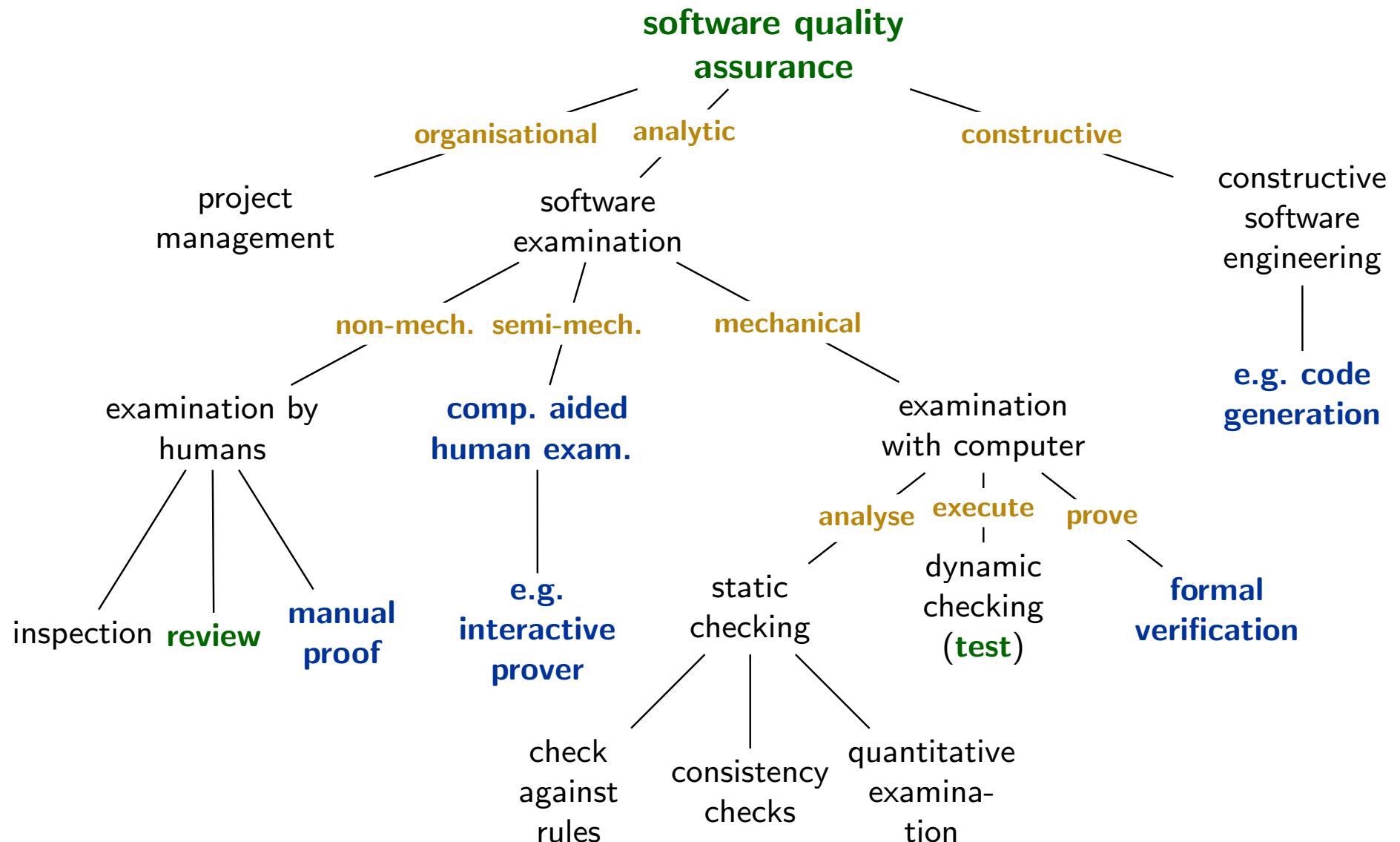
quality assurance — (1) A planned and systematic pattern of all actions necessary to provide adequate confidence that an item or product conforms to established technical requirements.

(2) A set of activities designed to evaluate the process by which products are developed or manufactured.

IEEE 610.12 (1990)

Note: in order to trust a product, it can be **built** well, or **proven** to be good (at best: both) — both is QA in the sense of (1).

Concepts of Software Quality Assurance



(Ludewig and Licher, 2013)

Fault, Error, Failure

fault — abnormal condition that can cause an element or an item to fail.

Note: Permanent, intermittent and transient **faults** (especially soft-errors) are considered.

Note: An **intermittent fault** occurs time and time again, then disappears. This type of fault can occur when a component is on the verge of breaking down or, for example, due to a glitch in a switch. Some **systematic faults** (e.g. timing marginalities) could lead to intermittent faults.

ISO 26262 (2011)

error — discrepancy between a computed, observed or measured value or condition, and the true, specified, or theoretically correct value or condition.

Note: An error can arise as a result of unforeseen operating conditions or due to a **fault** within the system, subsystem or, component being considered.

Note: A fault can manifest itself as an error within the considered element and the error can ultimately cause a **failure**.

ISO 26262 (2011)

failure — termination of the ability of an element, to perform a function as required.

Note: Incorrect specification is a source of failure.

ISO 26262 (2011)

We want to avoid **failures**, thus we try to detect **faults**, e.g. by looking for **errors**.

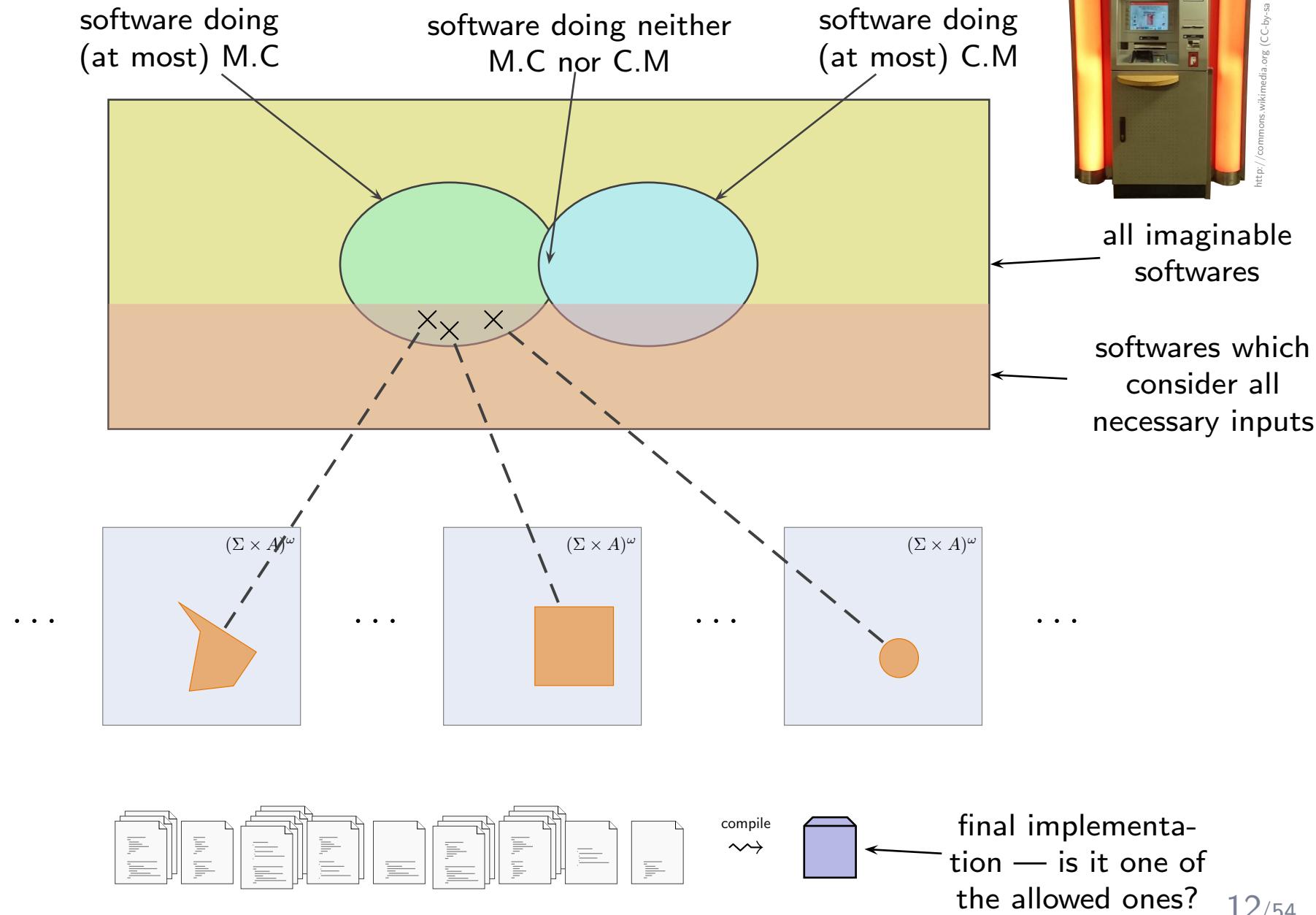
Back to the Illustration



all imaginable softwares

softwares which consider all necessary inputs

$$\mathcal{S} = (M.C) \text{ or } (C.M)$$



So, What Do We Do?

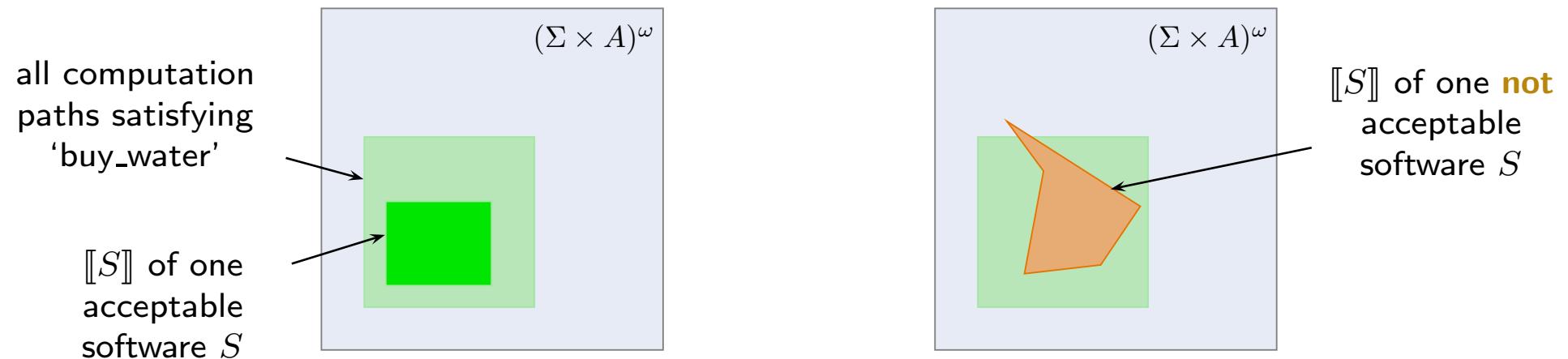
- If we are lucky, the requirement specification is a constraint on **computation paths**.
- LSC ‘buy_water’ is such a software specification \mathcal{S} .

• It denotes all controller softwares which “faithfully” sell water.
(Or which refuse to accept C50 coins, or block the ‘WATER’ button).

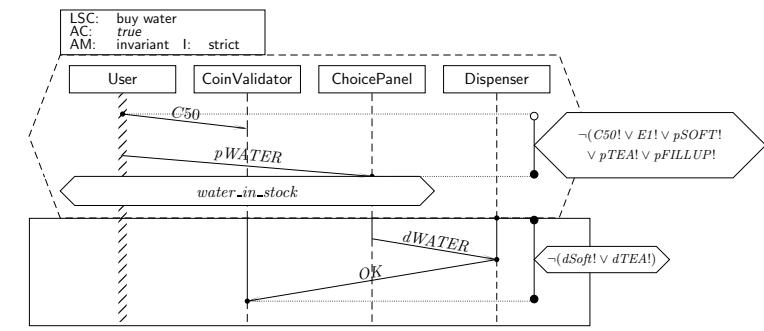
- Formally

$$[\![\text{buy_water}]\!]_{\text{spec}} = \{S \mid [\![S]\!] \text{ satisfies ‘buy_water’}\}.$$

- In pictures:

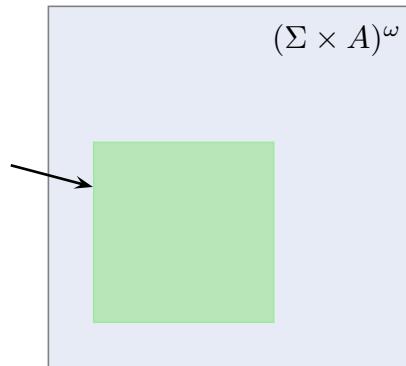


- Then we can check correctness of a given software S by examining its computation paths $[\![S]\!]$.

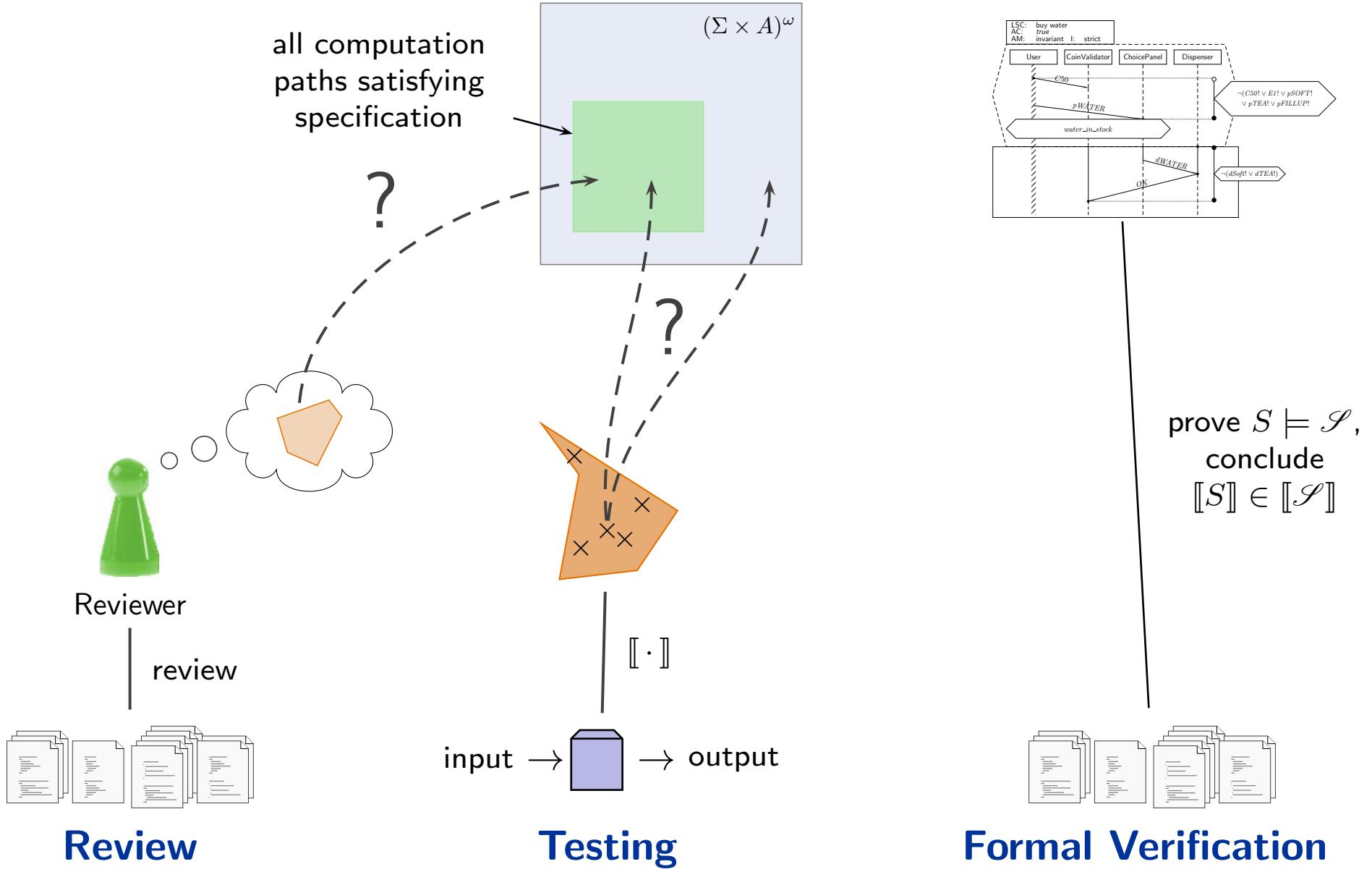


Three Basic Directions

all computation
paths satisfying
specification



Three Basic Directions



Formal Verification

Correctness Formulae (“Hoare Triples”)

- One style of requirements specifications: pre- and post-conditions (on whole programs or on procedures).
- Let S be a program with states from Σ and let p and q be formulae such that there is a satisfaction relation $\models \subseteq \Sigma \times \{p, q\}$.
*correctness formula,
Hoare triple*
- S is called **partially correct** wrt. p and q , denoted by $\models \{p\} S \{q\}$, if and only if
$$\forall \pi = \sigma_0 \xrightarrow{\alpha_1} \sigma_1 \xrightarrow{\alpha_2} \sigma_2 \cdots \sigma_{n-1} \xrightarrow{\alpha_n} \sigma_n \in \llbracket S \rrbracket \bullet \sigma_0 \models p \implies \sigma_n \models q$$

(“if S terminates from a state satisfying p , then the final state of that computation satisfies q ”)
- S is called **totally correct** wrt. p and q , denoted by $\models_{tot} \{p\} S \{q\}$, if and only if
 - $\{p\} S \{q\}$ (S is partially correct), and
 - $\forall \pi \in \llbracket S \rrbracket \bullet \pi^0 \models p \implies |\pi| \in \mathbb{N}_0$
(S terminates from all states satisfying p ; length of paths: $|\cdot| : \Pi \rightarrow \mathbb{N}_0 \dot{\cup} \{\perp\}$).

Example

Computing squares (of numbers $0, \dots, 27$).

- Pre-condition: $p \equiv 0 \leq x \leq 27$, post-condition: $q \equiv y = x^2$.

- Program S_1 :

$$\models^? \{p\} S_1 \{q\}, \models_{tot}^? \{p\} S_1 \{q\}$$

```
1 int y = x;  
2 y = (x - 1) * x + y;
```

- Program S_2 :

$$\models^? \{p\} S_2 \{q\}, \models_{tot}^? \{p\} S_2 \{q\}$$

```
1 int y = x;  
2 int z; // uninitialized  
3 y = ((x - 1) * x + y) + z;
```

- Program S_3 :

$$\models^? \{p\} S_3 \{q\}, \models_{tot}^? \{p\} S_3 \{q\}$$

```
1 int y = x;  
2 y = (x - 1) * x + y;  
3 while (1);
```

- Program S_4 :

$$\models^? \{p\} S_4 \{q\}, \models_{tot}^? \{p\} S_4 \{q\}$$

```
1 int y = x;  
2 int z; // uninitialized  
3 y = ((x - 1) * x + y) + z;  
4 while (z);
```

7:1
No YES

Deterministic Programs

Syntax:

$$S := \text{skip} \mid u := t \mid S_1; S_2 \mid \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi} \mid \text{while } B \text{ do } S_1 \text{ do}$$

where u is a variable, t a type-compatible expression, B a Boolean expression.

Semantics: (is induced by the following transition relation)

- (i) $\langle \text{skip}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$
- (ii) $\langle u := t, \sigma \rangle \rightarrow \langle E, \sigma[u := \sigma(t)] \rangle$
- (iii)
$$\frac{\langle S_1, \sigma \rangle \rightarrow \langle S_2, \tau \rangle}{\langle S_1; S, \sigma \rangle \rightarrow \langle S_2; S, \tau \rangle}$$
- (iv) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_1, \sigma \rangle$, if $\sigma \models B$,
- (v) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_2, \sigma \rangle$, if $\sigma \not\models B$,
- (vi) $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle S; \text{while } B \text{ do } S \text{ do}, \sigma \rangle$, if $\sigma \models B$,
- (vii) $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$, if $\sigma \not\models B$,

E denotes the empty program; define $\underbrace{E; S}_{\equiv} \equiv \underbrace{S; E}_{\equiv} \equiv \underbrace{S}_{\equiv}$.

Note: the first component of $\langle S, \sigma \rangle$ is a program (structural operational semantics).

Computations of Deterministic Programs

Definition.

- (i) A **transition sequence** of S (starting in σ) is a finite or infinite sequence

$$\langle S, \sigma \rangle = \langle S_0, \sigma_0 \rangle \rightarrow \langle S_1, \sigma_1 \rangle \rightarrow \dots$$

(that is, $\langle S_i, \sigma_i \rangle$ and $\langle S_{i+1}, \sigma_{i+1} \rangle$ are in transition relation for all i).

- (ii) A **computation (path)** of S (starting in σ) is a **maximal** transition sequence of S (starting in σ), i.e. infinite or not extendible.

- (iii) A computation of S is said to

- terminate** in τ if and only if it is finite and ends with $\langle E, \tau \rangle$,
- diverge** if and only if it is infinite. S **can diverge from** σ if and only if there is a diverging computation starting in σ .

- (iv) We use \rightarrow^* to denote the transitive, reflexive closure of \rightarrow .

Lemma. For each deterministic program S and each state σ , there is exactly one computation of S which starts in σ .

Example

- | | |
|--|-----------------------------|
| <ul style="list-style-type: none">(i) $\langle \text{skip}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$(ii) $\langle u := t, \sigma \rangle \rightarrow \langle E, \sigma[u := \sigma(t)] \rangle$(iii) $\frac{\langle S_1, \sigma \rangle \rightarrow \langle S_2, \tau \rangle}{\langle S_1; S, \sigma \rangle \rightarrow \langle S_2; S, \tau \rangle}$(iv) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_1, \sigma \rangle, \text{ if } \sigma \models B,$(v) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_2, \sigma \rangle, \text{ if } \sigma \not\models B,$(vi) $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle S; \text{while } B \text{ do } S \text{ do}, \sigma \rangle, \text{ if } \sigma \models B,$(vii) $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle E, \sigma \rangle, \text{ if } \sigma \not\models B,$ | $E; S \equiv S; E \equiv S$ |
|--|-----------------------------|

Consider **program** $S \equiv \underline{a[0] := 1; a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}}$
and a **state** σ with $\sigma \models x = 0$.

Example

- | | |
|--|--|
| <ul style="list-style-type: none">(i) $\langle \text{skip}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$(ii) $\langle u := t, \sigma \rangle \rightarrow \langle E, \sigma[u := \sigma(t)] \rangle$(iii) $\frac{\langle S_1, \sigma \rangle \rightarrow \langle S_2, \tau \rangle}{\langle S_1; S, \sigma \rangle \rightarrow \langle S_2; S, \tau \rangle}$(iv) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_1, \sigma \rangle, \text{ if } \sigma \models B,$(v) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_2, \sigma \rangle, \text{ if } \sigma \not\models B,$(vi) $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle S; \text{while } B \text{ do } S \text{ do}, \sigma \rangle, \text{ if } \sigma \models B,$(vii) $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle E, \sigma \rangle, \text{ if } \sigma \not\models B,$ | $E; S \equiv S; E \equiv S$
 |
|--|--|

Consider **program** $S \equiv a[0] := 1; a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}$
and a **state** σ with $\sigma \models x = 0$.

$$\langle S, \sigma \rangle \xrightarrow{(i), (ii)} \langle E; S, \sigma[a[\sigma]:=1] \rangle$$

Example

- | | | |
|-------|--|-----------------------------|
| | (i) $\langle \text{skip}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$ | $E; S \equiv S; E \equiv S$ |
| (ii) | $\langle u := t, \sigma \rangle \rightarrow \langle E, \sigma[u := \sigma(t)] \rangle$ | |
| (iii) | $\frac{\langle S_1, \sigma \rangle \rightarrow \langle S_2, \tau \rangle}{\langle S_1; S, \sigma \rangle \rightarrow \langle S_2; S, \tau \rangle}$ | |
| (iv) | $\langle \text{if } B \text{ then } S_1 \text{ else } S_2 \text{ fi}, \sigma \rangle \rightarrow \langle S_1, \sigma \rangle$, if $\sigma \models B$, | |
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| (vii) | $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$, if $\sigma \not\models B$, | |

Consider **program** $S \equiv a[0] := 1; a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}$
 and a **state** σ with $\sigma \models \underline{x = 0}$.

$$\langle S, \sigma \rangle \xrightarrow{(ii),(iii)} \langle a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma[a[0] := 1] \rangle$$

Example

- | | | |
|-------|--|-----------------------------|
| (i) | $\langle \text{skip}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$ | $E; S \equiv S; E \equiv S$ |
| (ii) | $\langle u := t, \sigma \rangle \rightarrow \langle E, \sigma[u := \sigma(t)] \rangle$ | |
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| (vii) | $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$, if $\sigma \not\models B$, | |

Consider **program** $S \equiv a[0] := 1; a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}$ and a **state** σ with $\sigma \models x = 0$.

$$\begin{array}{lcl}
 \langle S, \sigma \rangle & \xrightarrow{(ii),(iii)} & \langle a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma[a[0] := 1] \rangle \\
 & \xrightarrow{(ii),(iii)} & \langle \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma' \rangle \\
 & \xrightarrow{(vi)} & \langle \underbrace{x := x + 1}_{S}; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma' \rangle
 \end{array}$$

where $\sigma' = \sigma[a[0] := 1][a[1] := 0]$.

Example

- | | | |
|-------|--|-----------------------------|
| | (i) $\langle \text{skip}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$ | $E; S \equiv S; E \equiv S$ |
| (ii) | $\langle u := t, \sigma \rangle \rightarrow \langle E, \sigma[u := \sigma(t)] \rangle$ | |
| (iii) | $\frac{\langle S_1, \sigma \rangle \rightarrow \langle S_2, \tau \rangle}{\langle S_1; S, \sigma \rangle \rightarrow \langle S_2; S, \tau \rangle}$ | |
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| (vii) | $\langle \text{while } B \text{ do } S \text{ do}, \sigma \rangle \rightarrow \langle E, \sigma \rangle$, if $\sigma \not\models B$, | |

Consider **program** $S \equiv a[0] := 1; a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}$ and a **state** σ with $\sigma \models x = 0$.

$$\begin{array}{lcl}
 \langle S, \sigma \rangle & \xrightarrow{(ii),(iii)} & \langle a[1] := 0; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma[a[0] := 1] \rangle \\
 & \xrightarrow{(ii),(iii)} & \langle \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma' \rangle \\
 & \xrightarrow{(vi)} & \langle x := x + 1; \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \sigma' \rangle \\
 & \xrightarrow{(ii),(iii)} & \langle \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ do}, \underbrace{\sigma'[x := 1]}_{\sigma''} \rangle \\
 & \xrightarrow{(vii)} & \langle E, \underbrace{\sigma'[x := 1]}_{= \sigma''} \rangle
 \end{array}$$

where $\sigma' = (\sigma[a[0] := 1])[a[1] := 0]$.

Input/Output Semantics of Deterministic Programs

Definition.

Let S be a deterministic program.

- (i) The **semantics of partial correctness** is the function

$$\mathcal{M}[\![S]\!]: \Sigma \rightarrow 2^\Sigma$$

with $\mathcal{M}[\![S]\!](\sigma) = \{\tau \mid \langle S, \sigma \rangle \rightarrow^* \langle E, \tau \rangle\}$.

- (ii) The **semantics of total correctness** is the function

$$\mathcal{M}_{tot}[\![S]\!]: \Sigma \rightarrow 2^\Sigma \dot{\cup} \{\perp\}$$

with $\mathcal{M}_{tot}[\![S]\!](\sigma) = \mathcal{M}[\![S]\!](\sigma) \cup \{\perp \mid S \text{ can diverge from } \sigma\}$.
 \perp is an error state representing divergence.

Note: $\mathcal{M}_{tot}[\![S]\!](\sigma)$ has exactly one element, $\mathcal{M}[\![S]\!](\sigma)$ at most one.

Correctness of Deterministic Programs

Definition.

- (i) A correctness formula $\{p\} S \{q\}$ **holds in the sense of partial correctness**, denoted by $\models \{p\} S \{q\}$, if and only if

$$\mathcal{M}[S](\llbracket p \rrbracket) \subseteq \llbracket q \rrbracket. \quad \leftarrow \{\sigma \mid \sigma \models q\}$$

We say S is partially correct wrt. p and q .

- (ii) A correctness formula $\{p\} S \{q\}$ **holds in the sense of total correctness**, denoted by $\models_{tot} \{p\} S \{q\}$, if and only if

$$\mathcal{M}_{tot}[S](\llbracket p \rrbracket) \subseteq \llbracket q \rrbracket. \quad \leftarrow \perp \text{ is not in!}$$

We say S is totally correct wrt. p and q .

Example: Correctness

- By the previous example, **we have shown**

$$\models \{x = 0\} \ S \ \{x = 1\} \text{ and } \models_{tot} \{x = 0\} \ S \ \{x = 1\}.$$

(because we only assumed $\sigma \models x = 0$ for the example, which is exactly the precondition.)

- **We have also shown:**

$$\models \{x = 0\} \ S \ \{x = 1 \wedge a[x] = 0\}.$$

- The following correctness formula **does not hold** for S :

$$\not\models_{tot} \{x = 2\} \ S \ \{true\}.$$

(e.g., if $\sigma \models a[i] \neq 0$ for all $i > 2$.)

- In the sense of **partial correctness**,

$$\{x = 2 \wedge \forall i \geq 2 \bullet a[i] = 1\} \ S \ \{false\}$$

also holds.

Proof-System PD (for sequential, deterministic programs)

Axiom 1: Skip-Statement

$$\{p\} \text{ skip } \{p\}$$

Rule 4: Conditional Statement

$$\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$$

Axiom 2: Assignment

$$\{p[u := t]\} u := t \{p\}$$

Rule 5: While-Loop

$$\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$$

Rule 3: Sequential Composition

$$\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$$

Rule 6: Consequence

$$\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$$

Theorem. PD is correct (“sound”) and (relative) complete for partial correctness of deterministic programs, i.e. $\vdash_{PD} \{p\} S \{q\}$ if and only if $\models \{p\} S \{q\}$.



Substitution

In PD uses **substitution** of the form $p[u := t]$.

(In formula p , replace all (free) occurrences of (program or logical) variable u by term t .)

Usually straightforward, but indexed and bound variables need to be treated specially:

Expressions:

- plain variable: $x[u := t] \equiv \begin{cases} t & , \text{ if } x = u \\ x & , \text{ otherwise} \end{cases}$
- constant c : $c[u := t] \equiv c$.
- constant op , terms s_i :
 $op(s_1, \dots, s_n)[u := t] \equiv op(s_1[u := t], \dots, s_n[u := t])$.
- indexed variable, u plain
or $u \equiv b[t_1, \dots, t_m]$ and $a \neq b$:
 $(a[s_1, \dots, s_n])[u := t] \equiv a[s_1[u := t], \dots, s_n[u := t]]$)
- indexed variable, $u \equiv a[t_1, \dots, t_m]$:
 $(a[s_1, \dots, s_n])[u := t] \equiv \text{if } \bigwedge_{i=1}^n s_i[u := t] = t_i \text{ then } t \text{ else } a[s_1[u := t], \dots, s_n[u := t]] \text{ fi}$
- conditional expression:
 $\text{if } B \text{ then } s_1 \text{ else } s_2 \text{ fi}[u := t] \equiv \text{if } B[u := t] \text{ then } s_1[u := t] \text{ else } s_2[u := t] \text{ fi}$

Formulae:

- boolean expression $p \equiv s$:
 $p[u := t] \equiv s[u := t]$
- negation:
 $(\neg q)[u := t] \equiv \neg(q[u := t])$
- conjunction etc.:
 $(q \wedge r)[u := t] \equiv q[u := t] \wedge r[u := t]$
- quantifier:
 $(\forall x : q)[u := t] \equiv \forall y : q[x := y][u := t]$
 y fresh (not in q, t, u),
same type as x .

Example Proof

$DIV \equiv \underbrace{q := 0; r := x;}_S \text{ while } r \geq y \text{ do } \underbrace{r := r - y; q := q + 1}_S \text{ do }$

(The first (textually represented) program that has been formally verified (Hoare, 1969)).

We want to prove

$$\models \{x \geq 0 \wedge y \geq 0\} DIV \underbrace{\{q \cdot y + r = x \wedge r < y\}}_{=: Q}$$

Note: writing a program S which satisfies this correctness formula
is much easier if S **may change** x and y ...

The proof needs a **loop invariant**, we choose (**creative act!**):

$$P \equiv q \cdot y + r = x \wedge r \geq 0$$

We prove

- (1) $\{x \geq 0 \wedge y \geq 0\} \underbrace{q := 0; r := x}_{S_1} \{P\}$ and
- (2) $\{P \wedge r \geq y\} \underbrace{r := r - y; q := q + 1}_{S_2} \{P\}$ in PD, and
- (3) $P \wedge \neg(r \geq y) \rightarrow \underbrace{q \cdot y + r = x \wedge r < y}_{Q}$ “by hand”.

Example Proof

(A1) $\{p\} \text{ skip } \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
(A2) $\{p[u := t]\} u := t \{p\}$	(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$
(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$	(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

Assume:

- (1) $\{x \geq 0 \wedge y \geq 0\} q := 0; r := x \{P\}$,
- (2) $\{P \wedge r \geq y\} \underbrace{r := r - y; q := q + 1}_{\text{and}} \{P\}$, and
- (3) $P \wedge \neg(r \geq y) \rightarrow q \cdot y + r = x \wedge r < y$.

By rule (R5), we obtain, using (2),

$$\vdash \{P\} \text{ while } r \geq y \text{ do } \underbrace{r := r - y; q := q + 1}_{S} \text{ do } \{P \wedge \neg(r \geq y)\}$$

concl.

Example Proof

(A1) $\{p\} \text{ skip } \{p\}$

(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$

(A2) $\{p[u := t]\} u := t \{p\}$

(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$

(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$

(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

Assume:

= R

- (1) $\{x \geq 0 \wedge y \geq 0\} q := 0; r := x \{P\},$
- (2) $\{P \wedge r \geq y\} r := r - y; q := q + 1 \{P\}, \text{ and}$
- (3) $P \wedge \neg(r \geq y) \rightarrow q \cdot y + r = x \wedge r < y.$

S₁

r in R3

- By rule (R5), we obtain, using (2),

$\vdash \{P\} \text{ while } r \geq y \text{ do } r := r - y; q := q + 1 \text{ do } \{P \wedge \neg(r \geq y)\}$

- By rule (R3), we obtain, using (1),

$\vdash \underbrace{\{x \geq 0 \wedge y \geq 0\}}_{R} \text{ DIV } \{P \wedge \neg(r \geq y)\}$

- By rule (R6), we obtain, using (3),

$\vdash \{x \geq 0 \wedge y \geq 0\} \text{ DIV } \{q \cdot y + r = x \wedge r < y\}$

Proof: (2)

$$(A1) \{p\} \text{ skip } \{p\}$$

$$(R4) \frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\},}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$$

$$(A2) \{p[u := t]\} u := t \{p\}$$

$$(R5) \frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$$

$$(R3) \frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$$

$$(R6) \frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$$

- $P \equiv q \cdot y + r = x \wedge r \geq 0$,
- (2): $\{P \wedge r \geq y\} r := r - y; q := q + 1 \{P\}$

- $\{(q+1) \cdot y + r = x \wedge x \geq 0\} q := \underbrace{q+1}_{t} \{P\}$ by (A2),

Proof: (2)

$$(A1) \{p\} \text{ skip } \{p\}$$

$$(R4) \frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$$

$$(A2) \{p[u := t]\} u := t \{p\}$$

$$(R5) \frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$$

$$(R3) \frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$$

$$(R6) \frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$$

- $P \equiv q \cdot y + r = x \wedge r \geq 0$,
- (2): $\{P \wedge r \geq 0\} r := r - y; q := q + 1 \{P\}$

- $\{(q+1) \cdot y + r = x \wedge r \geq 0\} q := q + 1 \{P\}$ by (A2),
- $\{(q+1) \cdot y + (\underbrace{r-y}_{\epsilon}) = x \wedge (\underbrace{r-y}_{\epsilon}) \geq 0\} \underbrace{r := r-y}_{\epsilon} \{(q+1) \cdot y + \underbrace{r}_{\epsilon} = x \wedge \underbrace{r}_{\epsilon} \geq 0\}$ by (A2),

Proof: (2)

$$(A1) \{p\} \text{ skip } \{p\}$$

$$(R4) \frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$$

$$(A2) \{p[u := t]\} u := t \{p\}$$

$$(R5) \frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$$

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- $P \equiv q \cdot y + r = x \wedge r \geq 0$,
- (2): $\{P \wedge r \geq y\} r := r - y; q := q + 1 \{P\}$
- $\{(q + 1) \cdot y + r = x \wedge x \geq 0\} q := q + 1 \{P\}$ by (A2),
- $\{(q + 1) \cdot y + (r - y) = x \wedge (r - y) \geq 0\} r := r - y \{(q + 1) \cdot y + r = x \wedge x \geq 0\}$ by (A2),
- $\{(q + 1) \cdot y + (r - y) = x \wedge (r - y) \geq 0\} r := r - y; q := q + 1 \{P\}$ by (R3),
- (2) by (R6), using

$$P \wedge r \geq y \rightarrow (q + 1) \cdot y + (r - y) = x \wedge (r - y) \geq 0.$$

Proof: (1)

$$(A1) \{p\} \text{ skip } \{p\}$$

$$(R4) \frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$$

$$(A2) \{p[u := t]\} u := t \{p\}$$

$$(R5) \frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$$

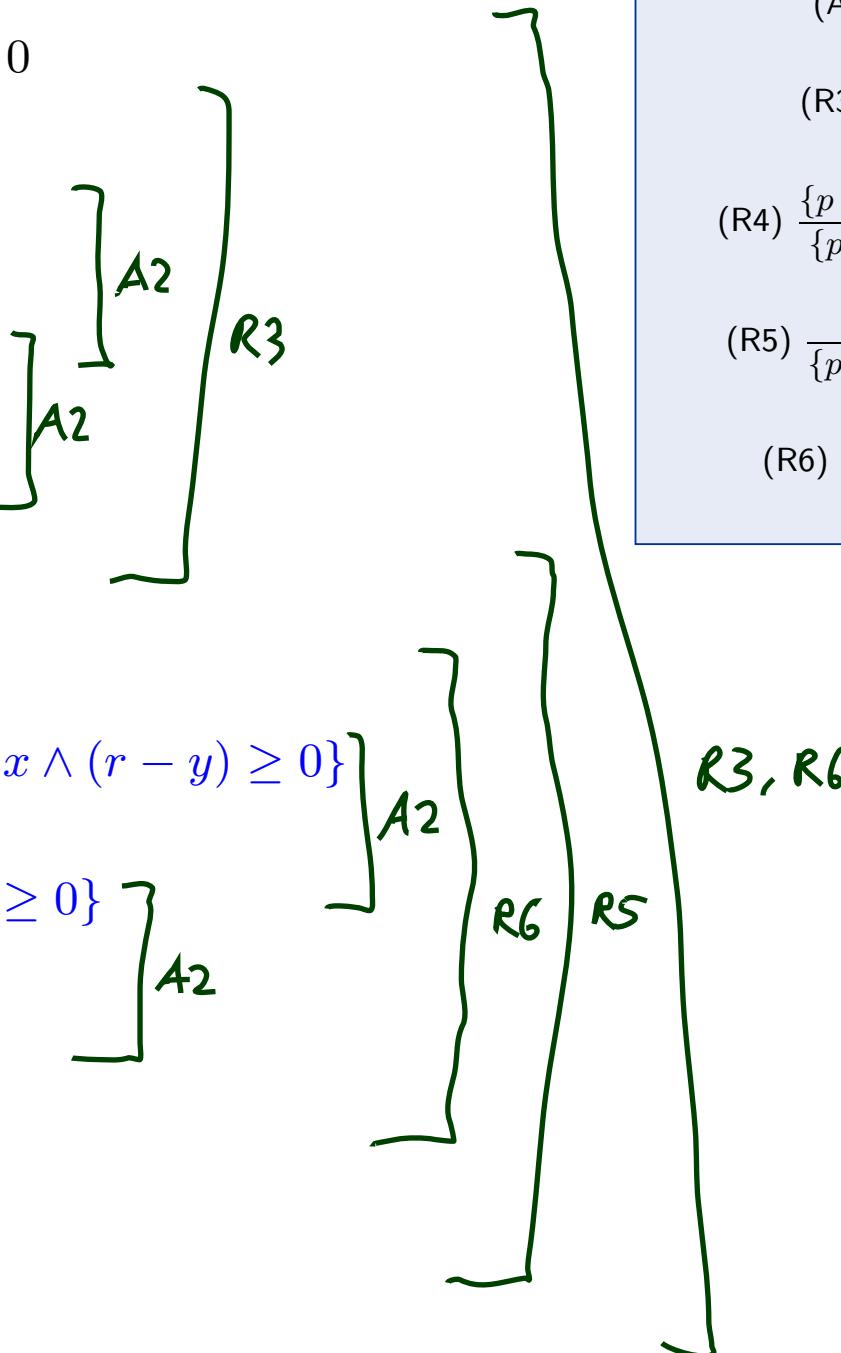
$$(R3) \frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$$

$$(R6) \frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$$

- $P \equiv q \cdot y + r = x \wedge r \geq 0$,
- (1) $\{x \geq 0 \wedge y \geq 0\} q := 0; r := x \{P\}$
- $\{q \cdot y + x = x \wedge x \geq 0\} r := x \{P\}$ by (A2),
- $\{0 \cdot y + x = x \wedge x \geq 0\} q := 0 \{q \cdot y + x = x \wedge x \geq 0\}$ by (A2),
- $\{0 \cdot y + x = x \wedge x \geq 0\} q := 0; r := x \{P\}$ by (R3),
- (1) by (R6) using
 $x \geq 0 \wedge y \geq 0 \rightarrow 0 \cdot y + x = x \wedge x \geq 0.$

Once Again

- $P \equiv q \cdot y + r = x \wedge r \geq 0$
 $\{x \geq 0 \wedge y \geq 0\}$
 $\{0 \cdot y + x = x \wedge x \geq 0\}$
- $q := 0;$
 $\{q \cdot y + x = x \wedge x \geq 0\}$
- $r := x;$
 $\{q \cdot y + r = x \wedge x \geq 0\}$
 $\{P\}$
- **while** $r \geq y$ **do**
 $\{P \wedge r \geq y\}$
 $\{(q+1) \cdot y + (r-y) = x \wedge (r-y) \geq 0\}$
- $r := r - y;$
 $\{(q+1) \cdot y + r = x \wedge x \geq 0\}$
- $q := q + 1$
 $\{q \cdot y + r = x \wedge x \geq 0\}$
 $\{P\}$
- **do**
 $\{P \wedge \neg(r \geq y)\}$
 $\{q \cdot y + r = x \wedge r < y\}$



(A1) $\{p\} \text{ skip } \{p\}$

(A2) $\{p[u := t]\} u := t \{p\}$

(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$

(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\},}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$

(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ do } \{p \wedge \neg B\}}$

(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

Modular Reasoning

We can add a rule for function calls (simplest case: only global variables):

$$(R7) \frac{\{p\} f \{q\}}{\{p\} f() \{q\}}$$

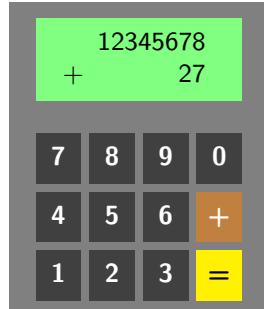
“If we have $\vdash \{p\} f \{q\}$ for the **implementation** of function f ,
then if f is **called** in a state satisfying p , the state after return of f will satisfy q .”

p is called **pre-condition** of f , q is called **post-condition**.

Example: if we have

- $\{true\} \text{ read_number } \{0 \leq ret < 10^8\}$
- $\{0 \leq x \wedge 0 \leq y\} \text{ add } \{(old(x) + old(y) < 10^8 \wedge ret = old(x) + old(y)) \vee ret < 0\}$
- $\{true\} \text{ display } \{(0 \leq old(x) < 10^8 \implies "old(x)" \wedge (old(x) < 0 \implies "-E-")\}$

we may be able to prove our (\rightarrow later) pocket calculator correct.



```
1 int main() {
2
3     while (true) {
4         int x = read_number();
5         int y = read_number();
6
7         int sum = add( x, y );
8
9         display(sum);
10    }
11 }
```

Assertions

We add another rule for **assertions**:

$$(A3) \{p\} \text{ assert}(p) \{p\}$$

- That is, if p holds **before** the assertion, then we can **continue** with the proof.
- Otherwise we “**get stuck**”.

So we **cannot** even prove

$$\{true\} x := 0; \text{ assert}(x = 27) \{true\}.$$

to hold (it is not derivable).

- Which is exactly what we want — if we add
 - $\langle \text{assert}(B), \sigma \rangle \rightarrow \langle E, \sigma \rangle$ if $\sigma \models B$,

to the transition relation.

(If the assertion does not hold, the empty program is not reached;
the assertion remains in the first component: **abnormal** program termination).

Why Assertions?

- Available in standard libraries of many programming languages, e.g. C:

1	ASSERT(3)	Linux Programmer's Manual	ASSERT(3)
2			
3	NAME		
4	assert — abort the program if assertion is false		
5			
6	SYNOPSIS		
7	#include <assert.h>		
8			
9	void assert(scalar expression);		
10			
11	DESCRIPTION		
12	[...] the macro assert() prints an error message to stan		
13	dard error and terminates the program by calling abort(3) if expression		
14	is false (i.e., compares equal to zero).		
15			
16	The purpose of this macro is to help the programmer find bugs in his		
17	program. The message "assertion failed in file foo.c, function		
18	do_bar(), line 1287" is of no help at all to a user.		

Why Assertions?

- Available in standard libraries of many programming languages, e.g. C:

```
1 ASSERT(3)          Linux Programmer's Manual    ASSERT(3)
2
3 NAME
4     assert — abort the program if assertion is false
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7     #include <assert.h>
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9     void assert(scalar expression);
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12     [...] the macro assert() prints an error message to stan-
13     dard error and terminates the program by calling abort(3) if expression
14     is false (i.e., compares equal to zero).
15
16     The purpose of this macro is to help the programmer find bugs in his
17     program. The message "assertion failed in file foo.c, function
18     do_bar(), line 1287" is of no help at all to a user.
```

- Assertions at work:

```
1 int square( int x )
2 {
3     assert( x < sqrt(x) );
4
5     return x * x;
6 }
```

```
1 void f( ... ) {
2     assert( p );
3     ...
4     assert( q );
5 }
```

The Verifying C Compiler

- The **Verifying C Compiler** (VCC) basically implements Hoare-style reasoning.

- **Special syntax:**

- `#include <vcc.h>`
- `_requires p` — pre-condition, *p* is a C expression
- `_ensures q` — post-condition, *q* is a C expression
- `_invariant expr` — loop invariant, *expr* is a C expression
- `_assert p` — intermediate invariant, *p* is a C expression
- `_writes &v` — VCC considers **concurrent** C programs; we need to declare for each procedure which global variables it is allowed to write to (also checked by VCC)

- **Special expressions:**

- `\thread_local(&v)` — no other thread writes to variable *v* (in pre-conditions)
- `\old(v)` — the value of *v* when procedure was called (useful for post-conditions)
- `\result` — return value of procedure (useful for post-conditions)

VCC Syntax Example

```
1 #include <vcc.h>
2
3 int q, r;
4
5 void div( int x, int y )
6 -( requires x >= 0 && y >= 0)
7 -( ensures q * y + r == x && r < y)
8 -( writes &q)
9 -( writes &r)
10 {
11     q = 0;
12     r = x;
13     while (r >= y)
14     -( invariant q * y + r == x && r >= 0)
15     {
16         r = r - y;
17         q = q + 1;
18     }
19 }
```

$DIV \equiv q := 0; r := x; \text{while } r \geq y \text{ do } r := r - y; q := q + 1 \text{ do}$
 $\{x \geq 0 \wedge y \geq 0\} \text{ DIV } \{q \cdot y + r = x \wedge r < y\}$

VCC Web-Interface

The screenshot shows a web browser window displaying the VCC web interface at rise4fun.com/Vcc/9Cpx. The page title is "Vcc @ rise4fun from Micr...". The Microsoft Research logo is visible at the top.

The main content asks, "Does this C program always work?" and displays the following C code:

```
1 #include <vcc.h>
2
3 int q, r;
4
5 void div( int x, int y )
6   -(requires x >= 0 && y >= 0)
7   -(ensures q * y + r == x && r < y)
8   -(writes &q)
9   -(writes &r)
10 {
11   q = 0;
12   r = x;
13
14   while (r >= y)
15     -(invariant q * y + r == x && r >= 0)
16   {
17     r = r - y;
18     q = q + 1;
19   }
20 }
```

Below the code, there are three buttons: "home", "video", and "permalink". A tooltip for the "home" button says "shortcut: Alt+B".

On the left, there is a sidebar with links to "samples" (hello, lsearch, safestring, bozosort, spinlock) and a "about Vcc - A Verifier for Concurrent C" section. This section describes VCC as a tool that proves correctness of annotated concurrent C programs or finds problems in them. It mentions that VCC extends C with design by contract features like pre- and postcondition as well as type invariants. Annotated programs are translated to logical formulas using the Boogie tool, which passes them to an automated SMT solver Z3 to check their validity.

At the bottom, there are links for "tools", "developer", and "about". The footer contains the copyright notice "rise4fun © 2015 Microsoft Corporation - terms of use - privacy & cookies - code of conduct".

VCC Architecture

VCC Features

- For the exercises, we use VCC only for **sequential, single-thread programs**.
- VCC checks a number of **implicit assertions**:
 - **no arithmetic overflow** in expressions (according to C-standard),
 - **array-out-of-bounds access**,
 - **NULL-pointer dereference**,
 - and many more.
- VCC also supports:
 - **concurrency**: different threads may write to shared global variables; VCC can check whether concurrent access to shared variables is properly managed;
 - **data structure invariants**: we may declare invariants that have to hold for, e.g., records (e.g. the length field l is always equal to the length of the string field str); those invariants may **temporarily** be violated when updating the data structure.
 - and much more.
- Verification **does not always succeed**:
 - The backend SMT-solver may not be able to discharge proof-obligations (in particular non-linear multiplication and division are challenging);
 - In many cases, we need to provide **loop invariants** manually.

Interpretation of Results

- VCC says: “**verification succeeded**

We can **only conclude** that the tool — under its interpretation of the C-standard, under its platform assumptions (32-bit), etc.

— “thinks” that it can prove $\models \{p\} \text{ DIV } \{q\}$. Can be due to an error in the tool!

Yet we can ask **for a printout of the proof** and check it manually (hardly possible in practice) or with other tools like interactive theorem provers.

Note: $\models \{\text{false}\} f \{q\}$ **always holds**

— so a mistake in writing down the pre-condition can provoke a **false negative**.

- VCC says: “**verification failed**

- One case: “timeout” etc. — completely inconclusive outcome.

- The tool **does not provide counter-examples** in the form of a computation path.

- It (only) gives **hints on input values** satisfying p and causing a violation of q .

- May be a **false negative** if these inputs are actually never used.

- Make pre-condition p stronger, and try again.

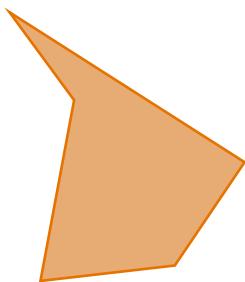
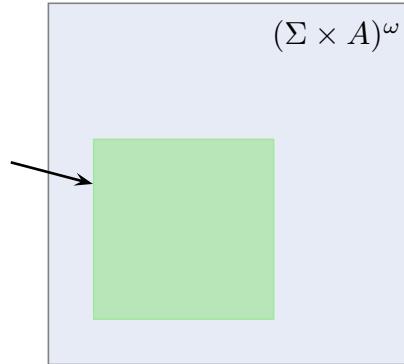
The screenshot shows a Mozilla Firefox browser window with the URL "rise4fun.com/Vcc/". The page displays a C code snippet for a division function (div) with annotations using the VCC tool. The annotations include requirements and ensures clauses, as well as thread-locality and writes annotations. Below the code, there is a message box stating "verification of div succeeded. [3.97]" with a play button icon and links for "home", "video", and "permalink". A tooltip indicates a keyboard shortcut: Alt+B.

```
1 #include <vcc.h>
2
3 int x, y, q, r;
4
5 void div()
6 __requires x >= 0 && y > 0 && x <= 15 && y <= 15
7 __ensures q == x / y && r == x % y
8 __requires __thread_local(&x)
9 __requires __thread_local(&y)
10 __writest &q
11 __writest &r
12 {
13     q = 0;
14     r = x;
15     while (r >= y)
16         __invariant q * y + r == x && r >= 0;
17     {
18         r = r - y;
19         q = q + 1;
20     }
21 }
```

verification of div succeeded. [3.97]

(Automatic) Formal Verification Techniques

all computation
paths satisfying
specification

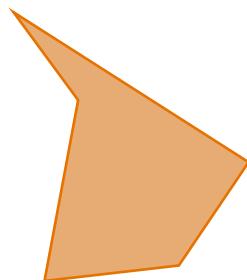
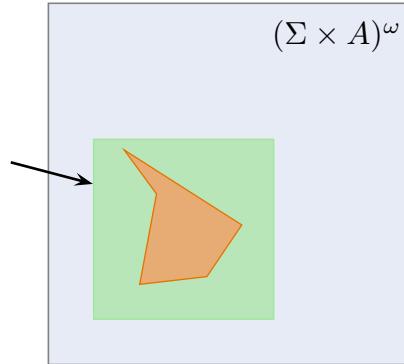


Investigate All Paths

(like Uppaal; possible for
finite-state software; no false
positives or negatives)

(Automatic) Formal Verification Techniques

all computation
paths satisfying
specification

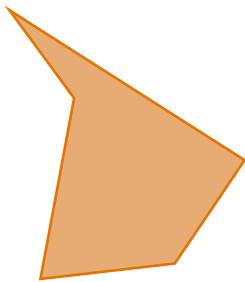
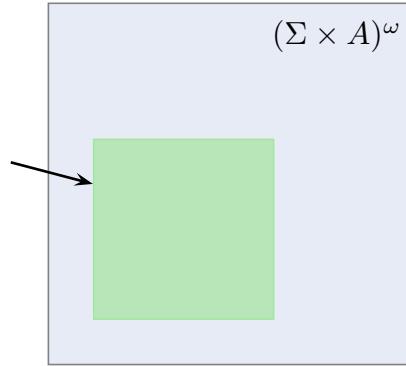


Investigate All Paths

(like Uppaal; possible for
finite-state software; no false
positives or negatives)

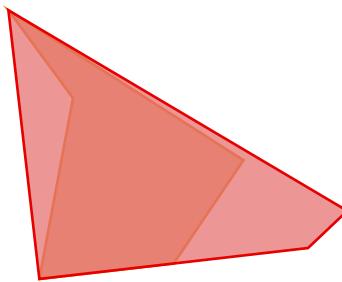
(Automatic) Formal Verification Techniques

all computation
paths satisfying
specification



Investigate All Paths

(like Uppaal; possible for
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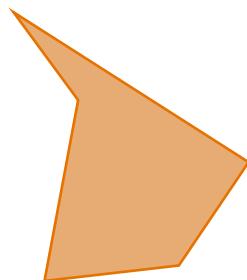
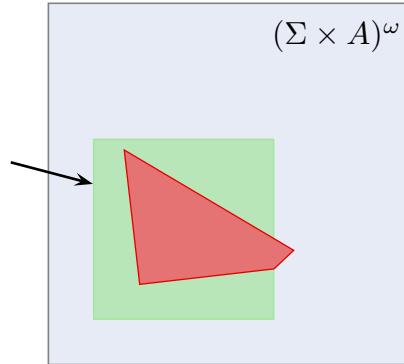


Over-Approximation

(some Software model-checkers;
goal: verify correctness; false
positives, no false negatives)

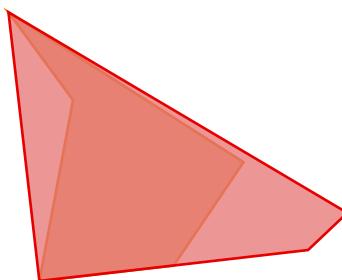
(Automatic) Formal Verification Techniques

all computation
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Investigate All Paths

(like Uppaal; possible for
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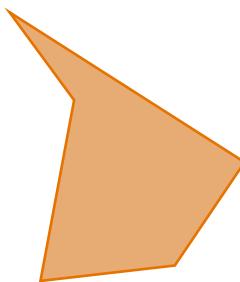
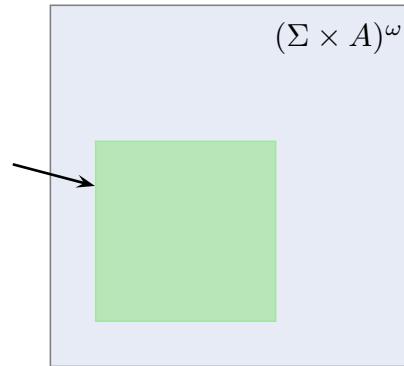


Over-Approximation

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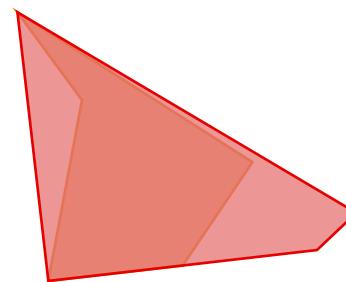
(Automatic) Formal Verification Techniques

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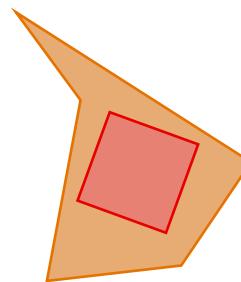
Investigate All Paths

(like Uppaal; possible for
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Over-Approximation

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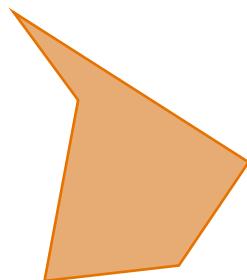
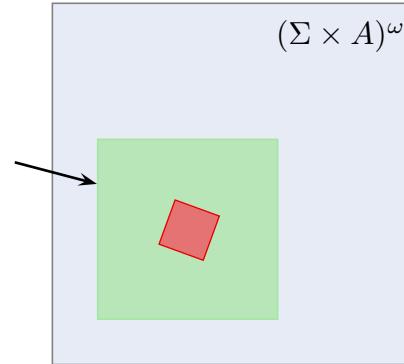


Under-Approximation

(e.g. bounded model-checking;
goal: find errors; false
negatives, no false positives)

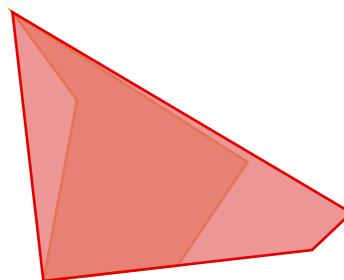
(Automatic) Formal Verification Techniques

all computation
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specification



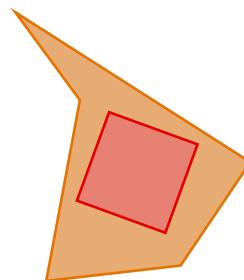
Investigate All Paths

(like Uppaal; possible for
finite-state software; no false
positives or negatives)



Over-Approximation

(some Software model-checkers;
goal: verify correctness; false
positives, no false negatives)



Under-Approximation

(e.g. bounded model-checking;
goal: find errors; false
negatives, no false positives)

References

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