

Topic Area Code Quality Assurance: Content

- VL15
 - Introduction and Vocabulary
 - Limits of Software Testing
- VL16
 - Statement-, branch-, term-coverage
 - Glass-Box Testing
 - Other Approaches
 - Model-Based testing
 - Runtime verification
 - In a larger scope.
- VL17
 - Software quality assurance
 - Program Verification
 - Proof System PD
- VL18
 - Review

Content

- Software quality assurance in a larger scope:
- vocabulary, fault, error, failure, concepts of software quality assurance (next to testing)

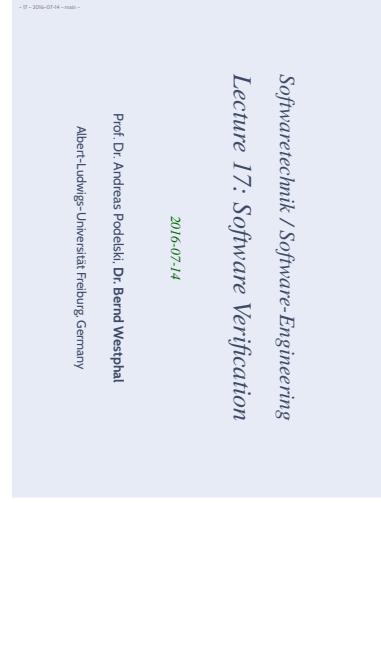
- Formal-Program Verification
- Deterministic Programs
 - Syntax
 - Semantics
 - Termination, Divergence
 - Correctness of deterministic programs
 - parallel correctness.
 - total correctness.
- Proof System PD

- The Verifier for Concurrent C

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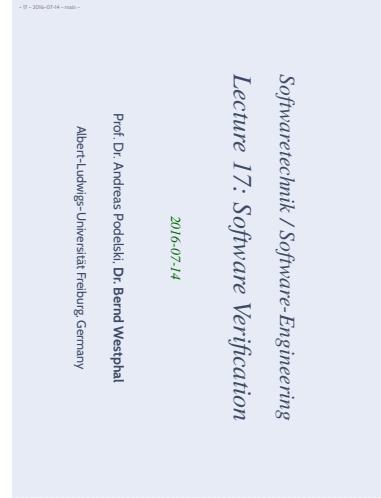
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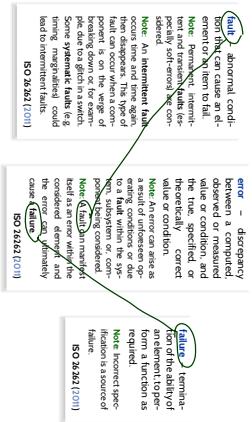
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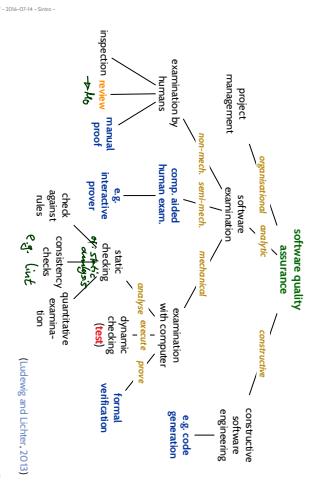
Fault, Error, Failure



We want to avoid failures, thus we try to detect faults and errors.

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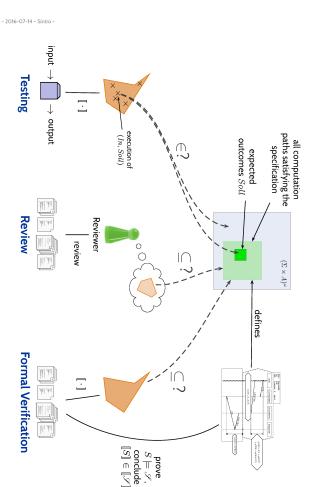
Concepts of Software Quality Assurance



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(Ludewig and Lüttke, 2013)

Three Basic Approaches



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Deterministic Programs

Syntax:

$S := \text{skip} \mid u := t \mid S_1 ; S_2 \mid \text{if } B \text{ then } S_1 \text{ else } S_2 \mid \text{while } B \text{ do } S_1 \text{ od}$

where $u \in V$ is a variable, t is a type-compatible expression, B is a Boolean expression.

Semantics: Is produced by the following transition relation: $- \sigma : V \rightarrow D(V)$

$$\frac{(i) \langle skip, \sigma \rangle \rightarrow (E, \sigma)}{(E, \sigma)}$$

$$\frac{(ii) \langle u := t, \sigma \rangle \rightarrow (E, \sigma[u := t])}{(E, \sigma)}$$

$$\frac{(iii) \langle S_1, \sigma \rangle \rightarrow (S_2, \sigma)}{(S_1 ; S_2, \sigma)}$$

- (iv) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2, \sigma \rangle \rightarrow (S_1, \sigma), \text{ if } \sigma[B] = B$
- (v) $\langle \text{if } B \text{ then } S_1 \text{ else } S_2, \sigma \rangle \rightarrow (S_2, \sigma), \text{ if } \sigma[B] \neq B$
- (vi) $\langle \text{while } B \text{ do } S \text{ od}, \sigma \rangle \rightarrow (S, \sigma) \text{ with } B \text{ s od, } \sigma, \text{ if } \sigma[B] = B$
- (vii) $\langle \text{while } B \text{ do } S \text{ od}, \sigma \rangle \rightarrow (E, \sigma), \text{ if } \sigma[B] \neq B$

E denotes the empty program; define $\underline{E} \equiv S ; \underline{E} \equiv S$

Note: the first component of (S, σ) is a program, structural operational semantics (SOS).

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Example

Consider program

$$S \equiv \langle \underline{0} := 1, a[1] := 0 \rangle$$

and a state σ with $\sigma[1] = x = 0$.

$S \models \langle \underline{0} := 1, a[1] := 0 \rangle$

$\frac{\langle \underline{0} := 1, a[1] := 0 \rangle}{\langle \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ od, } \sigma'[x := 1] \rangle}$

$\frac{\langle \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ od, } \sigma' \rangle}{\langle x := a[x] + 1, \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ od, } \sigma' \rangle}$

$\frac{\langle x := a[x] + 1, \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ od, } \sigma' \rangle}{\langle x := a[x] + 1, \text{while } a[x] \neq 0 \text{ do } x := x + 1 \text{ od, } \sigma'[x := 1] \rangle}$

where $\sigma' := \sigma[a[0] := 1][a[1] := 0]$.

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Proof of (1)

2006-07-14 - 2

- W** $\vdash_{\text{PA}} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$
where $P \equiv a \cdot y + b = x \wedge b \geq 0$.

$\{P\}$	$\{A_1(p), \neg A_2(p)\}$	$\{A_2(p), \neg A_1(p)\}$
$\neg A_1(p)$	$\neg A_1(p) \rightarrow B_1(S_1, p) \wedge \neg A_2(p) \rightarrow B_2(S_2, p)$	$\neg A_2(p) \rightarrow B_2(S_2, p) \wedge \neg A_1(p) \rightarrow B_1(S_1, p)$
$\neg A_2(p)$	$\neg A_1(p) \rightarrow B_1(S_1, p) \wedge \neg A_2(p) \rightarrow B_2(S_2, p)$	$\neg A_2(p) \rightarrow B_2(S_2, p) \wedge \neg A_1(p) \rightarrow B_1(S_1, p)$
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$\neg B_2(S_2, p)$	$\neg A_1(p) \rightarrow B_1(S_1, p) \wedge \neg A_2(p) \rightarrow B_2(S_2, p)$	$\neg A_2(p) \rightarrow B_2(S_2, p) \wedge \neg A_1(p) \rightarrow B_1(S_1, p)$

Proof of (I)

- 17 - 2006-07-14 - 2

- W** Claim: $\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$
where $P \equiv a \cdot y + b = x \wedge b \geq 0$.

$\vdash \frac{\vdash A \wedge B, S \vdash b \quad \vdash C \wedge D, T \vdash d}{\vdash A \wedge C, S \vdash b \wedge d}$ $\text{by (A)}.$	$\vdash \frac{\vdash S \vdash a \quad \vdash T \vdash b}{\vdash S \vdash a \wedge T \vdash b}$ by (A).	$\vdash \frac{\vdash S \vdash a \quad \vdash T \vdash b}{\vdash S \vdash a \vee T \vdash b}$ by (A).	$\vdash \frac{\vdash S \vdash a \quad \vdash T \vdash b}{\vdash S \vdash a \rightarrow T \vdash b}$ by (A).	$\vdash \frac{\vdash S \vdash a \quad \vdash T \vdash b}{\vdash S \vdash a \wedge \neg T \vdash b}$ by (A).
$\vdash \frac{x \geq 0 \quad a = \underbrace{y + y + x = x \wedge x \geq 0}_{b = z}}{x \geq 0 \quad b = z}$ by (A).	$\vdash \frac{x \geq 0 \quad a = \underbrace{y + y + x = x \wedge x \geq 0}_{b = z}}{x \geq 0 \quad b = z}$ by (A).	$\vdash \frac{x \geq 0 \quad a = \underbrace{y + y + x = x \wedge x \geq 0}_{b = z}}{x \geq 0 \quad b = z}$ by (A).	$\vdash \frac{x \geq 0 \quad a = \underbrace{y + y + x = x \wedge x \geq 0}_{b = z}}{x \geq 0 \quad b = z}$ by (A).	$\vdash \frac{x \geq 0 \quad a = \underbrace{y + y + x = x \wedge x \geq 0}_{b = z}}{x \geq 0 \quad b = z}$ by (A).
$\vdash \frac{x \geq 0 \quad a = \underbrace{y + y + x = x \wedge x \geq 0}_{b = z}}{x \geq 0 \quad b = z}$ by (A).				

Proof of (I)

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- W** *Summary.*
 $\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$
 where $P \equiv a \cdot y + b = x \wedge b \geq 0$.

Substitution

- The rule '**Assignment**' uses (syntactical) **substitution**: $\{p\}[u := t]\{p\}$
 (in formula p , replace all (free) occurrences of (program or logical) variable u by term t)

Proof of (2)

- (2) claims:
 $\{P \wedge b \geq y\} b := b - y; a := a + 1 \{P\}$
 where $P \equiv a \cdot y + b = x \wedge b \geq 0$.

(A1) p	$\text{skip } p$	(R4) $\frac{p \vdash B, S, \{q\}}{\{p\} \text{ R } B \text{ then } S; \text{ else } S_2 \{q\}}$
(A2) $\{p \vdash t = t\}$	$u = t \{p\}$	(R5) $\frac{}{\{p\} \text{ write } B, S \{q\}}$
(R3) $\frac{\{p\} S_1 \{q\} \quad \{p\} S_2 \{q\}}{\{p\} S_1 S_2 \{q\}}$		(R6) $\frac{S \vdash t \rightarrow q}{U \rightarrow_{\text{R}} \{p\} S \{q\}, q \rightarrow q}$

Proof of (2)

1

- (2) claims:
 $\vdash_{PD} \{P \wedge b \geq y\} \ b := b - y; a := a$
 where $P \equiv a \cdot y + b = x \wedge b \geq 0$.

(A1) $\{p\} \text{ sdp } \{p\}$	$\frac{\{p\} \wedge B \mid S_1 \{q\} \quad \{p\} \wedge \neg B \mid S_2 \{q\}}{\{p\} \mid B \text{ then } S_1 \text{ else } S_2 \{q\}}$
(A2) $\{p\} p = d \mid u = t \{q\}$	$\frac{\{p\} \mid u = t \{q\}}{\{p\} \mid d = p \mid S \{q\}}$
(B5) $\{p\} \text{ will do } B \mid S \{q\} \rightarrow \neg T$	$\frac{\{p\} \mid S \{q\}}{\{p\} \mid \neg T}$
(B6) $\frac{\{p\} \mid S \{q\}}{\{p\} \mid \neg p \mid S \{q\}}$	$\frac{\{p\} \mid S \{q\}}{\{p\} \mid S \{q\}}$

Assertions

- Extend the syntax of deterministic programs by

- and the semantics by rule

$\langle \text{assert}(B), \sigma \rangle \rightarrow \langle E, \sigma \rangle$ if $\sigma \models B$.
 (If the asserted boolean expression B does not hold in state σ , the empty program is not reached, otherwise the assertion remains in the first component, abnormal program termination).

Extend PD by axiom:

$$\langle A \wedge \neg p, \sigma \rangle \text{ assert}(p, \{p\})$$

- That is, if p holds before the assertion, then we can continue with the derivation in PD.
- If p does not hold, we "get stuck" and cannot complete the derivation.
- So we cannot derive $\{\text{true}\} x := i; \text{assert}(x = 27) \{\text{true}\}$ in PD.

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Modular Reasoning

We can add another rule for calls of functions $f : F$: simplest case: only global variables;

"If we have $\vdash \langle p \rangle F(q)$ for the implementation of function f , then the return of f will satisfy q ."
 then if f is called in a state satisfying p , the state after return of f will satisfy q .
 p is called pre-condition and q called post-condition of f .

Example: if we have

- $\{\text{true}\} \text{ read_number}(0 \leq \text{read} < 10^8)$
- $\{0 \leq x \leq 0 \leq y\} \text{ add} \{(\text{old}(x) \cdot \text{old}(y) < 10^8 \wedge \text{result} = \text{old}(x) + \text{old}(y)) \vee \text{result} < 0\}$
- $\{\text{true}\} \text{ display} \{(0 \leq \text{old}(x) < 10^8 \implies " \text{add}(x)" \cdot (\text{old}(x) < 0 \implies " \text{E}")\}$

we may be able to prove our pocket calculator correct.

uses	27
v1	0
v2	0
result	0
ans	0
ans + ans * v1	0
ans + ans * v1	0

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VCC

The Verifier for Concurrent C (VCC) basically implements Hoare-style reasoning

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ans + ans * v1	0
ans + ans * v1	0

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Return Values and Old Values

- For modular reasoning, it's often useful to refer in the post-condition

- to the return value as result ,
- the values of variable x at calling time as $\text{old}(x)$,
- Can be defined using auxiliary variables;

- Transform function

```
T f() { ... return cexpr; }
```

(over variables $V = \{v_1, \dots, v_n\}$, $\text{result}, v_i^{\text{old}} \notin V\} \text{ b}$

```
T f() {
```

```
    v1^{\text{old}} := v1; ...; v_n^{\text{old}} := v_n;
```

```
    ...;
    result := cexpr;
    return result;
}
```

- Then $\text{old}(x)$ is just an abbreviation for x^{old} .

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The Verifier for Concurrent C

The Verifier for Concurrent C (VCC) basically implements Hoare-style reasoning

Special syntax

#include "vcc.h"

_requires p -> pre-condition, p is basically a C expression

_ensures q -> post-condition, q is basically a C expression

_invariant cexpr -> loop invariant, cexpr is basically a C expression

_assert p -> intermediate invariant, p is basically a C expression

_vtc tcs kvs -> VCC considers concurrent C programs, we need to decide for each

_procedure which global variables it is allowed to write to (also checked by VCC)

Special expressions:

_thread_local(kvs) - no other thread writes to variable v (in pre-conditions)

_value(v) - the value of v when procedure was called (useful for post-conditions)

_result - return value of procedure (useful for post-conditions)

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VCC Syntax Example

The flowchart illustrates the execution of the following C-like code:

```

1. #include <stdio.h>
2. int a, b;
3. int *x, *y;
4. void foo( int x, int y )
5. {
6.     /* requires x != 0 && y != 0 */
7.     /* renames b */
8.     /* writes to y */
8.     /* writes to b */
9. }
10. int main()
11. {
12.     a = 0;
13.     b = 1;
14.     x = &a;
15.     y = &b;
16.     /* invariant a == y == b == x && b == y */
17.     /* a == b == y; */
18.     /* b == y; */
19.     /* a == b == y; */
20. }

```

The flowchart shows the state of variables at different stages of execution:

- Initial State:** $a = 0$, $b = 1$, $x = \&a$, $y = \&b$.
- After line 15:** $a = 0$, $b = 1$, $x = \&a$, $y = \&b$. A note indicates: "invariant $a == y == b == x \&& b == y$ ".
- Before line 8:** $a = 0$, $b = 1$, $x = \&a$, $y = \&b$. A note indicates: "renames b ".
- After line 8:** $a = 0$, $b = 1$, $x = \&a$, $y = \&b$. A note indicates: "writes to y ".
- After line 9:** $a = 0$, $b = 1$, $x = \&a$, $y = 1$. A note indicates: "writes to b ".
- Final State:** $a = 0$, $b = 1$, $x = \&a$, $y = 1$.

Annotations include:
 - "ptr-rev" and "ptr-conv" near the pointer assignments.
 - "loop invariant!" near the invariant check.

$\{x \geq 0 \wedge y \geq 0\} \text{ } DV \{x \geq 0 \wedge y \geq 0\}$

Vyv

VCC Features

- VCC also supports:
 - concurrency.** If threads may write to shared global variables, VCC can check whether concurrent access to different threads is properly managed.
 - data structures.** VCC can check that arrays hold for e.g. *n* records (i.e. the length field is always equal to the width of the first *n* fields), these bounds are *temporarily* validated when reading the data structure.
 - and much more.
 - Verification does not always succeed.**
 - The backend SMT-solver may not be able to discharge proof obligations (in particular non-linear multiplication and division are challenging).
 - In many cases, we need to provide loop invariants manually.

- U - 2016-07-14 - 5 - <http://Y1aa4fun.com/Vsc/491e>

- Other case: "timeout" etc. – completely inconclusive outcome.

VCC Web-Interface

VCC

Example program DV: <http://rise4fun.com/Vcc/4Kqe>

- VCC says: “verification failed”
 - May be a **false positive**.
 - The tool **does not provide counter-examples** in the form of a computation path, it (only) gives hints on how to fix the bug, and/or using a violation of φ
 - try to construct a (true) counter-example from the hints.
 - That is, a mistake in writing down the pre-condition can make errors in the program go undetected.

- Other case: “timeout” etc. – completely inconclusive outcome.

Interpretation of Results

- VCC says “**verification failed**”
 - May be a **false positive**
 - The tool does not provide counter **examples** in the form of a compilation path, it (only) gives hints on input values satisfying (and causing a violation of) q .
 - try to construct a (true) counter-example from the hints.
- That is, a mistake in writing down the pre-condition can make errors in the program go undetected.

- Other case: “`timeout`” etc. – completely **inconclusive** outcome.

negative

negative

- For example, program verification.
 - there are more approaches, e.g. software quality assurance, just testing.
 - Proof System PD can be used
 - to prove
 - that a given programs
 - correct wrt. its specification.
 - This approach considers all inputs inside the specification!
 - Tools like VCC implement this approach.

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References

- Horn, C. A. R. (1969). An axiomatic basis for computer programming. *Commun ACM*, 12(10), 576–580.
- IEEE (1990). *IEEE Standard Glossary of Software Engineering Terminology*. Std 1012-1990.
- ISO (2011). *Road vehicles – Functional safety – Part 1: Vocabulary*. 16262-1-2011.
- Ludewig, J. and Uchter, H. (2013). *Software Engineering*. dpunktverlag, 3. edition.