

Topic Area Architecture & Design: Content

- VL10 • Introduction and Vocabulary
  - (i) views and viewpoints, the 4+1 view
  - (ii) model-driven / based software engineering
- VL11 • Software Modelling I
  - (i) Modeling structure
  - (ii) Modeling structure
  - a) (simplified) class diagrams
  - b) (simplified) object diagrams
  - c) (simplified) object constraint logic (OCL)
  - d) Unified Modeling Language (UML)
- VL12 • Principles of Design
  - (i) modularity, separation of concerns
  - (ii) information hiding and data encapsulation
  - (iii) abstract data type, object orientation
- VL13 • Design Patterns
  - (i) Software Modelling II
  - (ii) Modeling behaviour
    - a) communicating finite automata
    - b) Uppaal query language
    - c) basic state-machines
    - d) an outlook on hierarchical state-machines
- VL14 •

Content

- Design Patterns
  - Strategy, Examples
  - Communicating Finite Automata (CFA)
    - concrete and abstract syntax, networks of CFA,
    - operational semantics,
  - Transition Sequences
  - Deadlock, Reachability
  - Uppaal
    - tool demo (formal), query language,
    - CFA model-checking
  - CFA at Work
    - drive to configuration scenarios, invariants
    - tool demo (verifier), CFA vs. Software

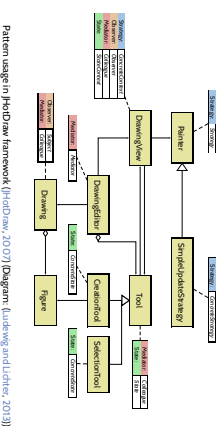
Design Patterns

- In a sense the same as **architectural patterns**, but on a lower scale.
- Often traced back to [Alexander et al., 1977; Alexander, 1979]



**Design patterns** – are descriptions of communicating objects and classes that are customized to solve a general design problem in a particular context. A design pattern names, abstracts, and identifies the key aspects of a common design structure that make it useful for creating a reusable object-oriented design. [Gamma et al., 1995]

Example: Pattern Usage and Documentation



Pattern usage in JactDraw framework (JactDraw, 2017) (Diagram, Ludewig and Ullrich, 2013)

Strategy	
<b>Problem</b>	<ul style="list-style-type: none"> <li>The only difference between similar classes is that they solve the same problem by different algorithms.</li> <li>Have one class <b>Strategy</b> interface with all common operations.</li> <li>Another class <b>Strategy</b> provides signatures for all operations to be implemented differently.</li> <li>From <b>Strategy</b>, derive one sub-class <b>ConcreteStrategy</b>.</li> <li><b>Strategy</b> interface uses concrete <b>Strategy</b> objects to execute the different implementations via delegation.</li> </ul>
<b>Solution</b>	<ul style="list-style-type: none"> <li>From <b>Strategy</b>, derive one sub-class <b>ConcreteStrategy</b>.</li> <li><b>Strategy</b> interface uses concrete <b>Strategy</b> objects to execute the different implementations via delegation.</li> </ul>
<b>Structure</b>	

of the Strategy	
<b>Problem</b>	The way of how a behavior is implemented is different and it is difficult to solve the same problem by different algorithms.
<b>Solution</b>	<ul style="list-style-type: none"> <li>From <b>Strategy</b>, derive one sub-class <b>ConcreteStrategy</b>.</li> <li><b>Strategy</b> interface uses concrete <b>Strategy</b> objects to execute the different implementations via delegation.</li> </ul>
<b>Structure</b>	

Observer	
<b>Problem</b>	Multiple objects need to reflect their state if one particular object's state is changed.
<b>Example</b>	<ul style="list-style-type: none"> <li>All GUI object displaying the system need to change if they are added or removed.</li> </ul>
<b>Structure</b>	

Singleton	
<b>Problem</b>	Of one class, exactly one instance should exist in the system.
<b>Example</b>	<ul style="list-style-type: none"> <li>Print system.</li> </ul>
<b>Structure</b>	

Mediator	
<b>Problem</b>	Object interacting in a complex way should only be loosely coupled and be easier to manage.
<b>Example</b>	<ul style="list-style-type: none"> <li>Complex system with many of interaction (mouse, buttons, input fields in a graphical user interface (GUI)) should be consistent in each interaction state.</li> </ul>
<b>Structure</b>	

Singleton	
<b>Problem</b>	Of one class, exactly one instance should exist in the system.
<b>Example</b>	<ul style="list-style-type: none"> <li>Print system.</li> </ul>
<b>Structure</b>	

The development of design patterns is considered to be one of the most important innovations of software engineering in recent years. [Fowler and Lindor, 2013]

- **Advantages:**
  - (Re-)use the experience of others and employ well-proven solutions.
  - Can improve on quality criteria like changeability or re-use.
  - Provide a vocabulary for the design process.
  - thus facilitates documentation of architectures and discussions about architecture.
  - Can be combined in flexible way.
  - Can be used for multiple purposes.
  - Help in teaching software design.

• **Disadvantages:**

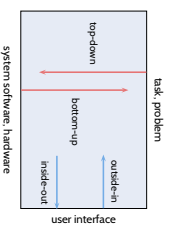
- Using a pattern is not a virtue as such.
  - Having too much global data cannot be justified by "but it's the pattern Singleton".
  - Again: reading is easy, writing need not be.
- Here: Understanding abstract descriptions of design patterns or their use in existing software may be easy – using design patterns appropriately in new designs requires **expertise**; **surprise**; **surprise** experience.

Quality Criteria on Architectures

Quality Criteria on Architectures

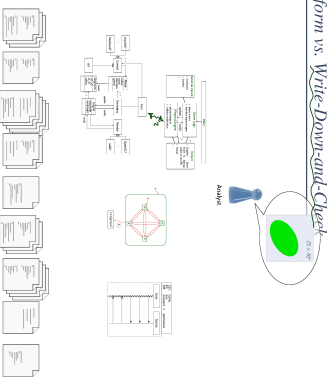
- **testability**
  - software design should beo tested (or formal verification) in mind (**testcase** or "design for verification").
  - high locality of design units may make testing significantly easier (module testing).
  - particular testing interfaces may improve testability (e.g. allow injection of user input not only via GUI or provide particular (eg. output for tests).
- **changeability** **maintainability**
  - most systems have to be changed or maintained.
  - in particular, when requirements change.
  - **risk assessment**: parts of the system with high probability for changes should be designed such that changes are possible with acceptable effort (abstract, modularise, encapsulate).
- **portability**
  - porting: adaptation to different platform (OS, hardware, infrastructure).
  - systems with a long lifetime may need to be adapted to different platforms over time.
  - **flexibility**: parts of the system may be changed or extended (e.g. via plug-in architecture).

Development Approaches



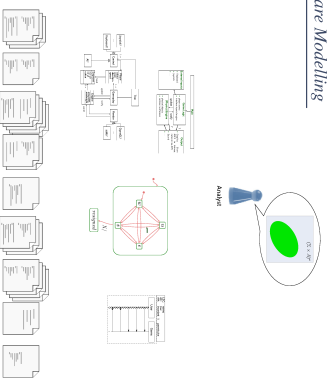
- **top-down** risk: needed functionality hard to realise on target platform.
- **bottom-up** risk: lower-level units do not "fit together".
- **inside-out** risk: user interface needed by customer hard to realise with existing system.
- **outside-in** risk: elegant system design not reflected nicely in already fixed UI.

Transform vs. Write-Down-and-Check



Content

- **Design Patterns**
  - ↳ Strategy, Examples
  - **Communicating Finite Automata (CFA)**
    - ↳ concrete and abstract syntax, networks of CFA,
    - ↳ operational semantics,
    - **Transition Sequences**
    - **Deadlock, Reachability**
  - **Uppaal**
    - ↳ tool demo (simulator),
    - ↳ query language,
    - ↳ CFA model-checking,
    - **CFA at Work**
      - ↳ drive-to-configuration, scenarios, invariants
      - ↳ tool demo (verifier),
      - **CFA vs. Software**

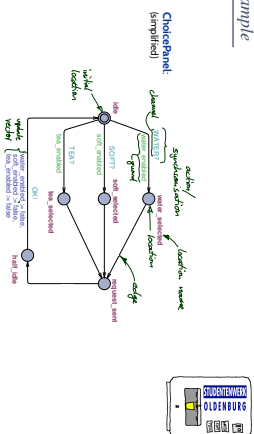


17/55

Communicating Finite Automata  
presentation follows (Olderog and Durck, 2008)

18/55

Example



19/55

Channel Names and Actions

- To define communicating finite automata, we need the following sets of symbols:
    - A set  $\{a, b\} \in \text{Chan}$  of channel names or channels;
    - For each channel  $a \in \text{Chan}$ , two visible actions:  $a^?$  and  $a^!$  denote input and output on the channel  $\{a^?, a^! \in \text{Chan}\}$ ;
    - $r \notin \text{Chan}$  represents an internal action, not visible from outside;
    - $\{a, b\} \in \text{Act} := \{a^? \mid a \in \text{Chan}\} \cup \{a^! \mid a \in \text{Chan}\} \cup \{r\}$  is the set of actions;
    - An alphabet  $B$  is a set of channels, i.e.  $B \subseteq \text{Chan}$ ;
    - For each alphabet  $B$ , we define the corresponding action set  $B^{\#} := \{a^? \mid a \in B\} \cup \{a^! \mid a \in B\} \cup \{r\}$ ;
- Note:  $\text{Chan}^{\#} = \text{Act}$ .

20/55

Integer Variables and Expressions, Resets

- Let  $\{r, w \in \mathbb{N}\} \cup V$  be a set of (finite domain) integer variables;
- By  $\{v \in \mathbb{N}\} \cup \Psi(V)$  we denote the set of integer expressions over  $V$  using function symbols  $+, \dots$ , and relation symbols  $<, \leq, \dots$ ;
- A modification on  $v \in V$  of the form  $v := e$ ,  $v \leftarrow v + 1$ ,  $v \in V$ ,  $e \in \Psi(V)$ ;
- By  $R(V)$  we denote the set of all modifications;
- By  $\tau$  we denote a finite list  $(\tau_1, \dots, \tau_n)$ ,  $n \in \mathbb{N}_0$ , of modifications  $\tau_i \in R(V)$ .  $\tau$  is called reset vector (or update vector) ( $\tau$  is the empty list ( $n = 0$ ))
- By  $R(V)^{\#}$  we denote the set of all such finite lists of modifications;

21/55

Communicating Finite Automata

**Definition. A communicating finite automaton is a structure**

$$\mathcal{A} = (L, B, V, E, f_{ini})$$

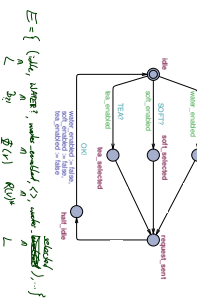
where

- $L \in \mathcal{P}(S)$  is a finite set of locations (or control states);
- $B \subseteq \text{Chan}$ ;
- $V$ : a set of data variables;
- $E \subseteq L \times B^{\#} \times \Psi(V) \times R(V)^{\#} \times L$  a finite set of directed edges such that  $(L, \alpha, v, \tau, L') \in E \wedge \text{chan}(\alpha) \in B \implies \tau = \text{true}$ ;
- Edges  $(L, \alpha, v, \tau, L')$  from location  $L$  to  $L'$  are labeled with an action  $\alpha$ , a guard  $v$  and a list  $\tau$  of modifications;
- $f_{ini}$  is the initial location;

22/55



$L = \{ \text{idle, water, add\_water, ...} \}$   
 $S = \{ \text{water, soft, ok, ...} \}$   
 $V = \{ \text{water, add\_water, ...} \}$   
 $L' = \{ \text{idle} \}$



**Definition.**  
 Let  $A_i = (L_i, R_i, V_i, E_i, f_{in,i}), 1 \leq i \leq n$ , be communicating finite automata.  
 The operational semantics of the network of CFA  $C(A_1, \dots, A_n)$  is the labeled transition system

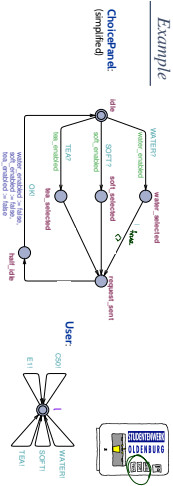
$$T(C(A_1, \dots, A_n)) = (Conf^c, Chan \cup \{ \tau \}, \xrightarrow{\cdot}, X \in Chan \cup \{ \tau \}, C_{init})$$

where

- $V = \bigcup_{i=1}^n V_i$ ,  $V_i$  is the set of variables of  $A_i$ .
- $Conf^c = \{ (\vec{r}, \vec{s}) \mid \vec{r} \in L_i, \vec{s} : V \rightarrow \mathcal{P}(V) \}$ .
- $C_{init} = \{ (\vec{r}_{init}, \vec{s}_{init}) \}$  with  $\vec{s}_{init}(\varphi) = \emptyset$  for all  $\varphi \in V$ .

The transition relation consists of transitions of the following two types

- An internal transition  $(\vec{r}, v) \xrightarrow{\tau} (\vec{r}', v')$  occurs if there is  $i \in \{1, \dots, n\}$  and
    - there is a  $\tau$ -edge  $(L_i, \tau, \varphi, \vec{r}', L_i) \in E_i$  such that
      - $v = \varphi$ , "source valuation satisfies guard"
      - $\vec{r}' = R_i$ , "automaton  $i$  changes location"
      - $v' = v \uparrow R_i$ , " $v'$  is the result of applying  $\vec{r}$  on  $v$ "
  - A synchronization transition  $(\vec{r}, v) \xrightarrow{\tau} (\vec{r}', v')$  occurs if there are  $i, j \in \{1, \dots, n\}$  with  $i \neq j$  and
    - there are edges  $(L_i, \tau, \varphi_i, \vec{r}', L_i) \in E_i$  and  $(L_j, \tau, \varphi_j, \vec{r}', L_j) \in E_j$  such that
      - $v = \varphi_i \wedge \varphi_j$ , "source valuation satisfies guards (0)"
      - $\vec{r}' = R_i = R_j$ , "automaton  $i$  and  $j$  change location"
      - $v' = v \uparrow (R_i \uparrow R_j)$ , " $v'$  is the result of applying  $\vec{r}$  on  $v$ "
- The style of communication is known under the name "rendezvous", "synchronous", "blocking" (communication is not possibly many others).



$\langle \text{idle}, \emptyset \rangle \xrightarrow{\text{water}} \langle \text{add\_water}, \emptyset \rangle \xrightarrow{\tau} \langle \text{water}, \emptyset \rangle \xrightarrow{\tau} \langle \text{no\_water}, \emptyset \rangle$

- $v : V \rightarrow \mathcal{P}(V)$  is a valuation of the variables.
  - Evaluation  $v'$  of the variables canonically assigns an integer value  $v'(a)$  to each integer expression  $a \in \mathcal{A}(V)$ .
  - $\models \subseteq (V \rightarrow \mathcal{P}(V)) \times \mathcal{A}(V)$  is the canonical satisfaction relation between valuations and integer expressions from  $\mathcal{A}(V)$ .
  - Effect of modification  $r \in \mathcal{R}(V)$  on  $v$ , denoted by  $v \uparrow r$ :
 
$$(v \uparrow r)(a) = \begin{cases} v(a), & \text{if } a = r, \\ v'(a), & \text{otherwise} \end{cases}$$
  - We set  $v \uparrow \{r_1, \dots, r_n\} := v \uparrow r_1 \uparrow \dots \uparrow r_n = ((v \uparrow r_1) \uparrow r_2) \uparrow \dots \uparrow r_n$ .
- That is, modifications are executed sequentially from left to right.

- A transition sequence of  $C(A_1, \dots, A_n)$  is any (infinite) sequence of the form
 
$$\langle \vec{r}_0, v_0 \rangle \xrightarrow{\lambda_1} \langle \vec{r}_1, v_1 \rangle \xrightarrow{\lambda_2} \langle \vec{r}_2, v_2 \rangle \xrightarrow{\lambda_3} \dots$$
- with  $\langle \vec{r}_i, v_i \rangle = C_{init}$ .
- For all  $i \in \mathbb{N}$ , there is  $\lambda_{i+1} \in T(C(A_1, \dots, A_n))$  with  $(\vec{r}_i, v_i) \xrightarrow{\lambda_{i+1}} (\vec{r}_{i+1}, v_{i+1})$ .

- A configuration  $(\vec{c}, \nu)$  of  $C(A_1, \dots, A_n)$  is called **deadlock** if and only if there are no transitions from  $(\vec{c}, \nu)$ , i.e. if  $\neg(\exists \lambda \in \Lambda \exists \beta' \nu' \in \text{Conf} \bullet \vec{c} \nu \xrightarrow{\lambda} \beta' \nu')$ .
- The network  $C(A_1, \dots, A_n)$  is said to **have a deadlock** if and only if there is a reachable configuration  $(\vec{c}, \nu)$  which is a deadlock.

- A configuration  $(\vec{c}, \nu)$  is called **reachable** (in  $C(A_1, \dots, A_n)$ ) from  $(\vec{c}_0, \nu_0)$  if and only if there is a transition sequence of the form  $(\vec{c}_0, \nu_0) \xrightarrow{\lambda_1} (\vec{c}_1, \nu_1) \xrightarrow{\lambda_2} (\vec{c}_2, \nu_2) \xrightarrow{\lambda_3} \dots \xrightarrow{\lambda_m} (\vec{c}_m, \nu_m) = (\vec{c}, \nu)$ .
- A configuration  $(\vec{c}, \nu)$  is called **reachable** (without 'from') if and only if it is reachable from  $\mathcal{Q}_{\text{init}}$ .
- A location  $\ell \in L_i$  is called **reachable** if and only if **any** configuration  $(\vec{c}, \nu)$  with  $\ell_i = \ell$  is reachable, i.e. there exist  $\ell$  and  $\nu$  such that  $\ell_i = \ell$  and  $(\vec{c}, \nu)$  is reachable.

Uppaal  
(Larsen et al., 1997; Behrmann et al., 2004)

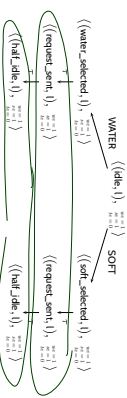
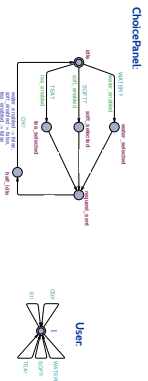
The Uppaal Query Language

- Consider  $N = C(A_1, \dots, A_n)$  over data variables  $V$ .
- basic formula**  
 $atom ::= A_i \ell \mid \nu \mid \text{deadlock}$   
 where  $\ell \in L_i$  is a location and  $\nu$  an expression over  $V$ .
- configuration formula**  
 $term ::= atom \mid \text{not term} \mid term \text{ and } term_2$
- existential path formulae**  
 $e\text{-formula} ::= \exists \phi \text{ term} \mid \exists \square \text{ term}$   
 (exists finally) (exists globally)
- universal path formulae**  
 $a\text{-formula} ::= \forall \phi \text{ term} \mid \forall \square \text{ term} \mid term_1 \rightarrow term_2$   
 (Always finally) (Always globally) (leads to)
- formulae for queries**  
 $F ::= e\text{-formula} \mid a\text{-formula}$

Satisfaction of Uppaal Queries by Configurations

- The **satisfaction relation** between configurations  $(\vec{c}, \nu) = ((\ell_1, \dots, \ell_n), \nu)$  of a network  $C(A_1, \dots, A_n)$  and formulae  $F$  of the Uppaal logic is defined inductively as follows:
  - $(\vec{c}, \nu) \models P$
  - $(\vec{c}, \nu) \models \text{deadlock}$  iff  $\langle \vec{c}, \nu \rangle$  is a *deadlock configuration*.
  - $(\vec{c}, \nu) \models A_i \ell$  iff  $\ell_i = \ell$
  - $(\vec{c}, \nu) \models \nu$  iff  $\nu \models \nu$
  - $(\vec{c}, \nu) \models \text{not term}$  iff  $\langle \vec{c}, \nu \rangle \not\models term$
  - $(\vec{c}, \nu) \models term_1 \text{ and } term_2$  iff  $\langle \vec{c}, \nu \rangle \models term_1$  and  $\langle \vec{c}, \nu \rangle \models term_2$

Example: Computation Paths vs. Computation Tree



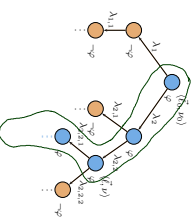
35/55

Satisfaction of Uppaal Queries by Configurations

Exists globally:

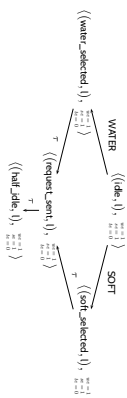
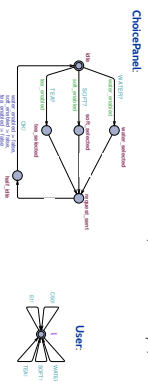
- $\langle \bar{c}_0, w_0 \rangle \models \exists \square \text{ term}$
- iff  $\exists$  path  $\xi$  of  $N$  starting in  $\langle \bar{c}_0, w_0 \rangle$   
 $\forall t \in \mathbb{N}_0, \bullet \xi^t \models \text{term}$

Example:  $\langle \bar{c}_0, w_0 \rangle \models \exists \square \varphi$



38/55

Example: Computation Paths vs. Computation Graph (or: Transition Graph)



36/55

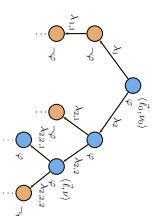
Satisfaction of Uppaal Queries by Configurations

Exists globally:

- $\langle \bar{c}_0, w_0 \rangle \models \exists \square \text{ term}$
- iff  $\exists$  path  $\xi$  of  $N$  starting in  $\langle \bar{c}_0, w_0 \rangle$   
 $\forall t \in \mathbb{N}_0, \bullet \xi^t \models \text{term}$

"on some computation path, all configurations satisfy term"

Example:  $\langle \bar{c}_0, w_0 \rangle \models \exists \square \varphi$



38/55

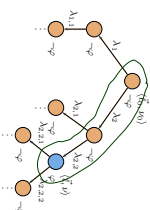
Satisfaction of Uppaal Queries by Configurations

Exists finally:

- $\langle \bar{c}_0, w_0 \rangle \models \exists \diamond \text{ term}$
- iff  $\exists$  path  $\xi$  of  $N$  starting in  $\langle \bar{c}_0, w_0 \rangle$   
 $\exists t \in \mathbb{N}_0, \bullet \xi^t \models \text{term}$

"Some configuration satisfying term is reachable"

Example:  $\langle \bar{c}_0, w_0 \rangle \models \exists \diamond \varphi$



37/55

Satisfaction of Uppaal Queries by Configurations

Always globally:

- $\langle \bar{c}_0, w_0 \rangle \models \forall \square \text{ term}$
- iff  $\langle \bar{c}_0, w_0 \rangle \models \exists \square \neg \text{term}$

"not (on some computation path, all configurations satisfy ~term)"  
 or "all reachable configurations satisfy term"

Always finally:

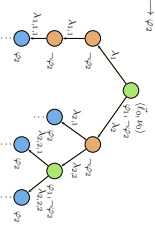
- $\langle \bar{c}_0, w_0 \rangle \models \forall \diamond \text{ term}$
- iff  $\langle \bar{c}_0, w_0 \rangle \not\models \exists \square \neg \text{term}$

"not (on some computation path, all configurations satisfy ~term)"  
 or "on all computation paths, there is a configuration satisfying term"

39/55

- Leads to:
  - If path  $\xi$  of  $\lambda$  starting in  $(\bar{c}_0, w_0) \forall i \in \mathbb{N}_0$ 
    - $\xi \models \text{form}_1 \implies \xi \models \forall \text{form}_2$

On all paths from each configuration satisfying  $\text{form}_1$ , a configuration satisfying  $\text{form}_2$  is reachable (response pattern)



Example:  $(\bar{c}_0, w_0) \models p_1 \rightarrow p_2$

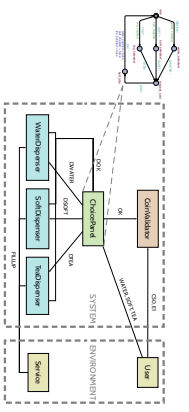
**Definition.** Let  $X = C(A_1, \dots, A_n)$  be a network and  $P$  a query.  
 (i) We say  $X$  satisfies  $P$ , denoted by  $X \models P$ , if and only if  $C_{\text{init}} \models P$ .  
 (ii) The model-checking problem for  $X$  and  $P$  is to decide whether  $(X, P) \in \text{MC}$ .

**Proposition:**  
 The model-checking problem for communicating finite automata is **decidable**.

- Design Patterns
  - Strategy, Examples
  - Communicating Finite Automata (CFA)
    - concrete and abstract syntax,
    - networks of CFA,
    - operational semantics.
  - Transition Sequences
- Deadlock, Reachability
  - Uppaal
    - tool demo (formal),
    - query language,
    - CFA model-checking.
- CFA at Work
  - drive to configuration scenarios, invariants
  - tool demo (verified)
- CFA vs. Software

CFA and Queries at Work

Model Architecture — Who Talks What to Whom



- Shared variables:
  - bool water-enabled, not-enabled, tea-enabled;
  - int v = 3, s = 3, c = 3;
- Note: Our model does not use scopes ("information hiding") for channels. That is, Service could send WATER if the modeller wanted to.

Design Sanity Check: Drive to Configuration

- Question: Is it (at all) possible to have no water in the vending machine model? (Of course, the design is arbitrary.)
- Approach: Check whether a configuration satisfying  $w = 0$  is reachable, i.e. check  $\text{Reach} \models \exists w = 0$  for the vending machine model  $\text{VM}$ .





### Design Check: Scenarios

- Question: Is the following existential LSC satisfied by the model? (Otherwise, the design is definitely broken)



- Approach: Use the following newly created CFA Scenario



- instead of User and check whether location end\_of\_scenario is reachable, i.e. check

$$A_{VM} \models \exists \text{Scenario. end\_of\_scenario}$$

- for the modified vending machine model  $A_{VM}^*$ .

4/6/25

### Design Verification: Invariants

- Question: Is it the case that the "tea" button is **only** enabled if there is €150 in the machine? (Otherwise, the design is broken)



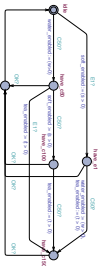
- Approach: Check whether the implication

$$\text{tea\_enabled} \implies \text{Coinvalidator.have\_c150}$$

- holds in all reachable configurations, i.e. check

$$A_{VM} \models \forall \square \text{tea\_enabled} \implies \text{Coinvalidator.have\_c150}$$

- for the vending machine model  $A_{VM}^*$ .



4/7/25

### Design Verification: Sanity Check

- Question: Is the "tea" button **ever** enabled? (Otherwise, the considered invariant



$$\text{tea\_enabled} \implies \text{Coinvalidator.have\_c150}$$

holds vacuously)

- Approach: Check whether a configuration satisfying  $\text{tea\_enabled} = 1$  is reachable. Exactly like we did with  $w = 0$  earlier.

4/8/25

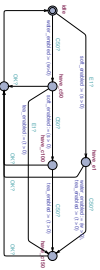
### Design Verification: Another Invariant

- Question: Is it the case that, if there is money in the machine and water is stuck, that the "water" button is enabled?



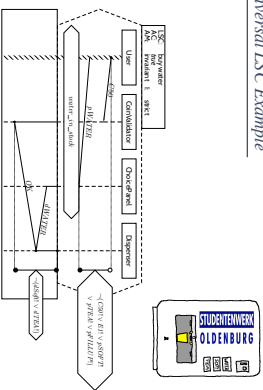
- Approach: Check

$$A_{VM} \models \forall \square (\text{Coinvalidator.have\_c50} \vee \text{Coinvalidator.have\_c100} \vee \text{Coinvalidator.have\_c150}) \implies \text{water\_enabled}$$



4/9/25

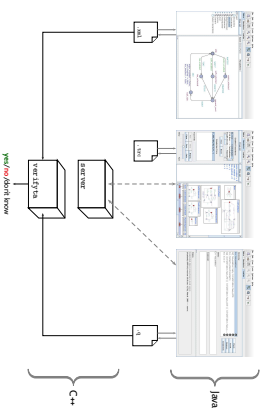
### Recall: Universal LSC Example



4/10/25

50/25

### Uppaal Architecture



5/1/25

51/25

- Design Patterns
  - Strategy, Examples
- Communicating Finite Automata (CFA)
  - concrete and abstract syntax,
  - networks of CFA,
  - operational semantics,
- Transition Sequences
- Deadlock, Reachability
- Upqpal
  - tool demo (simulator),
  - query language,
  - CFA model-checking
- CFA at Work
  - drive to configuration, scenarios, invariants
  - tool demo (verifier)
- CFA vs. Software

52/25

### Tell Them What You've Told Them...

- A network of communicating finite automata
  - describes a labelled transition system,
  - can be used to model software behaviour.
- The Upqpal Query Language can be used to
  - formalize reachability (EX, CF,  $\forall$ CF, ...) and
  - **testable** ( $CF_1 \rightarrow CF_2$ ) properties.
- Since the model-checking problem of CFA is **decidable**,
  - there are tools which **automatically check** whether a network of CFA satisfies a given query.
- Use model-checking, e.g. to
  - **obtain a computation path** to a certain configuration (*drive-to-configuration*),
  - **check whether a scenario is possible**,
  - **check whether an invariant is satisfied**.
- (if not, analyse the design further using the obtained **counter-example**)

53/25

### References

### References

- Amodeo, C. (1979). *The Invention Logic of Building*. Oxford University Press.
- Amodeo, C., Lohman, S., and Shoenen, M. (1977). *A User's Introduction to Logic*. McGraw-Hill, New York.
- Bertalan, C., David, A., and Lamm, C. (2024). A formal model of the design process. In *Proceedings of the ACM Conference on Design and Architecture in Software (DAS)*, pages 1–12. ACM, New York.
- Busch, M. (2007). *Model Checking*. Springer, Berlin.
- Clarke, E.M., Long, K.C., Peterson, L., and Xu, W. (1997). *Methods in Model Checking*. MIT Press, Cambridge, MA.
- Clarke, E.M., Long, K.C., and Lu, H. (2008). *Model Checking*. Morgan Kaufmann, San Francisco, CA.

55/25

54/25