

Softwaretechnik / Software-Engineering

Lecture 17: Wrapup & Questions

2019-07-22

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Topic Area Code Quality Assurance: Content

VL 14	● Introduction and Vocabulary
⋮	● Test case, test suite, test execution.
⋮	● Positive and negative outcomes.
VL 15	● Limits of Software Testing
⋮	● Glass-Box Testing
⋮	● Statement-, branch-, term-coverage.
⋮	● Other Approaches
⋮	● Model-based testing,
VL 16	● Program Verification
⋮	● partial and total correctness,
⋮	● Proof System PD.
VL 17	● Runtime verification.
⋮	● Review

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Proof-System PD Cont'd

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Proof-System PD (for sequential, deterministic programs)

Axiom 1: Skip-Statement

$$\{p\} \text{ skip } \{p\}$$

Axiom 2: Assignment

$$\{p[u := t]\} u := t \{p\}$$

Rule 3: Sequential Composition

$$\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$$

Rule 4: Conditional Statement

$$\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$$

Rule 5: While-Loop

$$\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}}$$

Rule 6: Consequence

$$\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$$

Theorem. PD is correct ("sound") and (relative) complete for partial correctness of deterministic programs, i.e. $\vdash_{PD} \{p\} S \{q\}$ if and only if $\models \{p\} S \{q\}$.

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Example Proof

$$DIV \equiv \overbrace{a := 0; b := x;}^{=:S_0^D} \text{ while } \overbrace{b \geq y}^{=:B^D} \text{ do } \overbrace{b := b - y; a := a + 1}^{=:S_1^D} \text{ od}$$

(The first (textually represented) program that has been formally verified (Hoare, 1969).

We can prove $\models \{x \geq 0 \wedge y \geq 0\} DIV \{a \cdot y + b = x \wedge b < y\}$

by showing $\vdash_{PD} \underbrace{\{x \geq 0 \wedge y \geq 0\}}_{=:p^D} DIV \underbrace{\{a \cdot y + b = x \wedge b < y\}}_{=:q^D}$, i.e., derivability in PD:

$$\frac{\frac{(1) \quad \{p^D\} S_0^D \{P\}, \quad \frac{\frac{(2) \quad \{P \wedge (B^D)\} S_1^D \{P\}}{P \rightarrow P, \{P\} \text{ while } B^D \text{ do } S_1^D \text{ od } \{P \wedge \neg(B^D)\}}, \quad \frac{(3) \quad P \wedge \neg(B^D) \rightarrow q^D}{P \wedge \neg(B^D) \rightarrow q^D}}{\{P\} \text{ while } B^D \text{ do } S_1^D \text{ od } \{q^D\}} \quad (R5) \quad (R6)}{\{p^D\} S_0^D; \text{ while } B^D \text{ do } S_1^D \text{ od } \{q^D\}} \quad (R3)$$

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(A1) $\{p\} \text{ skip } \{p\}$	(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$	(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}}$
(A2) $\{p[u := t]\} u := t \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$	(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

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Example Proof

$$DIV \equiv \overbrace{a := 0; b := x;}^{=:S_0^D} \text{ while } \overbrace{b \geq y}^{=:B^D} \text{ do } \overbrace{b := b - y; a := a + 1}^{=:S_1^D} \text{ od}$$

(The first (textually represented) program that has been formally verified (Hoare, 1969).

We can prove $\models \{x \geq 0 \wedge y \geq 0\} DIV \{a \cdot y + b = x \wedge b < y\}$

by showing $\vdash_{PD} \underbrace{\{x \geq 0 \wedge y \geq 0\}}_{=:p^D} DIV \underbrace{\{a \cdot y + b = x \wedge b < y\}}_{=:q^D}$, i.e., derivability in PD:

$$\frac{\frac{(1) \quad \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}, \quad \frac{\frac{(2) \quad \{P \wedge (b \geq y)\} b := b - y; a := a + 1 \{P\}}{P \rightarrow P, \{P\} \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{P \wedge \neg(b \geq y)\}}, \quad \frac{(3) \quad P \wedge \neg(b \geq y) \rightarrow a \cdot y + b = x \wedge b < y}{P \wedge \neg(b \geq y) \rightarrow a \cdot y + b = x \wedge b < y}}{\{P\} \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{a \cdot y + b = x \wedge b < y\}} \quad (R5) \quad (R6)}{\{x \geq 0 \wedge y \geq 0\} a := 0; b := x; \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{a \cdot y + b = x \wedge b < y\}} \quad (R3)$$

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(A1) $\{p\} \text{ skip } \{p\}$	(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$	(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}}$
(A2) $\{p[u := t]\} u := t \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$	(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

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Example Proof Cont'd

$$\begin{array}{c}
 \text{(1)} \quad \frac{}{\{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}} \quad \frac{\frac{}{P \rightarrow P} \quad \frac{\frac{}{\{P \wedge (b \geq y)\} b := b - y; a := a + 1 \{P\}}{\{P\} \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{P \wedge \neg(b \geq y)\}} \quad \text{(R5)} \quad \frac{}{P \wedge \neg(b \geq y) \rightarrow a \cdot y + b = x \wedge b < y} \quad \text{(3)}}{\{P\} \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{a \cdot y + b = x \wedge b < y\}} \quad \text{(R6)} \\
 \hline
 \{x \geq 0 \wedge y \geq 0\} a := 0; b := x; \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{a \cdot y + b = x \wedge b < y\} \quad \text{(R3)}
 \end{array}$$

In the following, we show

- (1) $\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\},$
- (2) $\vdash_{PD} \{P \wedge b \geq y\} b := b - y; a := a + 1 \{P\},$
- (3) $\models P \wedge \neg(b \geq y) \rightarrow a \cdot y + b = x \wedge b < y.$

As **loop invariant**, we choose (**creative act!**):

$$P \equiv a \cdot y + b = x \wedge b \geq 0$$

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Proof of (1)

$$\begin{array}{ll}
 \text{(A1)} \quad \{p\} \text{ skip } \{p\} & \text{(R4)} \quad \frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}} \\
 \text{(A2)} \quad \{p[u := t]\} u := t \{p\} & \text{(R5)} \quad \frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}} \\
 \text{(R3)} \quad \frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}} & \text{(R6)} \quad \frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}
 \end{array}$$

- (1) claims:

$$\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$$

where $P \equiv a \cdot y + b = x \wedge b \geq 0.$

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Proof of (1)

(A1) $\{p\} \text{ skip } \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
(A2) $\{p[u := t]\} u := t \{p\}$	(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}}$
(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$	(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

• (1) claims:

$$\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$$

$$\text{where } P \equiv a \cdot y + b = x \wedge b \geq 0.$$

$$\bullet \vdash_{PD} \underbrace{\{0 \cdot y + x = x \wedge x \geq 0\}}_{\rho[a:=0]} a := \underbrace{0}_{\tau} \underbrace{\{a \cdot y + x = x \wedge x \geq 0\}}_{\rho} \text{ by (A2),}$$

Proof of (1)

(A1) $\{p\} \text{ skip } \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
(A2) $\{p[u := t]\} u := t \{p\}$	(R5) $\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}}$
(R3) $\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$	(R6) $\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

• (1) claims:

$$\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$$

$$\text{where } P \equiv a \cdot y + b = x \wedge b \geq 0.$$

$$\bullet \vdash_{PD} \{0 \cdot y + x = x \wedge x \geq 0\} a := 0 \{a \cdot y + x = x \wedge x \geq 0\} \text{ by (A2),}$$

$$\bullet \vdash_{PD} \{a \cdot y + x = x \wedge x \geq 0\} b := x \{a \cdot y + b = x \wedge b \geq 0\} \text{ by (A2),}$$

Proof of (1)

(A1) $\{p\} \text{ skip } \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
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• (1) claims:

$$\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$$

where $P \equiv a \cdot y + b = x \wedge b \geq 0$.

• $\vdash_{PD} \{0 \cdot y + x = x \wedge x \geq 0\} a := 0 \{a \cdot y + x = x \wedge x \geq 0\}$ by (A2),

• $\vdash_{PD} \{a \cdot y + x = x \wedge x \geq 0\} b := x \{\underbrace{a \cdot y + b = x \wedge b \geq 0}_{\equiv P}\}$ by (A2),

• thus, $\vdash_{PD} \{0 \cdot y + x = x \wedge x \geq 0\} a := 0; b := x \{P\}$ by (R3),

• using $x \geq 0 \wedge y \geq 0 \rightarrow 0 \cdot y + x = x \wedge x \geq 0$ and $P \rightarrow P$, we obtain

$$\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$$

by (R6). □

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Substitution

The rule 'Assignment' uses (syntactical) **substitution**: $\{p[u := t]\} u := t \{p\}$

(In formula p , replace all (free) occurrences of (program or logical) variable u by term t .)

Defined as usual, only **indexed** and **bound** variables need to be treated specially:

$$a \geq x [x := u+3] \rightsquigarrow a \geq u+3$$

$$(a \geq x \wedge \forall x \bullet b \geq x) [x := u+3] \rightsquigarrow ?$$

$$\rightsquigarrow a \geq x \wedge \forall z \bullet b \geq z \rightsquigarrow a \geq u+3 \wedge \forall z \bullet b \geq z$$

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Substitution

The rule 'Assignment' uses (syntactical) **substitution**: $\{p[u := t]\} u := t \{p\}$

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Defined as usual, only **indexed** and **bound** variables need to be treated specially:

Expressions:

- plain variable x : $x[u := t] \equiv \begin{cases} t & , \text{ if } x = u \\ x & , \text{ otherwise} \end{cases}$
- constant c :
 $c[u := t] \equiv c$.
- constant op , terms s_i :
 $op(s_1, \dots, s_n)[u := t]$
 $\equiv op(s_1[u := t], \dots, s_n[u := t]).$
- conditional expression:
 $(B ? s_1 : s_2)[u := t]$
 $\equiv (B[u := t] ? s_1[u := t] : s_2[u := t])$

Formulae:

- **boolean expression** $p \equiv s$:
 $p[u := t] \equiv s[u := t]$
- **negation**:
 $(\neg q)[u := t] \equiv \neg(q[u := t])$
- **conjunction etc.**:
 $(q \wedge r)[u := t]$
 $\equiv q[u := t] \wedge r[u := t]$
- **quantifier**:
 $(\forall x : q)[u := t] \equiv \forall y : q[x := y][u := t]$
 y fresh (not in q, t, u), same type as x .

- indexed variable**, u plain or $u \equiv b[t_1, \dots, t_m]$ and $a \neq b$:

$$(a[s_1, \dots, s_n])[u := t] \equiv a[s_1[u := t], \dots, s_n[u := t]]$$
- indexed variable**, $u \equiv a[t_1, \dots, t_m]$:

$$(a[s_1, \dots, s_n])[u := t] \equiv (\bigwedge_{i=1}^n s_i[u := t] = t_i ? t : a[s_1[u := t], \dots, s_n[u := t]])$$

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Example Proof Cont'd

$$\begin{array}{c}
\text{(2)} \\
\frac{\{P \wedge (b \geq y)\} b := b - y; a := a + 1 \{P\}}{P \rightarrow P, \quad \{P\} \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{P \wedge (b \geq y)\}, \quad P \wedge (b \geq y) \rightarrow a \cdot y + b = x \wedge b < y} \text{(R5)} \quad \text{(3)} \\
\frac{\{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}, \quad \{P\} \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{a \cdot y + b = x \wedge b < y\}}{\{x \geq 0 \wedge y \geq 0\} a := 0; b := x; \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od } \{a \cdot y + b = x \wedge b < y\}} \text{(R6)} \quad \text{(R3)}
\end{array}$$

In the following, we show

- (1) $\vdash_{PD} \{x \geq 0 \wedge y \geq 0\} a := 0; b := x \{P\}$, ✓
- (2) $\vdash_{PD} \{P \wedge b \geq y\} b := b - y; a := a + 1 \{P\}$,
- (3) $\models P \wedge \neg(b \geq y) \rightarrow a \cdot y + b = x \wedge b < y$.

As **loop invariant**, we choose (**creative act!**):

$$P \equiv a \cdot y + b = x \wedge b \geq 0$$

$$\begin{array}{lll}
\text{(A1)} \{p\} \text{ skip } \{p\} & \text{(R3)} \frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}} & \text{(R5)} \frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}} \\
\text{(A2)} \{p[u := t]\} u := t \{p\} & \text{(R4)} \frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}} & \text{(R6)} \frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}
\end{array}$$

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Proof of (2)

(A1) $\{p\} \text{ skip } \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
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• (2) claims:

$$\vdash_{PD} \{P \wedge b \geq y\} b := b - y; a := a + 1 \{P\}$$

$$\text{where } P \equiv a \cdot y + b = x \wedge b \geq 0.$$

- $\vdash_{PD} \{(a+1) \cdot y + \underbrace{(b-y)}_{\substack{\leftarrow \\ a}} = x \wedge \underbrace{(b-y)}_{\substack{\leftarrow \\ b}} \geq 0\} b := b - y \{(a+1) \cdot y + \underbrace{b}_{\substack{\leftarrow \\ b}} = x \wedge \underbrace{b}_{\substack{\leftarrow \\ b}} \geq 0\}$
by (A2),

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Proof of (2)

(A1) $\{p\} \text{ skip } \{p\}$	(R4) $\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
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• (2) claims:

$$\vdash_{PD} \{P \wedge b \geq y\} b := b - y; a := a + 1 \{P\}$$

$$\text{where } P \equiv \underbrace{a \cdot y + b = x \wedge b \geq 0}_{\substack{\rightarrow ?}}$$

- $\vdash_{PD} \{(a+1) \cdot y + (b-y) = x \wedge (b-y) \geq 0\} b := b - y \{(a+1) \cdot y + b = x \wedge b \geq 0\}$
by (A2),

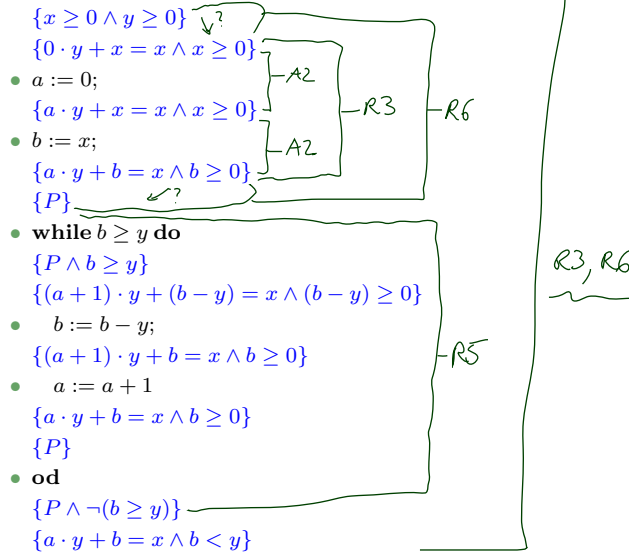
- $\vdash_{PD} \{(a+1) \cdot y + b = x \wedge b \geq 0\} a := a + 1 \underbrace{\{a \cdot y + b = x \wedge b \geq 0\}}_{\substack{\equiv P \\ \checkmark}} \text{ by (A2),}$

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Once Again

- $P \equiv a \cdot y + b = x \wedge b \geq 0$

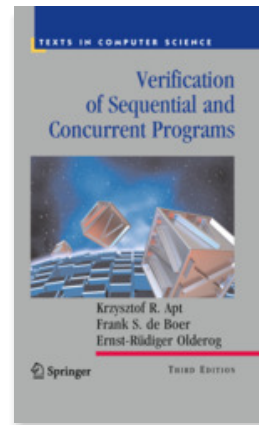


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(A1)	$\{p\} \text{ skip } \{p\}$
(A2)	$\{p[u := t]\} u := t \{p\}$
(R3)	$\frac{\{p\} S_1 \{r\}, \{r\} S_2 \{q\}}{\{p\} S_1; S_2 \{q\}}$
(R4)	$\frac{\{p \wedge B\} S_1 \{q\}, \{p \wedge \neg B\} S_2 \{q\}}{\{p\} \text{ if } B \text{ then } S_1 \text{ else } S_2 \text{ fi } \{q\}}$
(R5)	$\frac{\{p \wedge B\} S \{p\}}{\{p\} \text{ while } B \text{ do } S \text{ od } \{p \wedge \neg B\}}$
(R6)	$\frac{p \rightarrow p_1, \{p_1\} S \{q_1\}, q_1 \rightarrow q}{\{p\} S \{q\}}$

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Literature Recommendation



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- **Formal Program Verification**
 - **Proof System PD**
- **The Verifier for Concurrent C**
 - Assertions, Modular Verification, VCC
- **Runtime-Verification**
 - **Assertions**, LSC-Observers
- **Reviews**
 - **Roles** and **artefacts**
 - Review **procedure**
 - Stronger and weaker **variants**
- **Code QA Techniques** Revisited
 - **Test, Runtime-Verification, Review,**
 - **Static Checking, Formal Verification**
- **Do's and Don'ts** in Code QA
- **Dependability**

The Verifier for Concurrent C

- The **Verifier for Concurrent C** (VCC) basically implements Hoare-style reasoning.
- **Special syntax:**
 - `#include <vcc.h>`
 - `_(requires p)` — **pre-condition**, *p* is (basically) a C expression
 - `_(ensures q)` — **post-condition**, *q* is (basically) a C expression
 - `_(invariant expr)` — **loop invariant**, *expr* is (basically) a C expression
 - `_(assert p)` — **intermediate invariant**, *p* is (basically) a C expression
 - `_(writes &v)` — VCC considers **concurrent** C programs; we need to declare for each procedure which global variables it is allowed to write to (also checked by VCC)
- **Special expressions:**
 - `\thread_local(&v)` — no other thread writes to variable *v* (in pre-conditions)
 - `\old(v)` — the value of *v* when procedure was called (useful for post-conditions)
 - `\result` — return value of procedure (useful for post-conditions)

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VCC Syntax Example

```

1  #include <vcc.h>
2
3  int a, b;
4
5  void div( int x, int y )
6  {
7      _requires x >= 0 && y >= 0;
8      _ensures a * y + b == x && b < y;
9      _writes &a;
10     _writes &b;
11
12     {
13         a = 0;
14         b = x;
15         while (b >= y)
16         {
17             _invariant a * y + b == x && b >= 0;
18             b = b - y;
19             a = a + 1;
20         }
21     }
22 }

```

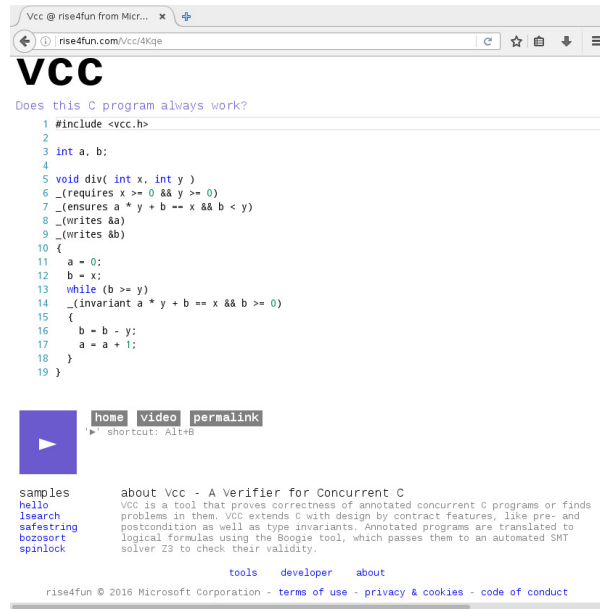
Handwritten annotations: $\{P\}$ next to line 6, $\{Q\}$ next to line 8, and P next to line 17.

$$DIV \equiv a := 0; b := x; \text{ while } b \geq y \text{ do } b := b - y; a := a + 1 \text{ od}$$

$$\{x \geq 0 \wedge y \geq 0\} DIV \{x \geq 0 \wedge y \geq 0\}$$

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Example program *DIV*: <http://rise4fun.com/Vcc/4Kqe>

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Interpretation of Results

- VCC result: “**verification succeeded**”
 - We can **only** conclude that the tool
 - under its interpretation of the C-standard, under its platform assumptions (32-bit), etc. — claims that there is a proof for $\models \{p\} \text{ DIV } \{q\}$.
 - May be due to an error in the tool! (That’s a **false negative** then.)
 - Yet we can ask **for a printout of the proof** and check it manually (hardly possible in practice) or with other tools like interactive theorem provers.
 - **Note:** $\models \{false\} f \{q\}$ **always** holds.
 - That is, a **mistake** in writing down the pre-condition can make errors in the program go undetected!
- VCC result: “**verification failed**”
 - May be a **false positive** (wrt. the goal of finding errors).
 - The tool **does not provide counter-examples** in the form of a computation path, it (only) gives **hints on input values** satisfying p and causing a violation of q .
 - \rightarrow try to construct a (true) counter-example from the hints.
 - or: make loop-invariant(s) (or pre-condition p) stronger, and try again.
- Other case: “**timeout**” etc. — completely **inconclusive** outcome.

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- For the exercises, we use VCC only for **sequential, single-thread programs**.
- VCC checks a number of **implicit assertions**:
 - **no arithmetic overflow** in expressions (according to C-standard),
 - **array-out-of-bounds access**,
 - **NULL-pointer dereference**,
 - and many more.
- Verification **does not always succeed**:
 - The backend SMT-solver may not be able to discharge proof-obligations (in particular non-linear multiplication and division are challenging);
 - In many cases, we need to provide **loop invariants** manually.
- VCC also supports:
 - **concurrency**:
different threads may write to shared global variables; VCC can check whether concurrent access to shared variables is properly managed;
 - **data structure invariants**:
we may declare invariants that have to hold for, e.g., records (e.g. the length field l is always equal to the length of the string field str); those invariants may **temporarily** be violated when updating the data structure.
 - and much more.

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Modular Reasoning

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Modular Reasoning

We can add another rule for calls of functions $f : F$ (simplest case: only global variables):

$$(R7) \frac{\{p\} F \{q\}}{\{p\} f() \{q\}} \quad \text{implementation}$$

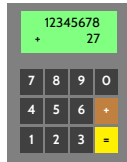
"If we have $\vdash \{p\} F \{q\}$ for the **implementation** of function f ,
then if f is **called** in a state satisfying p , the state after return of f will satisfy q ."

p is called **pre-condition** and q is called **post-condition** of f .

Example: if we have

- $\{true\} \text{read_number} \{0 \leq \text{result} < 10^8\}$
- $\{0 \leq x \wedge 0 \leq y\} \text{add} \{(\text{old}(x) + \text{old}(y) < 10^8 \wedge \text{result} = \text{old}(x) + \text{old}(y)) \vee \text{result} < 0\}$
- $\{true\} \text{display} \{(0 \leq \text{old}(sum) < 10^8 \implies "old(sum)") \wedge (\text{old}(sum) < 0 \implies "-E-")\}$

we may be able to prove our pocket calculator correct.



```
1 int x, y, sum;
2
3 int main() {
4
5     while (true) {
6         x = read_number();
7         y = read_number();
8         sum = add(); // add 'x' and 'y'
9         display(); // display 'sum'
10    }
11 }
```

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Return Values and Old Values

- For **modular reasoning**, it's often useful to refer in the post-condition to
 - the **return value** as result ,
 - the **values** of variable x **at calling time** as $\text{old}(x)$.

- Can be defined using **auxiliary variables**:

- Transform function

$$T f() \{ \dots; \text{return } \text{expr}; \}$$

(over variables $V = \{v_1, \dots, v_n\}$; where $\text{result}, v_i^{\text{old}} \notin V$) into

$$T f() \{$$

$$\text{ } v_1^{\text{old}} := v_1; \dots; v_n^{\text{old}} := v_n;$$

$$\text{ } \dots;$$

$$\text{ } \text{result} := \text{expr};$$

$$\text{ } \text{return } \text{result};$$

$$\text{ } \}$$

over $V' = V \cup \{v^{\text{old}} \mid v \in V\} \cup \{\text{result}\}$.

- Then $\text{old}(x)$ is just an abbreviation for x^{old} .

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Assertions

-17-2019-07-22 - main -

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Assertions

- Extend the **syntax** of **deterministic programs** by

$$S := \dots \mid \text{assert}(B)$$

- and the **semantics** by rule

$$\langle \text{assert}(B), \sigma \rangle \rightarrow \langle E, \sigma \rangle \text{ if } \sigma \models B.$$

(If the asserted boolean expression B does not hold in state σ , the empty program is not reached; otherwise the assertion remains in the first component: **abnormal** program termination).

Extend PD by axiom:

$$(A7) \{p\} \text{assert}(p) \{p\}$$

- That is, if p holds **before** the assertion, then we can **continue** with the derivation in PD.
If p does not hold, we “**get stuck**” (and cannot complete the derivation).
- So we **cannot** derive $\{true\} x := 0; \text{assert}(x = 27) \{true\}$ in PD.

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- **Formal Program Verification**

- **Proof System PD**

- **The Verifier for Concurrent C**

- **Assertions, Modular Verification, VCC**

- **Runtime-Verification**

- **Assertions, LSC-Observers**

- **Reviews**

- **Roles and artefacts**
- **Review procedure**
- **Stronger and weaker variants**

- **Code QA Techniques Revisited**

- **Test, Runtime-Verification, Review, Static Checking, Formal Verification**

- **Do's and Don'ts in Code QA**

- **Dependability**

Run-Time Verification

A Very Useful Special Case: Assertions

- Maybe the **simplest instance** of **runtime verification**: **Assertions**.
- Available in standard libraries of many programming languages (C, C++, Java, ...).
- For example, the C standard library manual reads:

```
1 ASSERT(3)      Linux Programmer's Manual      ASSERT(3)
2
3 NAME
4  assert -- abort the program if assertion is false
5
6 SYNOPSIS
7  #include <assert.h>
8
9  void assert(scalar expression);
10
11 DESCRIPTION
12  [...] the macro assert() prints an error message to stan-
13  dard error and terminates the program by calling abort(3) if expression
14  is false (i.e., compares equal to zero).
15
16  The purpose of this macro is to help the programmer find bugs in his
17  program. The message "assertion failed in file foo.c, function
18  do_bar(), line 1287" is of no help at all to a user.
```

- In C code, assert can be **disabled** in **production code** (-D NDEBUG).
- Use `java -ea ...` to **enable assertion checking** (disabled by default).
(cf. <https://docs.oracle.com/javase/8/docs/technotes/guides/language/assert.html>)

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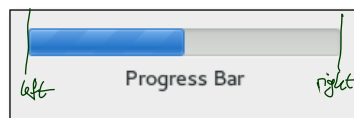
Assertions At Work

- The abstract f -example from **run-time verification**:
(specification: $\{p\} f \{q\}$)

```
1 void f( ... ) {
2     assert(p);
3     ...
4     assert(q);
5 }
```

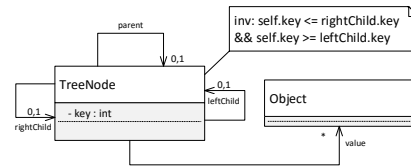
```
1 ASSERT(3)      Linux Programmer's Manual      ASSERT(3)
2
3 NAME
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15
16  The purpose of this macro is to help the programmer find bugs in his
17  program. The message "assertion failed in file foo.c, function
18  do_bar(), line 1287" is of no help at all to a user.
```

- Compute the width of a progress bar:



```
1 int progress_bar_width( int progress , int window_left , int window_right )
2 {
3     assert(window_left <= window_right); /* pre-condition */
4     ...
5     /* treat special cases 0 and 100 */
6     ...
7     assert(0 < progress && progress < 100); // extremal cases already treated
8     ...
9     assert(window_left <= r && r <= window_right); /* post-condition */
10    return r;
11 }
12 }
```

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- Recall the **structure model** with Proto-OCL constraint from Exercise Sheet 4/2012
- Assume, we add a method `set_key()` to class **TreeNode**:

```

1 class TreeNode {
2     private int key;
3     TreeNode parent, leftChild, rightChild;
4
5     public int get_key() { return key; }
6
7     public void set_key( int new_key ) {
8         key = new_key;
9     }
10 }
11

```

- We can **check consistency** with the Proto-OCL constraint at runtime by using assertions:

```

1 public void set_key( int new_key ) {
2     assert( parent == null || parent.get_key() <= new_key );
3     assert( leftChild == null || new_key <= leftChild.get_key() );
4     assert( rightChild == null || new_key <= rightChild.get_key() );
5
6     key = new_key;
7 }

```

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Run-Time Verification: Idea



Software *S*

- Assume, there is a function *f* in software *S* with the following specification:
 - pre-condition:** *p*, **post-condition:** *q*.
- Computation paths of *S* may look like this:

$$\sigma_0 \xrightarrow{\alpha_1} \sigma_1 \xrightarrow{\alpha_2} \sigma_2 \cdots \xrightarrow{\alpha_{n-1}} \sigma_n \xrightarrow{\text{call } f} \sigma_{n+1} \cdots \sigma_m \xrightarrow{f \text{ returns}} \sigma_{m+1} \cdots$$

- Assume there are functions *check_p* and *check_q*, which **check** whether *p* and *q* hold at the current program state, and which **do not modify the program state** (except for program counter).
- Idea:** create software *S'* by
 - (i) extending *S* by implementations of *check_p* and *check_q*,
 - (ii) call *check_p* right after entering *f*,
 - (iii) call *check_q* right before returning from *f*.
- For *S'*, obtain computation paths like:

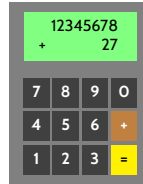
$$\sigma_0 \xrightarrow{\alpha_1} \sigma_1 \xrightarrow{\alpha_2} \sigma_2 \cdots \xrightarrow{\alpha_{n-1}} \sigma_n \xrightarrow{\text{call } f} \sigma_{n+1} \xrightarrow{\text{check}_p} \sigma'_{n+1} \cdots \sigma_m \xrightarrow{\text{check}_q} \sigma'_m \xrightarrow{f \text{ returns}} \sigma_{m+1} \cdots$$

- If *check_p* and *check_q* notify us of violations of *p* or *q*, then we are **notified of *f* violating its specification** when running *S'* (= at run-time).

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Run-Time Verification: Example



```

1  int x, y, sum;
2
3  int main() {
4
5      while (true) {
6          x = read_number();
7          y = read_number();
8
9          sum = add( x, y );
10
11         verify_sum( x, y, sum );
12
13         display();
14     }
15 }

```

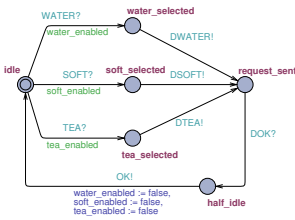
```

1  void verify_sum( int x, int y,
2                  int sum )
3  {
4      if (sum != (x+y)
5          || (x + y > 99999999
6              && !(sum < 0)))
7      {
8          fprintf( stderr,
9                  "verify_sum: error\n" );
10         abort();
11     }
12 }

```

More Complex Run-Time Verification: LSC Observers

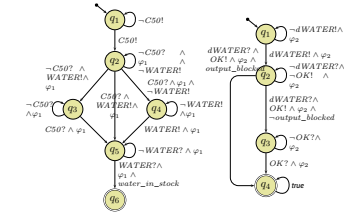
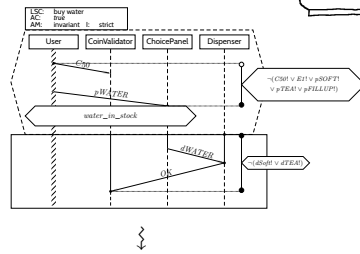
ChoicePanel:



```

st : { idle, wsel, ssel, tsel, reqs, half };
take_event( E : { TAU, WATER, SOFT, TEA, ... } ) {
    bool stable = 1;
    switch (st) {
        case idle:
            switch (E) {
                case WATER:
                    if (water_enabled) { st := wsel; stable := 0; }
                    ;;
                case SOFT:
                    ;;
            }
        case wsel:
            switch (E) {
                case TAU:
                    send_WATER(); st := reqs;
                    key_observer_1_just_sent_DWATER();
                    ;;
            }
    }
}

```



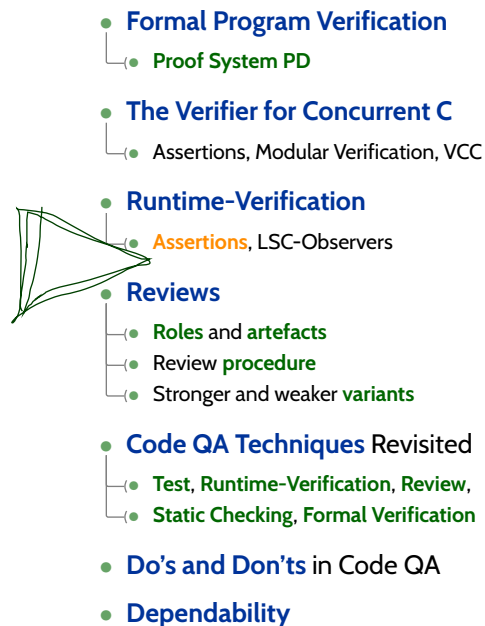
Experience. Assertions for pre/post conditions and intermediate invariants
are an **extremely powerful** tool
with a very attractive gain/effort ratio (low effort, high gain).

- Assertions effectively work as **safe-guard** against
 - ▶ **unexpected use** of functions and
 - ▶ **regression**,
e.g. during later maintenance or efficiency improvement.
 - Assertions can serve as **formal** (support of) **documentation**:
 - `assert(expr);`
means
 - “Dear reader, at this point in the program, I expect condition *expr* to hold.”
- Be good to your readers: **add a comment** that explains the **why**.

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Content



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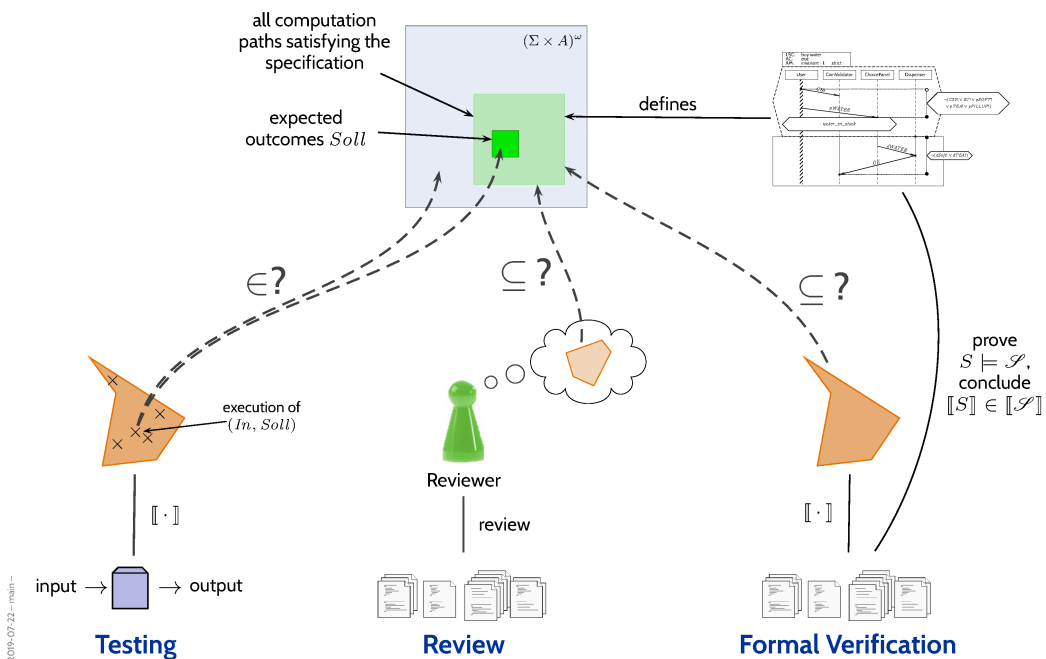
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Review

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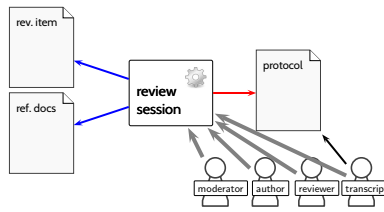
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Recall: Three Basic Directions



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- **Input to Review Session:**

- **Review item:** can be every closed, human-readable part of software (documentation, module, test data, installation manual, etc.)

Social aspect: it is an **artefact** which is examined, not the **human** (who created it).

- **Reference documents:** need to enable an assessment (requirements specification, guidelines (e.g. coding conventions), catalogue of questions ("all variables initialised?"), etc.)

- **Roles:**

Moderator: leads session, responsible for properly conducted procedure.

Author: (representative of the) creator(s) of the artefact under review; is present to listen to the discussions; can answer questions; does not speak up if not asked.

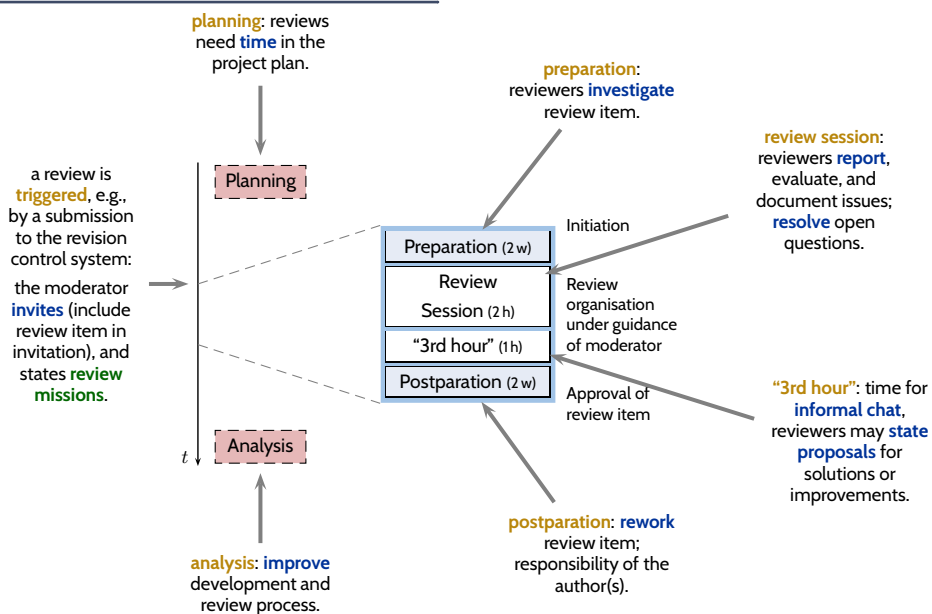
Reviewer(s): person who is able to judge the artefact under review; maybe different reviewers for different aspects (programming, tool usage, etc.), at best experienced in detecting inconsistencies or incompleteness.

Transcript Writer: keeps minutes of review session, can be assumed by author.

- The **review team** consists of everybody but the author(s).

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Review Procedure Over Time

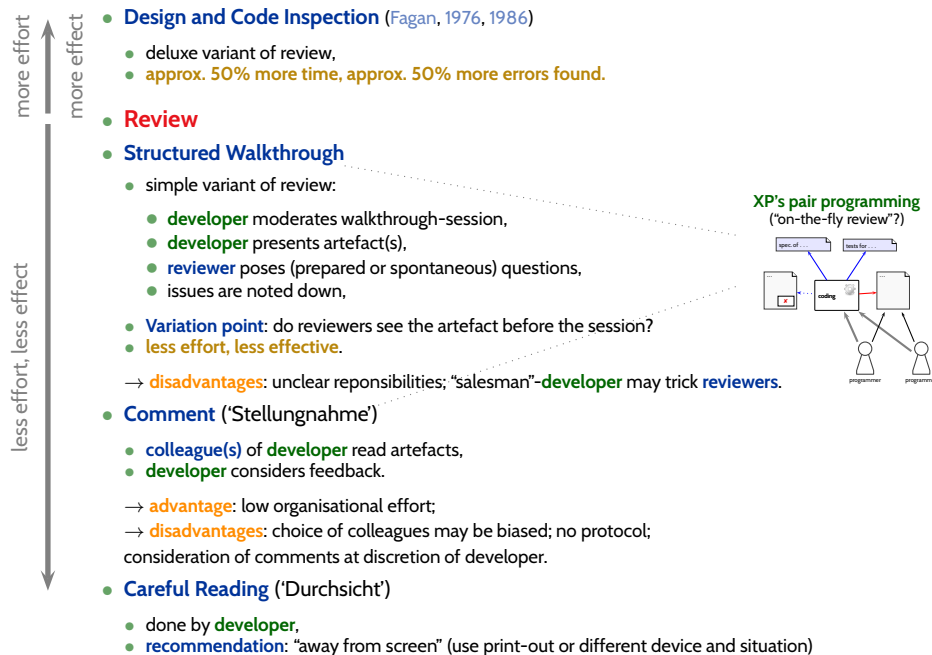


- Reviewers **re-assess** reworked review item (until **approval** is declared).

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- | | |
|--|--|
| <ul style="list-style-type: none"> (i) The moderator organises the review, issues invitations, supervises the review session. (ii) The moderator may terminate the review if conduction is not possible, e.g., due to inputs, preparation, or people missing. (iii) The review session is limited to 2 hours. If needed: organise more sessions. (iv) The review item is under review, not the author(s). Reviewers choose their words accordingly. Authors neither defend themselves nor the review item. (v) Roles are not mixed up, e.g., the moderator does not act as reviewer. (Exception: author may write transcript.) (vi) Style issues (outside fixed conventions) are not discussed. | <ul style="list-style-type: none"> (vii) The review team is not supposed to develop solutions. Issues are not noted down in form of tasks for the author(s). (viii) Each reviewer gets the opportunity to present her/his findings appropriately. (ix) Reviewers need to reach consensus on issues, consensus is noted down. (x) Issues are classified as: <ul style="list-style-type: none"> • critical (review unusable for purpose), • major (usability severely affected), • minor (usability hardly affected), • good (no problem). (xi) The review team declares: <ul style="list-style-type: none"> • accept without changes, • accept with changes, • do not accept. (xii) The protocol is signed by all participants. |
|--|--|

Stronger and Weaker Review Variants



- **Formal Program Verification**
 - **Proof System PD**
- **The Verifier for Concurrent C**
 - Assertions, Modular Verification, VCC
- **Runtime-Verification**
 - **Assertions**, LSC-Observers
- **Reviews**
 - **Roles** and **artefacts**
 - Review **procedure**
 - Stronger and weaker **variants**
- **Code QA Techniques Revisited**
 - **Test, Runtime-Verification, Review,**
 - **Static Checking, Formal Verification**
- **Do's and Don'ts** in Code QA
- **Dependability**

Code Quality Assurance Techniques Revisited

Techniques Revisited

	auto- matic	prove "can run"	toolchain considered	exhaus- tive	prove correct	partial results	entry cost
Test	(✓)	✓	✓	✗	✗	✓	✓
Runtime- Verification							
Review							
Static Checking							
Verification							

Strengths:

- can be **fully automatic** (yet not easy for GUI programs);
- negative test **proves "program not completely broken"**, "can run" (or positive scenarios);
- **final product is examined**, thus toolchain and platform considered;
- one can stop at any time and take **partial results**;
- few, simple test cases are usually **easy to obtain**;
- provides **reproducible counter-examples** (good starting point for repair).

Weaknesses:

- (in most cases) **vastly incomplete**, thus no proofs of correctness;
- creating test cases for complex functions (or complex conditions) **can be difficult**;
- **maintenance** of many, complex test cases be **challenging**;
- executing many tests may need **substantial time** (but: can sometimes be run in parallel);

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Techniques Revisited

	auto- matic	prove "can run"	toolchain considered	exhaus- tive	prove correct	partial results	entry cost
Test	(✓)	✓	✓	✗	✗	✓	✓
Runtime- Verification	✓	(✓)	✓	(✗)	✗	✓	(✓)
Review							
Static Checking							
Verification							

Strengths:

- **fully automatic** (once observers are in place);
- **provides counter-example**;
- (nearly) **final product is examined**, thus toolchain and platform considered;
- one can stop at any time and take **partial results**;
- **assert-statements have a very good effort/effect ratio**.

Weaknesses:

- counter-examples **not necessarily reproducible**;
- may negatively affect **performance**;
- code is changed, program may only run **because of** the observers;
- completeness depends on usage, may also be **vastly incomplete**, so no correctness proofs;
- constructing observers for complex properties may be **difficult**, one needs to learn how to construct observers.

– 17 – 2019-07-22 – Seminarpage –

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Techniques Revisited

	auto- matic	prove "can run"	toolchain considered	exhaus- tive	prove correct	partial results	entry cost
Test	(✓)	✓	✓	✗	✗	✓	✓
Runtime- Verification	✓	(✓)	✓	(✗)	✗	✓	(✓)
Review	✗	✗	✗	(✓)	(✓)	✓	(✓)
Static Checking							
Verification							

Strengths:

- human readers can **understand the code**, may spot point errors;
- reported to be **highly effective**;
- one can stop at any time and take **partial results**;
- intermediate **entry costs**;
good effort/effect ratio achievable.

Weaknesses:

- no tool support**;
- no results on actual execution, **toolchain not reviewed**;
- human readers may **overlook** errors; usually not aiming at proofs.
- does (in general) **not provide counter-examples**,
developers may deny existence of error.

– 17 – 2019-07-22 – Summary –

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Techniques Revisited

	auto- matic	prove "can run"	toolchain considered	exhaus- tive	prove correct	partial results	entry cost
Test	(✓)	✓	✓	✗	✗	✓	✓
Runtime- Verification	✓	(✓)	✓	(✗)	✗	✓	(✓)
Review	✗	✗	✗	(✓)	(✓)	✓	(✓)
Static Checking	✓	(✗)	✗	✓	(✓)	✓	(✗)
Verification							

Strengths:

- there are (commercial), **fully automatic** tools (lint, Coverity, Polyspace, etc.);
- some tools are **complete** (relative to assumptions on language semantics, platform, etc.);
- can be **faster than testing**;
- one can stop at any time and take **partial results**.

Weaknesses:

- no results on actual execution, **toolchain not reviewed**;
- can be very **resource consuming** (if few false positives wanted),
e.g., code may need to be "designed for static analysis".
- many false positives can be very **annoying to developers** (if fast checks wanted);
- distinguish **false from true positives** can be challenging;
- configuring the tools** (to limit false positives) can be challenging.

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Techniques Revisited

	auto- matic	prove "can run"	toolchain considered	exhaus- tive	prove correct	partial results	entry cost
Test	(✓)	✓	✓	✗	✗	✓	✓
Runtime- Verification	✓	(✓)	✓	(✗)	✗	✓	(✓)
Review	✗	✗	✗	(✓)	(✓)	✓	(✓)
Static Checking	✓	(✗)	✗	✓	(✓)	✓	(✗)
Verification	(✓)	✗	✗	✓	✓	(✗)	✗

Strengths:

- some **tool support** available (few commercial tools);
- **complete** (relative to assumptions on language semantics, platform, etc.);
- thus can provide **correctness proofs**;
- can prove correctness for **multiple language semantics and platforms** at a time;
- can be **more efficient than other techniques**.

Weaknesses:

- no results on actual execution, **toolchain not reviewed**;
- not many **intermediate results**: "half of a proof" may not allow any useful conclusions;
- **entry cost high**: significant training is useful to know how to deal with tool limitations;
- proving things is challenging: failing to find a proof does not allow any useful conclusion;
- **false negatives** (broken program "proved" correct) hard to detect.

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Techniques Revisited

	auto- matic	prove "can run"	toolchain considered	exhaus- tive	prove correct	partial results	entry cost
Test ✓	(✓)	✓	✓	✗	✗	✓	✓
Runtime- Verification ✓	✓	(✓)	✓	(✗)	✗	✓	(✓)
Review ✓	✗	✗	✗	(✓)	(✓)	✓	(✓)
Static Checking (x)	✓	(✗)	✗	✓	(✓)	✓	(✗)
Verification ✓	(✓)	✗	✗	✓	✓	(✗)	✗

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Some Final, General Guidelines

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Do's and Don'ts in Code Quality Assurance



Avoid using special **examination versions** for examination.
(Test-harness, stubs, etc. **may have errors** which may cause false positives and (!) negatives.)



Avoid to **stop examination** when the first error is detected.
Clear: Examination should be aborted if the examined program is not executable at all.

Do not modify the artefact under examination **during** examination.

- otherwise, it is **unclear what exactly** has been examined ("moving target"),
(examination results need to be uniquely traceable to one artefact version.)
- fundamental flaws are sometimes **easier to detect**
with a **complete picture** of unsuccessful/successful tests,
- **changes are particularly error-prone**, should not happen "en passant" in examination,
- fixing flaws during examination may cause them to **go uncounted** in the **statistics**
(which we need for all kinds of estimation),
- roles **developer** and **examiner** are different anyway:
an **examiner** fixing flaws would **violate the role assignment**.



Do not switch (fine grained) between **examination** and **debugging**.

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Dependability Case

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Proposal: Dependability Cases (Jackson, 2009)

- A **dependable** system is one you can **depend** on — that is, you can place your trust in it.

“Developers [should] **express the critical properties**
and **make an explicit argument** that the system satisfies them.”

Proposed Approach:

- Identify the **critical requirements**,
and determine what **level of confidence** is needed.
(Most systems do also have **non-critical** requirements.)
- Construct a **dependability case**, i.e.
an **argument**, that the software, in concert with other components,
establishes the **critical properties**.
- The **dependability case** should be
 - **auditable**: can (easily) be evaluated by third-party certifier.
 - **complete**: no holes in the argument;
any assumptions that are not justified should be noted
(e.g. assumptions on compiler, on protocol obeyed by users, etc.)
 - **sound**: e.g. should not claim full correctness [...] based on nonexhaustive testing;
should not make unwarranted assumptions on independence of component failures;
etc.

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- **Runtime Verification**
 - (as the name suggests) checks properties at **program run-time**,
 - generous use of **assert** can be a valuable safe-guard against
 - **regressions**, usage **outside specification**, etc.
 - and serve as **formal documentation** of (intermediate) assumptions.
 - Very attractive **effort / effect** ratio.
- **Review** (structured examination of artefacts by humans)
 - (mild variant) advocated in the XP approach,
 - **not uncommon**:
 - lead programmer reviews **all commits** from team members,
 - literature reports good effort/effect ratio achievable.
- All approaches to **code quality assurance** have their
 - **advantages** and **drawbacks**.
 - Which to use? It depends!
- Overall: Consider **Dependability Cases**
 - an (auditable, complete, sound) argument,
 - that a software has the **critical properties**.

Looking Back: 17 Lectures on Software Engineering

Contents of the Course

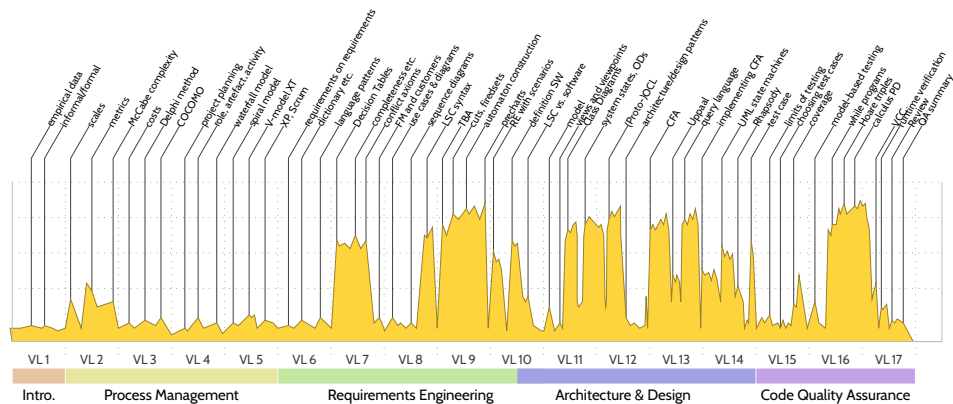
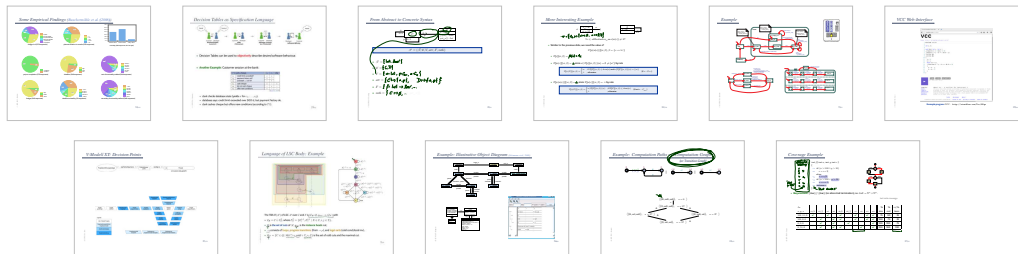


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What Did We Do?



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Topic Area: Project Management

Topic Area: Requirements Engineering

Topic Area: Architecture & Design

Topic Area: Software Quality Assurance

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Topic Area: Project Management

- **measure**, know what you measure (scales, pseudo-metrics)
- estimate, measure, improve estimation – it's about **experience**
- describe processes in terms of **artefact**, **activity**, **role**, etc. – and **risk**

Topic Area: Requirements Engineering

- requirements characterise **acceptable** and **unacceptable** softwares (there may be a gray zone)
- **formal requirements**: unambiguous, exact analysis methods
- requirements engineers see **the absence of meaning**

Topic Area: Architecture & Design

- Model: "Nobody builds a house without a **plan**." (L. Lamport)
- software has **structural** and **behavioural** aspects
- there are **methods and tools** to analyse software models (know how to interpret analysis outcomes)

Topic Area: Software Quality Assurance

- testing is **almost always incomplete**; testing is **necessary** (know how to interpret the outcomes: true/false positive/negative)
- there are methods and tools to **prove correctness** code (correctness is relative: correct wrt. specification (and assumptions))

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That's Today's Software Engineering — More or Less...

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Questions?

Advertisements

- **Further studies:**

- **Real-Time Systems** (not in 2019/20)
(specification and verification of real-time systems)
- **Software Design, Modelling, and Analysis in UML** (not in 2019/20)
(a formal, in-depth view on structural and behavioural modelling)
- **Cyber-Physical Systems I - Discrete Models**
(more on variants of CFA and queries (LTL, CTL, CTL*))
- **Cyber-Physical Systems - Hybrid Models**
(Modelling and analysis of cyber-physical systems with hybrid automata)
- **Program Verification**
(the theory behind tools like VCC)
- **Formal Methods for Java**
(JML and "VCC for Java")
- **Decision Procedures**
(the basis for program verification)

→ <https://swt.informatik.uni-freiburg.de/teaching>

- **Individual Projects**

(BSc/MSc project, Lab Project, BSc/MSc thesis)

- **formal modelling** of industrial case studies
- **improving analysis techniques**
- **own topics**

→ **contact us** (3–6 months before planned start).

- Want to be a **tutor**, e.g. Software Engineering 2020,
→ **contact us** (around early September / early March).
- Want to be a **scientific student assistant**?
→ **contact us**.



Thanks For Your Participation...

References

References

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