Software Design, Modelling and Analysis in UML

Lecture 14: Hierarchical State Machines I

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Notions of Steps: The Step

Note: we call one evolution $(\sigma,\varepsilon) \xrightarrow[u]{(cons,Snd)} (\sigma',\varepsilon')$ a step.

Thus in our setting, a step directly corresponds to

(We have to extend the concept of "single transition" for hierarchical state machines.) That is: We're going for an interleaving semantics without true parallelism. one object (namely u) takes a single transition between regular states.

Example: $E[k > 0] / s_1$

What is a run-to-completion step...?

Intuition: a maximal sequence of steps, where the first step is a dispatch step and all later steps are commence steps.

Note: one step corresponds to one transition in the state machine.

A run-to-completion step is in general not syntacically definable — one transition may be taken multiple times during an RTC-step.

Notions of Steps: The Run-to-Completion Step

Contents & Goals

RTC-Rules: Discard, Dispatch, Commence.

This Lecture:

- Educational Objectives: Capabilities for following tasks/questions.
 What does this State Machine mean' What happens if I nject this event?
 Can you please model the following behaviour.
 What is initial state.
- What does this hierarchical State Machine mean? What may happen if I inject this event?
- What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state,...

- Step, RTC, Divergence
 Putting It All Together
 Rhapsody Demo

Hierarchical State Machines Syntax

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Step and Run-to-completion Step

Proposal: Let $(\sigma_0, \varepsilon_0) \xrightarrow{(cons_0, Snd_0)} u_0$ $\xrightarrow[u_{n-1}]{(cons_{n-1},Snd_{n-1})} (\sigma_n, \varepsilon_n), \quad n > 0,$ per counst extend the septement with such soft by the paperties below

- be a finite (!), non-empty, maximal, consecutive sequence such that $f(x_i, f_{i+1})$ are is the object u is alive in σ_0 , relative.
- $u_0 = u$ and $(cons_0, Snd_0)$ indicates dispatching to u, i.e. $cons = \{(u, \vec{v} \mapsto \vec{d})\}$,
- ullet there are no receptions by u in between, i.e.
- $u_{n-1}=u$ and u is stable only in σ_0 and σ_n , i.e.

 $cons_i \cap \{u\} \times Evs(\mathcal{E}, \mathcal{D}) = \emptyset, i > 1,$

 $\sigma_0(u)(stable) = \sigma_n(u)(stable) = 1 \text{ and } \sigma_i(u)(stable) = 0 \text{ for } 0 < i < n,$

Let $0=k_1< k_2<\cdots< k_N=n$ be the maximal sequence of indices such that $u_{k_i}=u$ for $1\le i\le N.$ Then we call the sequence

 $(\sigma_0(u) =) \quad \sigma_{k_1}(u), \sigma_{k_2}(u) \dots, \sigma_{k_N}(u) \quad (= \sigma_{n-1}(u))$

a (!) run-to-completion computation of u (from (local) configuration $\sigma_0(u)$). $_{6:m}$

Notions of Steps: The Run-to-Completion Step Cont'd

Divergence

We say, object u can diverge on reception cons from (local) configuration $a_0(u)$ if and only if there is an infinite, consecutive sequence

$$(\sigma_0, \varepsilon_0) \xrightarrow{(cons_0, Snd_0)} (\sigma_1, \varepsilon_1) \xrightarrow{(cons_1, Snd_1)} \cdots$$

such that u doesn't become stable again.

Note: disappearance of object not considered in the definitions. By the current definitions, it's <u>north and</u> divergence, we an RTC-step.

The Missing Piece: Initial States

Semantics of UML Model — So Far

The semantics of the UML model

 $\mathcal{M} = (\mathcal{CD}, \mathcal{SM}, \mathcal{OD})$

Recall: a labelled transition system is (S, \rightarrow, S_0) . We have

- S: system configurations (σ, ε)
- \rightarrow : labelled transition relation $(\sigma, \varepsilon) \xrightarrow[u]{(cons, Snd)} (\sigma', \varepsilon')$.

Wanted: initial states S_0 .

Require a (finite) set of **object diagrams** \mathcal{OD} as part of a UML model

(C2, LM, O2).

And set

 $S_0 = \{(\sigma,\varepsilon) \mid \sigma \in G^{-1}(\mathcal{OD}), \mathcal{OD} \in \mathscr{OD}, \varepsilon \text{ em pty}\}$

is the transition system $(S, -, S_0)$ constructed on the previous slide. From $S_0 \subseteq S_0$ from $S_0 \subseteq S_0$ for $S_0 \subseteq S_0$ f

• $\mathscr{O}\mathscr{D}$ is a set of object diagrams over $\mathscr{C}\mathscr{D}$. there is a 1-to-1 relation between classes and state machines, \circ some classes in $\mathscr C\mathscr D$ are stereotyped as 'signal' (standard), some signals and attributes are stereotyped as 'external' (non-standard),

Other Approach: (used by Rhapsody tool) multiplicity of classes We can read that as an abbreviation for an object diagram.

10,00

Run-to-Completion Step: Discussion.

What people may dislike on our definition of RTC-step is that it takes a global and non-compositional view. That is:

In the projection onto a single object we still see the effect of interaction with other objects.

- Adding classes (or even objects) may change the divergence behaviour of existing ones.
- Compositional would be: the behaviour of a set of objects is determined by the behaviour of each object "in isolation". Our semantics and notion of RTC-step doesn't have this (often desired) property.

Can we give (syntactical) criteria such that any global run-to-completion step is an interleaving of local ones?

Maybe: Strict interfaces.

(A): Refer to private features only via "self".

(Recall that other objects of the same class can modify private attributes.)

(B): Let objects only communicate by events, i.e.

don't let them modify each other's local state via links at all.

(Proof left as exercise...)

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Putting It All Together

OCL Constraints and Behaviour x:4/2-1 (2)

*We call M consistent if, for each OLL constraint expr \(\int \ln \lambda \lambda \rangle \ra

If the developer makes a mistake, then \mathcal{M}' is inconsistent.

Not common: if SM is given, then constraints are also considered when choosing transitions in the RTC-algorithm. In other words, even in presence of mistakes, the SM never move to inconsistent configurations.

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UML State-Machines: What do we have to cover?

The Full Story

UML distinguishes the following kinds of states:

AND	OR	composite state	final state	simple state		
20 E 0 20 E 0	2 2		$E_{\kappa/act_{E_{\kappa}}}$	do/acryo ext/acryos E1/acr _{E1}	entry/act and/	example
terminate submachine state	entry point exit point	junction, choice	fork/join	(shallow) history deep history	pseudo-state initial	
» ×	⊗ O	X	\ \	3 E	•	example

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Representing All Kinds of States

 $\circ \ \, \text{Until now:}$ $(S,s_0,\to),\quad s_0\in S,\to \ \subseteq S\times (\mathscr E\cup\{\bot\})\times Expr_\mathscr S\times Act\mathscr S\times S$

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Hierarchical State Machines

Representing All Kinds of States

• From now on: (hierarchical) state machines $(S,s_0,\rightarrow),\quad s_0\in S,\rightarrow \ \subseteq S\times (\mathscr{E}\cup\{\cdot\})\times \mathit{Expr}_{\mathscr{S}}\times \mathit{Ad}_{\mathscr{S}}\times S$

 $(S,kind,region,\rightarrow,\psi,annot)$

18,66

From UML to Hierarchical State Machines: By Example

Well-Formedness: Regions (follows from diagram)

kind $region \subseteq 2^S, S_i \subseteq S$ $child \subseteq S$

composite state pseudo-state

final state

st fin st init, ...

 $S_1 \cup \cdots \cup S_r$

implicit top state top st



... translates to $(S, kind, region, \rightarrow, \psi, annot) =$



20,00

 The region function induces a child function Final and pseudo states don't comprise regions.

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• States $s \in S$ with kind(s) = st may comprise regions. • No region: simple state. Each state (except for top) lies in exactly one region,

One region: OR-state.
 Two or more regions: AND-state.

Representing All Kinds of States

Until now:

 $(S,s_0,\rightarrow),\quad s_0\in S,\rightarrow \ \subseteq S\times (\mathcal{E}\cup \{\text{--}\})\times Expr_{\mathscr{S}}\times Act\mathscr{S}\times S$

From now on: (hierarchical) state machines

 $(s_0 ext{ is then redundant} - ext{replaced by proper state} ext{(!) of kind '} init'.)$

From UML to Hierarchical State Machines: By Example

 $(S,kind,region,\rightarrow,\psi,annot)$

example •

 $\in S$ kind

region

 $(S,kind,region,\rightarrow,\psi,annot)$

S ⊇ {op} is a finite set of states
 • Kind; S → {q, mir, fin, shig, dhist, tork, join, junc, choi, ent, exi, term} is a function which labels states with their kind,
 • region: S → 2^{nt} is a function which characteries the regions of a state,
 • region: (new)

• — is a set of transitions. (changed) • $\psi:(-) \to 2^g \times 2^g$ is an incidence function, and (new) • amout $:(-) \to (\mathscr{E} \cup \{\bot\}) \times Epin \mathscr{S} \times Ad\mathscr{S}$ provides an amoutation for each transition. (new)

composite state final state simple state

OR.

Well-Formedness: Initial State (requirement on diagram)

- Each non-empty region has a reasonable initial state and at least one transition from there, i.e.
- for each $s \in S$ with $region(s) = \{S_1,\dots,S_n\},\ n \geq 1$, for each $1 \leq i \leq n$, there exists exactly one initial pseudo-state $(s_1^i,int) \in S_i$ and at least one transition $t \in \to$ with s_1^i as source,

- and such transition's target s_2^i is in S_i , and (for simplicity!) $kind(s_2^i) = st$, and $annot(t) = (_, true, act)$.
- No ingoing transitions to initial states.
 No outgoing transitions from final states.



Plan



- Initial pseudostate, final state.
- Composite states.
- Entry/do/exit actions, internal transitions.
 History and other pseudostates, the rest.

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Initial Pseudostates and Final States

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Towards Final States: Completion of States

Towards Final States: Completion of States

Initial Pseudostate

/act₁

\$1 /act2 |



- Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".
- ullet Dispatching (here: E) can then alternatively be viewed as

Special case: the region of top.

 then go to a state which is destination of an initiation transition,
 execute the action of the chosen initiation transitions between exit and entry actions. when entering a region without a specific destination state,

If class C has a state-machine, then "create-C transformer" is the
construction of the "constructor" of C (here not introduced explicitly) and
a transformer corresponding to one initiation transition of the top region.

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Initial Pseudostate



- then go to a state which is destination of an initiation transition, when entering a region without a specific destination state,

execute the action of the chosen initiation transitions between exit and entry actions.

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Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".

Dispatching (here: E) can then alternatively be viewed as
 (i) fetch event (here: E) from the ether,

Towards Final States: Completion of States

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- Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".
- \bullet Dispatching (here: E) can then alternatively be viewed as
- (i) fetch event (here: E) from the ether,
- (ii) take an enabled transition (here: to s_2),

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Towards Final States: Completion of States

2	•
	E/act_1
20	
	/act2
0.3	ė

- Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".
- ullet Dispatching (here: E) can then alternatively be viewed as
- fetch event (here: E) from the ether,
- (ii) take an enabled transition (here: to s_2),
- (ii) remove event from the ether, (iv) after having finished entry and do action of current state (here: s_2) the state is then called completed —,
- (v) raise a completion event with strict priority over events from ether!

26/00

(v) raise a completion event — with strict priority over events from ether!
 (v) if there is a transition enabled which is sensitive for the completion event, then take it (here: (s₂, s₃)).
 e otherwise become stable.

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(iv) after having finished entry and do action of current state (here: s_2) — the state is then called completed —,

(iii) remove event from the ether, (ii) take an enabled transition (here: to s_2), (i) fetch event (here: E) from the ether,

Towards Final States: Completion of States

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ullet Dispatching (here: E) can then alternatively be viewed as

(ii) take an enabled transition (here: to s_2),

fetch event (here: E) from the ether,

 s_1 E/act_1 s_2 act_2 s_3

- Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".
- ullet Dispatching (here: E) can then alternatively be viewed as
- (i) fetch event (here: E) from the ether,
- (ii) take an enabled transition (here: to s_2), (iii) remove event from the ether,

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(ii) remove event from the ether,
 (iv) after having finished entry and do action of current state (here: 2) — the state is then called completed —,

Final States

Towards Final States: Completion of States

Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".

 \bullet Dispatching (here: E) can then alternatively be viewed as

s annot •

- a step of object u moves u into a final state (s, fin), and
 all stilling regions are in a final state,
 then (connegntionally) a completion event for the current composite state s is raised.

Final States

- a step of object u moves u into a final state (s, fin), and
- \circ all sibling regions are in a final state, then (conceptionally) a completion event for the current composite state s is raised.
- \bullet If there is a transition of a parent state (i.e., inverse of child) of s enabled which is sensitive for the completion event,
- then take that transition,
- ullet otherwise kill u
- \rightsquigarrow adjust (2.) and (3.) in the semantics accordingly

27,66

Final States

Final States

- If there is a transition of a parent state (i.e., inverse of child) of s enabled which is sensitive for the completion event,
- $\begin{tabular}{ll} \bullet & then take that transition, \\ \bullet & otherwise kill u \\ \end{tabular}$
- \rightsquigarrow adjust (2.) and (3.) in the semantics accordingly

- a step of object u moves u into a final state (s, fin), and
 all sibling regions are in a final state,
- then (conceptionally) a completion event for the current composite state \boldsymbol{s} is raised.

- One consequence: u never survives reaching a state (s, fin) with $s \in \mathit{child}(top)$.

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Now: in Core State Machines, there is no parent state.
 Later: in Hierarchical ones, there may be one.

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• One consequence: u never survives reaching a state (s,fin) with $s \in \mathit{child}(\mathit{top}).$

 \rightsquigarrow adjust (2.) and (3.) in the semantics accordingly

ullet otherwise kill uthen take that transition,

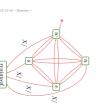
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Composite States

- In a sense, composite states are about abbreviation, structuring, and avoiding redundancy.
- Idea: in Tron, for the Player's Statemachine, instead of

Composite States
(formalisation follows [?])

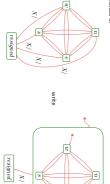


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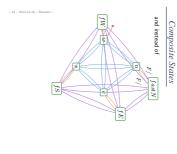
Composite States

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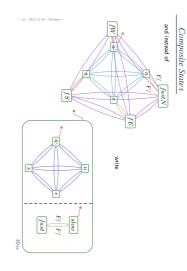


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Transitions

* what are legal
transitions?

when is a transition
enabled?

* what effects do transitions have?

\$ F

what may happen on E?
what may happen on E, F?
can E, G kill the object?

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States

what are legal state
configurations?
what is the type of the
implicit st attribute?

7/ 83 F/

\$ 5 E / G /

81 E/

• Naming convention: $\psi(t) = (source(t), target(t))$.

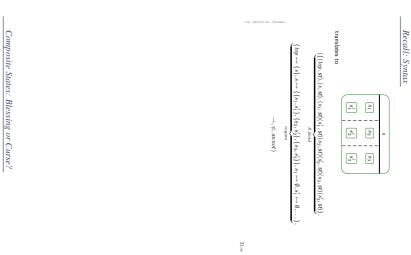
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 $(S,kind,region,\underbrace{\{t_1\},\{t_1\mapsto (\{s_2,s_3\},\{s_5,s_6\})\},\{t_1\mapsto (tr,gd,act)\})}_{\psi})$

Syntax: Fork/Join

 For brevity, we always consider transitions with (possibly) multiple sources and targets, i.e.

For instance,



State Configuration

- \bullet The type of st is from now on a set of states, i.e. $st:2^S$

- * A set $S_1\subseteq S$ is called (legal) state configurations if and only if $*top\in S_1$, and $*with each state <math>*s\in S_1$ that has a non-empty region $\emptyset \neq R \in region(s)$, exactly one (non pseudo-state) child of s is in S_1 , i.e.

 $|\{s\in R\mid \mathit{kind}(s)\in \{\mathit{st},\mathit{fin}\}\}\cap S_1|=1.$

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State Configuration

A Partial Order on States

The substate- (or child-) relation induces a partial order on states: $top \leq s, \text{ for all } s \in S, \\ * s \leq s', \text{ for all } s' \in child(s),$

 $\label{eq:state_state} \begin{tabular}{ll} \bullet & transitive, reflexive, antisymmetric, \\ \bullet & s' \leq s \mbox{ and } s'' \leq s \mbox{ implies } s' \leq s'' \mbox{ or } s'' \leq s'. \end{tabular}$

- The type of st is from now on a set of states, i.e. $st:2^S$

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 $|\{s\in R\mid kind(s)\in\{\mathit{st},\mathit{fin}\}\}\cap S_1|=1.$

Examples:

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Least Common Ancestor and Ting

A Partial Order on States

The substate- (or child-) relation induces a partial order on states: • $top \leq s$, for all $s \in S$,

 transitive, reflexive, antisymmetric, • $s \le s'$, for all $s' \in child(s)$,

• $s' \le s$ and $s'' \le s$ implies $s' \le s''$ or $s'' \le s'$.

 s_2

S₁ S₁

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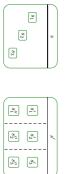
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- * The least common ancestor is the function $laa:2^S\to S$ such that * The states in S_1 are (transitive) children of $laa(S_1)$, i.e.
- $lca(S_1) \le s$, for all $s \in S_1 \subseteq S$,
- $\bullet \ lca(S_1) \ \text{is minimal, i.e.} \ if \ \hat{s} \leq s \ \text{for all} \ s \in S_1, \ \text{then} \ \hat{s} \leq lca(S_1)$ $\bullet \ \ \text{Note:} \ lca(S_1) \ \text{exists for all} \ S_1 \subseteq S \ \ \text{(last candidate:} \ top).$

36,00

Least Common Ancestor and Ting

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- $lca(S_1) \le s$, for all $s \in S_1 \subseteq S$,
- $lca(S_1)$ is minimal, i.e. if $\hat{s} \leq s$ for all $s \in S_1$, then $\hat{s} \leq lca(S_1)$
- Note: $lca(S_1)$ exists for all $S_1 \subseteq S$ (last candidate: top).



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Least Common Ancestor and Ting

- Two states $s_1,s_2 \in S$ are called **orthogonal**, denoted $s_1 \perp s_2$, if and only if they are unordered, i.e. $s_1 \not \leq s_2$ and $s_2 \not \leq s_1$, and they live in different regions of an AND-state, i.e.

 $\exists s, region(s) = \{S_1, \dots, S_n\}, 1 \leq i \neq j \leq n : s_1 \in child(S_i) \land s_2 \in child(S_j),$

37,66

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Least Common Ancestor and Ting

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81 82 s_3

8'' 8'' 8'' 8'' 8'' 8''

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Legal Transitions

• A set of states $S_1\subseteq S$ is called **consistent**, denoted by $\downarrow S_1$, if and only if for each $s,s'\in S_1$, • $s\leq s'$.

• $s' \leq s$, or • $s \perp s'$.

81 82 s_3

S₁' S₂' S₃' S₃'

38/00

Least Common Ancestor and Ting

A hiearchical state-machine $(S,kind,region,\rightarrow,\psi,annot)$ is called **well-formed** if and only if for all transitions $t\in\rightarrow$,

source and destination are consistent, i.e. ↓ source(t) and ↓ larget(t),
 source (and destination) states are pairwise unordered, i.e.
 forall s, s' ∈ source(t) (∈ larget(t)), s ⊥ s',

the top state is neither source nor destination, i.e.
 top \(\preceq\) source(t) \(\preceq\) source(t).
 Recall: final states are not sources of transitions.

39,00

Least Common Ancestor and Ting

- A set of states $S_1\subseteq S$ is called consistent, denoted by $\bot S_1$, if and only if for each $s,s'\in S_1$, $s\leq s'$, $s'\leq s'$, $s'\leq s$, or $s'\leq s$, or $s'\perp s'$.

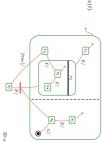
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Legal Transitions

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the top state is neither source nor destination, i.e.
 top \(\xi_\) ownrea(t) \(\text{Source}(t)\).
 Recall: final states are not sources of transitions.



The Depth of States

- $\begin{aligned} &\bullet \; depth(top) = 0, \\ &\bullet \; depth(s') = depth(s) + 1, \text{ for all } s' \in child(s) \end{aligned}$

The Depth of States

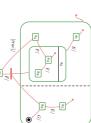
Enabledness in Hierarchical State-Machines

 \bullet The scope ("set of possibly affected states") of a transition t is the least common region of

 $source(t) \cup target(t)$.

- $$\begin{split} & \cdot \ depth(top) = 0, \\ & \cdot \ depth(s') = depth(s) + 1, \ \text{for all} \ s' \in child(s) \end{split}$$

Example:



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Enabledness in Hierarchical State-Machines

Enabledness in Hierarchical State-Machines

 \bullet The scope ("set of possibly affected states") of a transition t is the least common region of

 \bullet Two transitions t_1,t_2 are called consistent if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal). $source(t) \cup target(t)$.

- \bullet The scope ("set of possibly affected states") of a transition t is the least common region of
- $source(t) \cup target(t)$.
- Two transitions t₁, t₂ are called consistent if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal).
 The priority of transition t is the depth of its innermost source state, i.e.

 $prio(t) := \max\{depth(s) \mid s \in source(t)\}$

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Enabledness in Hierarchical State-Machines

- \bullet The scope ("set of possibly affected states") of a transition t is the least common region of
- $source(t) \cup target(t)$.
- \circ Two transitions t_1,t_2 are called consistent if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal).
- ullet The priority of transition t is the depth of its innermost source state, i.e.
- $prio(t) := \max\{depth(s) \mid s \in source(t)\}$
- A set of transitions $T \subseteq \to$ is enabled in an object u if and only if T is consistent,

- T is maximal wrt. priority, all transitions in T share the same trigger, all guards are satisfied by $\sigma(u)$, and for all $t \in T$, the source states are active, i.e.

 $source(t) \subseteq \sigma(u)(st) \ (\subseteq S).$

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Transitions in Hierarchical State-Machines

- Let T be a set of transitions enabled in u.
- Then $(\sigma, \varepsilon) \xrightarrow{(cons, Snd)} (\sigma', \varepsilon')$ if
- σ'(u)(st) consists of the target states of t,
- i.e. for simple states the simple states themselves, for composite states the initial states, $% \left(\frac{1}{2}\right) =\frac{1}{2}\left(\frac{1}{2}\right) ^{2}$
- * $\sigma', \varepsilon', \ cons.$ and Sud are the effect of firing each transition $t \in T$ one by one, in any order, i.e. for each $t \in T$, * the exit transformer of all affected states, highest depth first, * the transformer of i, * the entry transformer of all affected states, lowest depth first.

- \rightarrow adjust (2.), (3.), (5.) accordingly.

42,66

- In general, with each state $s \in S$ there is associated
- an entry, a do, and an exit action (default: skip)

 \leadsto adjust (2.), (3.) accordingly.

 t_{act}

Taking the transition above then amounts to applying

* a possibly empty set of trigger/siction pairs called internal transitions. Each of the following transitions (default: empty). $E_1, \dots, E_n \in \mathcal{S}$, 'entry', 'do', 'exit' are reserved names!

 $\begin{vmatrix} s_1 \\ entry/act_1^{entry} \\ do/act_1^{ob} \\ exit/act_{E_1}^{evit} \end{vmatrix}$

Entry/Do/Exit Actions

 $tr[gd]/act = \begin{cases} s_2 \\ entry/act_2^{ant} \\ do/act_2^{do} \\ exit/act_2^{aoit} \end{cases}$

ullet Recall: each action's supposed to have a transformer. Here: $t_{act_1^{out}}, t_{act_1^{out}}, \ldots$

 $t_{act_{a_2}^{sep}} \circ t_{act} \circ t_{act_{a_1}^{set}}$

instead of only

44,00

Entry/Do/Exit Actions, Internal Transitions

Entry/Do/Exit Actions

• In general, with each state $s \in S$ there is associated

 s_1 $entry/act_1^{entry}$ do/act_1^{do} $exit/act_2^{exit}$ E_1/act_{Σ_1}

tr[gd]/act

an entry, a do, and an exit action (default: skip)

* a posibly empty set of trigger/action pairs called E_n/act_E , trigger/action pairs called internal transitions (default: empty). $E_1,\dots,E_n\in\mathcal{E}$, 'entry', 'do', 'exit' are reserved names!

43,66

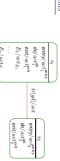
44/66

Internal Transitions



- For internal transitions, taking the one for E_1 , for instance, still amounts to taking only $t_{act_{E_1}}$.
 Intuition: The state is neither left nor entered, so: no exit, no entry,
- → adjust (2.) accordingly.
- Note: internal transitions also start a run-to-completion step.

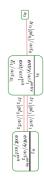
Internal Transitions



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- \leadsto adjust (2.) accordingly.
- Note: internal transitions also start a run-to-completion step.
- Note: the standard seems not to clarify whether internal transitions have priority over regular transitions with the same trigger at the same state. Some code generators assume that internal transitions have priority!

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Alternative View: Entry/Exit/Internal as Abbreviations



... as abbrevation for ...

 s_1

 s_0

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46,66

Alternative View: Entry/Exit/Internal as Abbreviations

Do Actions

 s_1 $entry/act_1^{entry}$ do/act_1^{ob} $exit/act_2^{exit}$ E_1/act_{E_1}

tr[gd]/act entry/act entry



... as abbrevation for ...

 s_0

- That is: Entry/Internal/Exit don't add expressive power to Core State Machines. If internal actions should have priority, s_1 can be embedded into an OR-state (see later).
- Abbreviation may avoid confusion in context of hierarchical states (see later).

Intuition: after entering a state, start its do-action.
If the do-action terminates,

 E_n/act_{E_n}

then the state is considered completed,

if the state is left before termination, the do-action is stopped.

47/66

 s_1 82

The Concept of History, and Other Pseudo-States

Do Actions

 $\begin{array}{c} s_1 \\ entry/act_1^{entry} \\ do/act_1^{eo} \\ exit/act_1^{eoir} \\ E_1/act_{E_1} \end{array}$

 $tr[gd]/act = \frac{s_2}{entry/act_2^{ent}}$ do/act_2^{ed} $exit/act_2^{ed}$

Recall the overall UML State Machine philosophy:
 "An object is either idle or doing a run-to-completion step."
 Now, what is it exactly while the do action is executing...?

47,00

if the state is left before termination, the do-action is stopped.

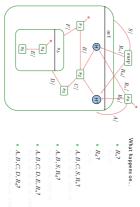
Intuition: after entering a state, start its do-action.
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48,66

History and Deep History: By Example



Note: not so sure about naming and symbols, e.g., I'd guessed it was just the other way round... Junction and Choice Junction ("static conditional branch"): • Choice: ("dynamic conditional branch") ķ Mad Had 50,66

Entry and Exit Point, Submachine State, Terminate

Entry and Exit Point, Submachine State, Terminate

Hierarchical states can be "folded" for readability.
 (but: this can also hinder readability.)

ullet Can even be taken from a different state-machine for re-use. S:s

Entry/exit points
 ○ ⊗

 Provide connection points for finer integration into the current level, than just via initial state.

Semantically a bit tricky:

 First the exit action of the exiting state, then the actions of the transition,

 then the entry actions of the entered state,
 then action of the transition from the entry point to an internal state, and then that internal state's entry action

Hierarchical states can be "folded" for readability.
 (but: this can also hinder readability.)

51,66

Junction and Choice

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good: abbreviation
 antiolds to so many similar transitions with different guards,
 the unfolded transitions are then checked for enabledness
 at best, start with trigger, branch into conditions, then apply actions

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ķ

Choice: ("dynamic conditional branch")

¢

• evil: may get stuck

enters the transition without knowing whether there's an enabled path
 at best, use "else" and convince yourself that it cannot get stuck
 maybe even better: avoid

50,66

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50/66

Entry and Exit Point, Submachine State, Terminate

- Hierarchical states can be "folded" for readability. (but: this can also hinder readability.)

- Entry/exit points

 Provide connection points for finer integration into the current level, than just via initial state.
- Semantically a bit tricky:
 First the exit action of the exiting state,
- then the actions of the transition,
- then the entry actions of the entered state,
 then action of the transition from the entry point to an internal state,
- and then that internal state's entry action.
- Terminate Pseudo-State

When a terminate pseudo-state is reached, the object taking the transition is immediately killed.

51,00

Deferred Events in State-Machines

Active and Passive Objects [?]

52,66

53,66

What about non-Active Objects? We're still working under the assumption that all classes in the class diagram (and thus all objects) are active.

That is, each object has its own thread of control and is (if stable) at any time ready to process an event from the ether.

But the world doesn't consist of only active objects.

For instance, in the crossing controller from the exercises we could wish to have the whole system live in one thread of control. So we have to address questions like:

Can we send events to a non-active object?

 And if so, when are these events processed? • etc.

54,66

Active and Passive Objects: Nomenclature

[7] propose the following (orthogonal!) notions:
 A class (and thus the instances of this class) is either active or passive as declared in the class diagram.

An active object has (in the operating system sense) an own thread: an own program counter, an own stack, etc.
 A passive object doesn't.

55/66

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A class is either reactive or non-reactive.
A reactive class has a (non-trivial) state machine.
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Active and Passive Objects: Nomenclature

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- An active object has (in the operating system sense) an own thread: an own program counter, an own stack, etc.
- A passive object doesn't.
- A class is either reactive or non-reactive.
 A reactive class has a (non-trivial) state machine.
- A non-reactive one hasn't.

Which combinations do we understand? non-reactive active passive

55/66

Passive and Reactive

- So why don't we understand passive/reactive?
 Assume passive objects u₁ and u₂, and active object u_r and that there are events in the ether for all three.

Which of them (can) start a run-to-completion step...?

Do run-to-completion steps still interleave...?

56,66

Passive Reactive Classes

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• Firstly, establish that each object u knows, via (implicit) link dsAct, the active object u_{act} which is responsible for dispatching events to u.

If u is an instance of an active class, then u_a = u.

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- If u is an instance of an active class, then $u_a=u$.



- Sending an event:
 Establish that of each signal we have a version E_C with an association $dest: C_{0,1}, C \in \mathscr{C}$.
- Then n!E in u₁: C₁ becomes:
- Create an instance u_e of E_{C_2} and set u_e 's dest to $u_d := \sigma(u_1)(n)$. Send to $u_a := \sigma(\sigma(u_1)(n))$ (dsAct), i.e., $\varepsilon' = \varepsilon \oplus (u_a, u_e)$.

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57,66

Passive and Reactive

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 Assume passive objects u₁ and u₂, and active object u_i and that there are events in the ether for all three.

Which of them (can) start a run-to-completion step...?

Do run-to-completion steps still interleave...?

Reasonable Approaches:

- Avoid for instance, by
- require that reactive implies active for model well-formedness.
 requiring for model well-formedness that events are never sent to instances of non-reactive classes.
- Explain here: (following [?])
 Delegate all dispatching of events to the active objects.

56/66

Passive Reactive Classes

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- If u is an instance of an active class, then $u_a=u$.



- Sending an event:

 Establish that of each signal we have a version Ec with an association dest: C_{0.1}, C∈ C. Dispatching an event:

 Observation: the ether only has events for active objects.

Then n!E in u₁: C₁ becomes:

- * Create an instance u_{ϵ} of E_{C_2} and process u_{ϵ} and waits until set u_{ϵ} is dext to $u_{\epsilon} := \sigma(u_1)(n)$. completion of corresponding RTC.

 * Send to $u_{\epsilon} := \sigma(\sigma(u_1)(n))(ds/dc)$. * u_{ℓ} may in particular discard event, i.e., $\varepsilon' = \varepsilon \oplus (u_{u_1}, u_{\ell})$. • Say u_e is ready in the ether for u_a . • Then u_a asks $\sigma(u_e)(dest) = u_d$ to process u_e — and waits until completion of corresponding RTC.

And What About Methods?

58,46

Behavioural Features

The implementation of a behavioural feature can be provided by:

An operation.

The class' state-machine ("triggered operation").

60,00

And What About Methods?

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In the current setting, the (local) state of objects is only modified by actions of transitions, which we abstract to transformers.

 UML follows an approach to separate
 the interface declaration from In general, there are also methods.

the implementation.
 In C++ lingo: distinguish declaration and definition of method.

- In the current setting, the (local) state of objects is only modified by actions of transitions, which we abstract to transformers.
- In general, there are also methods.
- UML follows an approach to separate
 the interface declaration from
- \bullet the implementation. In C++ lingo: distinguish declaration and definition of method.

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Behavioural Features

- The implementation of a behavioural feature can be provided by: An operation.
- In our setting, we simply assume a transformer like T_f . It is then, e.g. clear how to admit method calls as actions on transitions: function composition of transformers (clear but tedious: non-termination).
- In a setting with Java as action language: operation is a method body.
- The class' state-machine ("triggered operation").

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 In UML, the former is called behavioural feature and can (roughly) be • a call interface $f(\tau_{1_1},\ldots,\tau_{n_1}):\tau_1$

Note: The signal list is redundant as it can be looked up in the state machine of the class. But: certainly useful for documentation. a signal name E

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Behavioural Features

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An operation.

In our setting, we simply assume a transformer like T_f . It is then, e.g. clear how to admit method calls as actions on transitions function composition of transformers (clear but tedious non-termination).

In a setting with Java as action language: operation is a method body.

- The class' state-machine ("triggered operation").
 Calling F with no parameters for a stable instance of C
 creates an auxiliary event F and dispatches it (phassing the ether).
 Transition actions may fill in the etum value.
 On completion of the RTC etsay the call returns.
 For a non-stable instance, the caller blocks until stability is reached again.

Behavioural Features: Visibility and Properties

Extend typing rules to sequences of actions such that a well-typed action sequence only calls visible methods.

61,66

You are here.

Course Map

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TWN and

64,60

 $^{\sim}G = (N, E, f)$ Mathematics

 $w_{\pi} = ((\sigma_i, cons_i, Snd_i))_{i \in \mathbb{N}}$

Behavioural Features: Visibility and Properties

Visibility:
 Extend typing rules to sequences of actions such that a well-typed action sequence only calls visible methods.

Useful properties:
Concurrency
Concurrency
Control — is thread safe
Concurrency
Control — is thread safe
Concurrency
Control — is one mechanism ensures/should ensure mutual exclusion
Control — is cut thread safe, users have to ensure mutual exclusion
Control — is Squery — doesn't modify the state space (thus thread safe)

For simplicity, we leave the notion of steps unrouched, we construct our semantics around state machines.

Yet we could explain pre/ post in OCL (if we wanted to).

61,66

Discussion.

References

65/00

66,86