# Software Design, Modelling and Analysis in UML

Lecture 17: Hierarchical State Machines II

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### Contents & Goals

- Last Lecture:

  State Machines and OCL
  Hierarchical State Machines Syntax

### This Lecture:

- Initial and Final State

- Educational Objectives: Capabilities for following tasks/questions.
   What does this State Machine mean? What happens if I inject this event?
   Can you please model the following behaviour.
   What does this interarchical State Machine mean? What may happen if I inject this event?
   What is. AND-State. OR-State, pseudo-state, entry/exit/do. final state, ...
- Content:
   Composite State Semantics
   The Rest

2/44

3/44

### and instead of $F/\sqrt{fastN}$ Composite States fSfEwrite F/ F/ F/

Composite States

• In a sense, composite states are about abbreviation, structuring, and avoiding redundancy.

Idea: in Tron, for the Player's Statemachine, instead of

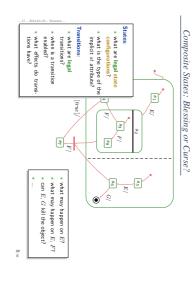
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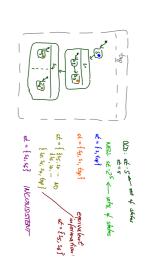
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Composite States
(formalisation follows [Damm et al., 2003])

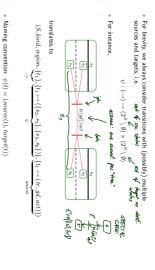
translates to  $\{top \mapsto \{s\}, s \mapsto \{\{s_1, s_1'\}, \{s_2, s_2'\}, \{s_3, s_3'\}\}, s_1 \mapsto \emptyset, s_1' \mapsto \emptyset, \dots\}, region$  $\frac{(q, ind)}{(\{(top, st), (s, st), (s_1, st)(s'_1, st)(s_2, st)(s'_2, st)(s_3, st)(s'_3, st)\}}_{S, kind}$  $\rightarrow$ ,  $\psi$ , annot)

6/44





## Syntax: Fork/Join



7/44

10/44

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A Partial Order on States

The substate- (or child-) relation induces a partial order on states: top ≤ s, for all s ∈ S,

•  $s \le s'$ , for all  $s' \in child(s)$ , • transitive, reflexive, antisymmetric, •  $s' \le s$  and  $s'' \le s$  implies  $s' \le s''$  or  $s'' \le s'$ 

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State Configuration

- The type of st is from now on a set of states, i.e.  $st:2^S$ 

\* A set  $S_1\subseteq S$  is called (legal) state configurations if and only if s  $top \in S_1$ , and s for each state  $s \in S_1$ , for each non-empty region  $\emptyset \neq R \in region(s)$ , exactly one (non pseudo-state) child of s (from R) is in  $S_1$ , i.e.  $|\{s_0 \in R \mid kind(s_0) \in \{st, fin\}\} \cap S_1| = 1.$ 

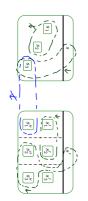
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## A Partial Order on States

The substate- (or child-) relation induces a partial order on states:  $\label{eq:standard} \mbox{ * transitive, reflexive, antisymmetric, }$   $\mbox{ * $s' \leq s$ and $s'' \leq s$ implies $s' \leq s''$ or $s'' \leq s'$. }$ •  $s \le s'$ , for all  $s' \in child(s)$ , •  $top \le s$ , for all  $s \in S$ , 8, Ws . s ≥ s # s ' ≤ s 8-82 S<sub>S</sub> 8,

Least Common Ancestor and Ting

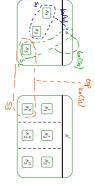
- A set of states  $S_1\subseteq S$  is called **consistent**, denoted by  $\downarrow S_1$ , if and only if for each  $s,s'\in S_1$ ,
- $s \leq s'$ , or  $s' \leq s$ , or



13/44

## Least Common Ancestor and Ting

- $\bullet$  The least common ancestor is the function  $ka:2^S\setminus\{\emptyset\}\to S$  such that • The states in  $S_1$  are (transitive) children of  $lca(S_1)$ , i.e.
- $lca(S_1) \le s$ , for all  $s \in S_1 \subseteq S$ ,
- $lca(S_1)$  is minimal, i.e. if  $\hat{s} \leq s$  for all  $s \in S_1$ , then  $\hat{s} \leq lca(S_1)$
- Note:  $lca(S_1)$  exists for all  $S_1 \subseteq S$  (last candidate: top).



11/4

10/44

Legal Transitions

I(t) source and destination are consistent, i.e.  $\downarrow$  source(t) and  $\downarrow$  tayet(t), I (ii) source (and destination) states are pairwise orthogonal, i.e. A hiearchical state-machine  $(S, kind, region, \rightarrow, \psi, annot)$  is called well-formed if and only if for all transitions  $t \in \rightarrow$ ,

(iii) the top state is neither source nor destination, i.e.

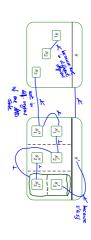
top ∉ source(t) ∪ power(t).
 togge
 Recall: final states are not sources of transitions.

forall s\( s' \) ∈ source(t) (∈ target(t)), s \( \pm s' \).

14/44

## Least Common Ancestor and Ting

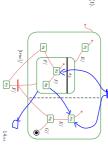
- \* Two states  $s_1,s_2\in S$  are called orthogonal, denoted  $s_1\perp s_2$ , if and only if \* they are unordered, i.e.  $s_1\not\leq s_2$  and  $s_2\not\leq s_1$ , and \* they "live" in different regions of an AND-state, i.e.
- $\exists s, region(s) = \{S_1, \ldots, S_n\} \ \exists 1 \leq i \neq j \leq n : s_1 \in child^*(S_i) \land s_2 \in child^*(S_j),$



12/44

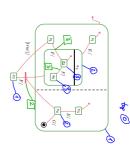
### Legal Transitions

- A hiearchical state-machine  $(S,kind,region,\rightarrow,\psi,amot)$  is called **well-formed** if and only if for all transitions  $t\in\rightarrow$ ,
- (ii) source (and destination) states are pairwise orthogonal, i.e. (i) source and destination are consistent, i.e. ↓ source(t) and ↓ target(t),
- forall s≠s' ∈ source(t) (∈ target(t)), s ⊥ s',
- s<sub>1</sub> E/



## The Depth of States

- $$\begin{split} & \cdot \ depth(top) = 0, \\ & \cdot \ depth(s') = depth(s) + 1, \, \text{for all} \, \, s' \in child(s) \end{split}$$



15/44

# Enabledness in Hierarchical State-Machines

 $prio(t) := \max\{depth(s) \mid s \in source(t)\}$ 

- T is consistent,

 $source(t) \subseteq \sigma(u)(st) \ (\subseteq S).$ 

16/44

 $\bullet$  The scope ("set of possibly affected states") of a transition t is the least common region of

 $source(t) \cup target(t)$ .

- Two transitions t<sub>1</sub>, t<sub>2</sub> are called consistent if and only if their scopes are orthogonal (i.e. states in scopes pairwise orthogonal).
   The priority of transition t is the depth of its innermost source state, i.e.

ullet A set of transitions  $T\subseteq o$  is enabled in an object u if and only if

• T is maximal wrt. priority, • all transitions in T share the same trigger, • all guards are satisfied by  $\sigma(u)$ , and • for all  $t \in T$ , the source states are active, i.e.

## Entry/Do/Exit Actions

- In general, with each state  $s \in S$  there is associated
- an entry, a do, and an exit action (default: skip)

Entry/Do/Exit Actions, Internal Transitions

a possibly empty set of trigger/action pairs called internal transitions,

 $tr[gd]/act = \frac{s_2}{entry/act_2^{entry}}$   $do/act_2^{do}$ 

(default: empty).  $E_1,\ldots,E_n\in\mathcal{E}$ , 'entry', 'do', 'exit' are reserved names!

ullet Recall: each action's supposed to have a transformer. Here:  $t_{act_1^{out}}, t_{act_2^{out}}, \ldots$ 

Taking the transition above then amounts to applying

 $t_{act_{s_2}^{serr}} \circ t_{act} \circ t_{act_{s_1}^{serr}}$ 

instead of only

18/44

 $\leadsto$  adjust (2.), (3.) accordingly.

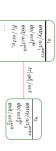
19/44

# Transitions in Hierarchical State-Machines

- Let T be a set of transitions enabled in u.
- Then  $(\sigma, \varepsilon) \xrightarrow{(cons, Snd)} (\sigma', \varepsilon')$  if
- $\sigma'(u)(st)$  consists of the target states of t,
- i.e. for simple states the simple states themselves, for composite states the initial states,
- $\sigma', \varepsilon', cons$ , and Snd are the effect of firing each transition  $t \in T$  one by one, in any order, i.e. for each  $t \in T$ ,
- the exit transformer of all affected states, highest depth first,
   the transformer of t,
   the entry transformer of all affected states, lowest depth first.
- $\rightarrow$  adjust (2.), (3.), (5.) accordingly.

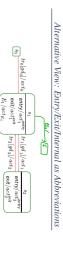
17/44

Internal Transitions



- For internal transitions, taking the one for E<sub>1</sub>, for instance, still amounts to taking only  $t_{act_{E_1}}$
- Intuition: The state is neither left nor entered, so: no exit, no entry.
- → adjust (2.) accordingly.
- Note: internal transitions also start a run-to-completion step.
- Note: the standard seems not to clarify whether internal transitions have priority over regular transitions with the same trigger at the same state.

Some code generators assume that internal transitions have priority!





- That is: Entry/Internal/Exit don't add express/depphysics Core State Machines. If internal actions should have priority, ay can be embedded into an OR-state (see later).
- Abbreviation may avoid confusion in context of hierarchical states (see later).

21/44

Do Actions



- Intuition: after entering a state, start its do-action.
- If the do-action terminates,
- then the state is considered completed,
- otherwise, if the state is left before termination, the do-action is stopped.
- Recall the overall UML State Machine philosophy:
   "An object is either idle or doing a run-to-completion step."
   Now, what is it exactly while the do action is executing...?

22)44

References

43/44

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44/4