Software Design, Modelling and Analysis in UML

Lecture 12: Core State Machines II

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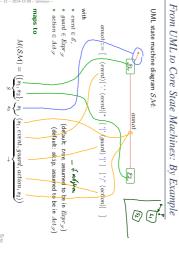
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Recall: Core State Machine

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 \begin{tabular}{ll} \bullet \ s_0 \in S \ \mbox{is an initial state,} \\ \bullet \ \mbox{and} \ \end{tabular} 
We assume a set Expr_{\mathscr{S}} of boolean expressions (may be OCL, may be something else) and a set Act_{\mathscr{S}} of actions over \mathscr{S}.

    S is a non-empty, finite set of (basic) states,

                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   Definition. A core state machine over signature \mathscr{S}=(\mathscr{T},\mathscr{C},V,atr,\mathscr{E}) is a tuple
                                                                                                                                                           is a labelled transition relation.
                                                                                                                                                                                                         \rightarrow \subseteq S \times \underbrace{(\mathscr{E} \cup \{\bot\})}_{\text{trigger}} \times \underbrace{Expr}_{\mathscr{G}} \times \underbrace{Act}_{\mathscr{G}} \times S
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          M = (S, s_0, \rightarrow)
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Contents & Goals

- Last Lecture:
- State machine syntax

This Lecture:

Educational Objectives: Capabilities for following tasks/questions.

What does this State Machine mean? What happens if I inject this event?

- Core state machines
- What is: Signal, Event, Ether, Transformer, Step, RTC. Can you please model the following behaviour.
- Content:
- The basic causality model
- System Configuration, Transformer
 Examples for transformer

Run-to-completion Step

Recall: Core State Machines

Annotations and Defaults in the Standard

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and let's play a bit with the defaults:

| You could be comply | You could be cou
• E(v:\tau) — similar, but v is a local variable, scope is the transition

    E(v) — when consuming E in object u,
attribute v of u is assigned the
corresponding attribute of E.

                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             Reconsider the syntax of transition annotations:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         (5) —> (5) —> (5)
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What is that useful for?

No Event:

6.2.3 The Basic Causality Model (1, 12)

The causality model is quite straightforward: "Causality model' is a specification of how things happen at run time [...].

- Objects respond to messages that are generated by objects executing communication actions.
- The dispatching method by which a particular behavior is associated with a given message depends on the higher-level formalism used and is not defined in the UML specification When these messages arrive, the receiving objects eventually respond by executing the behavior that is matched to that message.

(i.e., it is a semantic variation point).

The causality model also subsumes behaviors invoking each other and passing information to each other through arguments to parameters of the invoked

This purely 'procedural' or 'process' model can be used by itself or in conjunction with the object-oriented model of the previous example."

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State-Machines belong to Classes

- In the following, we assume that a UML models consists of a set \mathscr{CQ} of class diagrams and a set \mathscr{SM} of state chart diagrams (each comprising one state machines \mathscr{SM}).
- Furthermore, we assume that each state machine $\mathcal{SM}\in\mathcal{SM}$ is associated with a class $C_{\mathcal{SM}}\in\mathcal{C}(\mathcal{S})$.
- For simplicity, we even assume a bijection, i.e. we assume that each class $C\in \mathscr{C}(\mathscr{S})$ has a state machine \mathcal{SM}_C and that its class $C_{\mathcal{SM}_C}$ is C. If not explicitly given, then this one:

 $SM_0 := (\{s_0\}, s_0, \emptyset).$

We'll see later that, semantically, this choice does no harm.

- Intuition 1: \mathcal{SM}_C describes the behaviour of the instances of class C. Intuition 2: Each instance of C executes \mathcal{SM}_C with own "program counter"

- Note: we don't consider multiple state machines per class.
 (Because later (when we have AND-states) we'll see that this case can be viewed as a single state machine with as many AND-states.)

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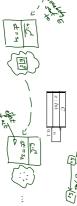
The Basic Causality Model

15.3.12 StateMachine (1, 563)

- Event occurrences are detected, dis-patched, and then processed by the state machine, one at a time.
- The semantics of event occurrence pro-cessing is based on the run-to- comple-tion assumption, interpreted as run-to-completion processing.
- The processing of a single event occur-rence by a state machine is known as a Run-to-completion processing means that an event [...] can only be taken from the pool and dispatched if the processing of the previous [...] is fully completed.
- Before commencing on a run-to-completion step, a state machine is in a stable state configuration with all entry/exit/internal-activities/but not necessarily do-activities) completed.
- The same conditions apply after the run-to-completion step is completed.
- Thus, an event occurrence will never be processed [...] in some intermediate and inconsistent situation.
- [IOW.] The run-to-completion step is the passage between two state configu-rations of the state machine. Stable letion step is
- The run-to-completion assumption simplifies the transition function of the StM, since concurrency conflicts are avoided during the processing of event, allowing the StM to safely complete its run-to-completion step.

6.2.3 The Basic Causality Model [7,12]

Objects respond to messages that are generated by objects executing communication actions. When these messages arrive, the receiving objects eventually re-spond by executing the behavior that is matched to that message.



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15.3.12 StateMachine [7, 563]

- The order of dequeuing is not defined, leaving open the possibility of modeling different priority-based schemes.
 Run-to-competion may be implemented in various ways. [...]

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(FFF) all is the - 1. \$5 = 28 | S \rightarrow $(\dot{\epsilon}_{n}^{\prime}\mathcal{E}_{v})$

And?

 $E[n\neq\emptyset]/x:=x+1;n!\,F$

 $F/x := 0 \qquad \qquad f := \emptyset$

We have to formally define what event occurrence is
 We have to define where events are stored – what the event pool is.
 We have to explain how transitions are chosen. "matching".

We have to explain what the effect of actions is – on state and event pool.

We have to decide on the granularity — micro-steps, steps, run-to-completion steps (aka. super-steps)?

We have to formally define a notion of stability and RTC-step completion.

And then: hierarchical state machines.

Roadmap: Chronologically

- (i) What do we (have to) cover? UML State Machine Diagrams Syntax
- (ii) Def.: Signature with signals.
- Summartics:

 The Basic Causality Model
 (v) Def: Ether (aka. event pool)
 (vi) Def: System configuration.
 (vii) Def: Tevent.
 (vii) Def: Transition system, computation.
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- (xi) Def.: step, run-to-completion step. (x) Transition relation induced by core state machine.

(iii) Def.: Core state machine.(iv) Map UML State Machine Diagrams to core state machines.

System Configuration, Ether, Transformer

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Ether aka. Event Pool

We call a tuple (Bth, ready, \oplus , \ominus , \ominus) an ether over $\mathscr S$ and $\mathscr S$ if and only if it provides for an one condition and Sth • a operation to insert an event destined for a given object, i.e. for $\mathcal{E}_{\mathbf{k}}$, $\mathcal{E}_{\mathbf{k}}$ Definition. Let $\mathscr{S}=(\mathscr{T},\mathscr{C},V,atr,\mathscr{E})$ be a signature with signals and \mathscr{D} a structure. clear the ether for a given object, i.e. $ready: Eth \times \mathscr{D}(\mathscr{C}) \to 2^{\mathscr{D}(\mathscr{E})}$ $[\cdot\,]: Eth \times \mathscr{D}(\mathscr{C}) \to Eth.$

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 One FIFO queue per active object is an ether. · [·] remore all luel pails from a john seperate

 \bullet Lossy queue (\oplus becomes a relation then).

Multi-queues (one per sender).

Priority queue.

One-place buffer.

Trivial example: sink, "black hole".

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The order of dequeuing is not defined, leaving open the possibility of modeling different priority-based schemes.
 Run-to-competion may be implemented in various ways. [...]

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15.3.12 StateMachine [1,563]