Software Design, Modelling and Analysis in UML

Lecture 15: Hierarchical State Machines I or: Cole State Machines I

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Contents & Goals

Last Lecture:

• RTC-Rules: Discard, Dispatch, Commence, 🖾 Step, RTC

This Lecture:

- Educational Objectives: Capabilities for following tasks/questions.
 - What does this State Machine mean? What happens if I inject this event?
 - Can you please model the following behaviour.
 - What is: initial state.
 - What does this **hierarchical** State Machine mean? What **may happen** if I inject this event?
 - What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state, ...

• Content:

- Transformer: Create and Destroy, Divergence
- Putting It All Together
- Hierarchical State Machines Syntax

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Missing Transformers: Create and Destroy

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Transformer: Create



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Transformer: Create

abstract syntax	concrete syntax
$\mathtt{create}(C, expr, v)$	
intuitive semantics	
Create an object of class C and assign it to	attribute v of the
object denoted by expression e	xpr.
well-typedness	
$expr: \tau_D, v \in atr(D),$	
$atr(C) = \{ \langle v_{\mathbf{i}} : \tau_{\mathbf{i}}, expr_i^0 \rangle \mid 1 \leq i \}$	$i \leq n\}$
semantics	
observables	
(error) conditions	
$I[\![expr_i^0]\!](\sigma,u_x)$ not defined for some $i.$	

- We use an "and assign"-action for simplicity it doesn't add or remove expressive power, but moving creation to the expression language raises all kinds of other problems such as order of evaluation (and thus creation).
- Also for simplicity: no parameters to construction (\sim parameters of constructor). Adding them is straightforward (but somewhat tedious).

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How To Choose New Identities?

- Re-use: choose any identity that is not alive now, i.e. not in $\operatorname{dom}(\sigma).$
 - Doesn't depend on history.
- and the second • May "undangle" dangling references - may happen on some platforms.
- Fresh: choose any identity that has not been alive ever, i.e. not in $dom(\sigma)$ and any predecessor in current run.
 - Depends on history.
 - Dangling references remain dangling could mask "dirty" effects of platform.

Transformer: Create



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Transformer: Destroy



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What to Do With the Remaining Objects?

Assume object u_0 is destroyed. v_3 ...

- object u_1 may still refer to it via association \boldsymbol{n} :
 - allow dangling references?
 - or remove u_0 from $\sigma(u_1)(\mathbf{n})$?
- object u_0 may have been the last one linking to object u_2 :
 - leave u_2 alone?

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- or remove u_2 also?
- Plus: (temporal extensions of) OCL may have dangling references.

Our choice: Dangling references and no garbage collection!

But: the more "dirty" effects we see in the model, the more expensive it often is to analyse. Valid proposal for simple analysis: monotone frame semantics, no destruction at all.



This is in line with "expect the worst", because there are target platforms which don't provide garbage collection — and models shall (in general) be correct without assumptions on target platform.

Transformer: Destroy

abstract syntax destroy(<i>expr</i>)	concrete syntax
intuitive semantics Destroy the object denoted by expr	ession expr.
well-typedness $expr: au_C, \ C \in \mathscr{C}$	
semantics $t[u_x](\sigma,\varepsilon) = (\sigma',\varepsilon)$	function restriction
where $\sigma' = \sigma _{\operatorname{dom}(\sigma) \setminus \{u\}}$ with $u = I[$	$[expr]](\sigma, u_x).$
observables	
$Obs_{\texttt{destroy}}[u_x] = \{(u_x, \bot, (+,$	$\emptyset), u)\}$
(error) conditions	
$I[\![expr]\!](\sigma,u_x)$ not define	d.

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Step and Run-to-completion Step

Notions of Steps: The Step

Note: we call one evolution $(\sigma, \varepsilon) \xrightarrow[u]{(cons,Snd)} u (\sigma', \varepsilon')$ a step.

Thus in our setting, a step directly corresponds to

one object (namely *u*) takes a single transition between regular states.

(We have to extend the concept of "single transition" for hierarchical state machines.)

That is: We're going for an interleaving semantics without true parallelism.

Remark: With only methods (later), the notion of step is not so clear. For example, consider

- c_1 calls f() at c_2 , which calls g() at c_1 which in turn calls h() for c_2 .
- Is the completion of h() a step?
- Or the completion of f()?
- Or doesn't it play a role?

Sstmstep

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It does play a role, because **constraints**/invariants are typically (= by convention) assumed to be evaluated at step boundaries, and sometimes the convention is meant to admit (temporary) violation in between steps. 13/42

Notions of Steps: The Run-to-Completion Step

What is a run-to-completion step ...?

- Intuition: a maximal sequence of steps, where the first step is a dispatch step and all later steps are commence steps.
- Note: one step corresponds to one transition in the state machine.

A run-to-completion step is in general not syntacically definable — one transition may be taken multiple times during an RTC-step.



Proposal: Let

$$(\sigma_0, \varepsilon_0) \xrightarrow{(cons_0, Snd_0)} \dots \xrightarrow{(cons_{n-1}, Snd_{n-1})} (\sigma_n, \varepsilon_n), \quad n > 0,$$

be a finite (!), non-empty, maximal, consecutive sequence such that

- object u is alive in σ_0 ,
- $u_0 = u$ and $(cons_0, Snd_0)$ indicates dispatching to u, i.e. $cons = \{(u, \vec{v} \mapsto \vec{d})\},\$
- there are no receptions by u in between, i.e.

$$cons_i \cap \{u\} \times Evs(\mathscr{E}, \mathscr{D}) = \emptyset, i > 1,$$

• $u_{n-1} = u$ and u is stable only in σ_0 and σ_n , i.e.

$$\sigma_0(u)(stable) = \sigma_n(u)(stable) = 1 \text{ and } \sigma_i(u)(stable) = 0 \text{ for } 0 < i < n,$$

Let $0 = k_1 < k_2 < \cdots < k_N = n$ be the maximal sequence of indices such that $u_{k_i} = u$ for $1 \leq i \leq N$. Then we call the sequence

$$(\sigma_0(u) =) \quad \sigma_{k_1}(u), \sigma_{k_2}(u) \dots, \sigma_{k_N}(u) \quad (= \sigma_{n-1}(u))$$

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Divergence

We say, object u can diverge on reception cons from (local) configuration $\sigma_0(u)$ if and only if there is an infinite, consecutive sequence

$$(\sigma_0, \varepsilon_0) \xrightarrow{(cons_0, Snd_0)} (\sigma_1, \varepsilon_1) \xrightarrow{(cons_1, Snd_1)} \cdots$$

such that u doesn't become stable again.

• Note: disappearance of object not considered in the definitions. By the current definitions, it's neither divergence nor an RTC-step.



Run-to-Completion Step: Discussion.

What people may **dislike** on our definition of RTC-step is that it takes a **global** and **non-compositional** view. That is:

- In the projection onto a single object we still see the effect of interaction with other objects.
- Adding classes (or even objects) may change the divergence behaviour of existing ones.
- Compositional would be: the behaviour of a set of objects is determined by the behaviour of each object "in isolation".
 Our semantics and notion of RTC-step doesn't have this (often desired) property.

Can we give (syntactical) criteria such that any global run-to-completion step is an interleaving of local ones?

Maybe: Strict interfaces.

(*Proof left as exercise...*)

- (A): Refer to private features only via "self".
- (Recall that other objects of the same class can modify private attributes.)
- (B): Let objects only communicate by events, i.e.
 - don't let them modify each other's local state via links at all.

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References

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