# Software Design, Modelling and Analysis in UML 

# Lecture 15: Hierarchical State Machines I or: Core State Machines V 

2015-01-08

Prof. Dr. Andreas Podelski, Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

## Contents \& Goals

## Last Lecture:

- RTC-Rules: Discard, Dispatch, Commence, Step, RTC


## This Lecture:

- Educational Objectives: Capabilities for following tasks/questions.
- What does this State Machine mean? What happens if I inject this event?
- Can you please model the following behaviour.
- What is: initial state.
- What does this hierarchical State Machine mean? What may happen if I inject this event?
- What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state, ...
- Content:
- Transformer: Create and Destroy, Divergence
- Putting It All Together
- Hierarchical State Machines Syntax


## Missing Transformers: Create and Destroy

## Transformer: Create




SO NOT: hew (i"ch(0.5):
IF NEEDED: tap: hew Circle;
tap, inlet (0.5);

## Transformer: Create



- We use an "and assign"-action for simplicity - it doesn't add or remove expressive power, but moving creation to the expression language raises all kinds of other problems such as order of evaluation (and thus creation).
- Also for simplicity: no parameters to construction ( $\sim$ parameters of constructor). Adding them is straightforward (but somewhat tedious).


## Create Transformer Example

$\mathcal{S M}_{C}:$


> create $(C$, expr,$v)$
> $t_{\text {create }(C, \operatorname{expr}, v)}\left[u_{x}\right](\sigma, \varepsilon)=\ldots$



## How To Choose New Identities?

- Re-use: choose any identity that is not alive now, ie. not in $\operatorname{dom}(\sigma)$.
- Doesn't depend on history.

- May "undangle" dangling references - may happen on some platforms.
- Fresh: choose any identity that has not been alive ever, ie. not in $\operatorname{dom}(\sigma)$ and any predecessor in current run.
- Depends on history.
- Dangling references remain dangling - could mask "dirty" effects of platform.


## Transformer: Create



## Transformer: Destroy

| abstract syntax destroy (expr) | concrete syntax clelete expr. |
| :---: | :---: |
| intuitive semantics |  |
| Destroy the object denoted by expression expr. |  |
| well-typedness |  |
| expr : $\tau_{C}, C \in \mathscr{C}$ |  |
| semantics |  |
| observables | Cert |
| (error) conditions |  |
|  |  |

## Destroy Transformer Example



## What to Do With the Remaining Objects?

Assume object $u_{0}$ is destroyed.by $v_{3} \ldots$

- object $u_{1}$ may still refer to it via association $\boldsymbol{n}$ :
- allow dangling references?
- or remove $u_{0}$ from $\sigma\left(u_{1}\right)(\boldsymbol{n})$ ?

- object $u_{0}$ may have been the last one linking to object $u_{2}$ :
- leave $u_{2}$ alone?
- or remove $u_{2}$ also?
- Plus: (temporal extensions of) OCL may have dangling references.

Our choice: Dangling references and no garbage collection!
This is in line with "expect the worst", because there are target platforms which don't provide garbage collection - and models shall (in general) be correct without assumptions on target platform.

But: the more "dirty" effects we see in the model, the more expensive it often is to analyse. Valid proposal for simple analysis: monotone frame semantics, no destruction at all.

## Transformer: Destroy

abstract syntax
destroy (expr)
intuitive semantics
Destroy the object denoted by expression expr.
well-typedness

$$
\operatorname{expr}: \tau_{C}, C \in \mathscr{C}
$$

semantics

$$
t\left[u_{x}\right](\sigma, \varepsilon)=\left(\sigma^{\prime}, \varepsilon\right) \quad \text { function restriction }
$$

where $\sigma^{\prime}=\left.\sigma\right|_{\operatorname{dom}(\sigma) \backslash\{u\}}$ with $u=I \llbracket \operatorname{expr} \rrbracket\left(\sigma, u_{x}\right)$.
observables
$O b s_{\text {destroy }}\left[u_{x}\right]=\left\{\left(u_{x}, \perp,(+, \emptyset), u\right)\right\}$
(error) conditions
$I \llbracket \operatorname{expr} r \rrbracket\left(\sigma, u_{x}\right)$ not defined.

Step and Run-to-completion Step

## Notions of Steps: The Step

Note: we call one evolution $(\sigma, \varepsilon) \xrightarrow[u]{(c o n s, S n d)}\left(\sigma^{\prime}, \varepsilon^{\prime}\right)$ a step.
Thus in our setting, a step directly corresponds to
one object (namely $u$ ) takes a single transition between regular states.
(We have to extend the concept of "single transition" for hierarchical state machines.)
That is: We're going for an interleaving semantics without true parallelism.
Remark: With only methods (later), the notion of step is not so clear.
For example, consider

- $c_{1}$ calls f() at $c_{2}$, which calls g() at $c_{1}$ which in turn calls h() for $c_{2}$.
- Is the completion of h() a step?
- Or the completion of $f()$ ?
- Or doesn't it play a role?

It does play a role, because constraints/invariants are typically (= by convention) assumed to be evaluated at step boundaries, and sometimes the convention is meant to admit (temporary) violation in between steps.

## Notions of Steps: The Run-to-Completion Step

What is a run-to-completion step...?

- Intuition: a maximal sequence of steps, where the first step is a dispatch step and all later steps are commence steps.
- Note: one step corresponds to one transition in the state machine.

A run-to-completion step is in general not syntacically definable - one transition may be taken multiple times during an RTC-step.

## Example:



$\sigma: \quad \frac{: C}{} \quad$|  |
| :--- |
| $x=2$ |

$\varepsilon:$


## Notions of Steps: The RTC Step Cont'd

Proposal: Let

$$
\left(\sigma_{0}, \varepsilon_{0}\right) \xrightarrow[u_{0}]{\left(\operatorname{cons}_{0}, S n d_{0}\right)} \ldots \frac{\left(\operatorname{cons}_{n-1}, S n d_{n-1}\right)}{u_{n-1}}\left(\sigma_{n}, \varepsilon_{n}\right), \quad n>0
$$

be a finite (!), non-empty, maximal, consecutive sequence such that

- object $u$ is alive in $\sigma_{0}$,
- $u_{0}=u$ and $\left(\right.$ cons $\left._{0}, S n d_{0}\right)$ indicates dispatching to $u$, i.e. cons $=\{(u, \vec{v} \mapsto \vec{d})\}$,
- there are no receptions by $u$ in between, ie.

$$
\operatorname{cons}_{i} \cap\{u\} \times \operatorname{Evs}(\mathscr{E}, \mathscr{D})=\emptyset, i>1,
$$

- $u_{n-1}=u$ and $u$ is stable only in $\sigma_{0}$ and $\sigma_{n}$, ie.

$$
\sigma_{0}(u)(\text { stable })=\sigma_{n}(u)(\text { stable })=1 \text { and } \sigma_{i}(u)(\text { stable })=0 \text { for } 0<i<n
$$

Let $0=k_{1}<k_{2}<\cdots<k_{N}=n$ be the maximal sequence of indices such that $u_{k_{i}}=u$ for $1 \leq i \leq N$. Then we call the sequence

$$
\left(\sigma_{0}(u)=\right) \quad \sigma_{k_{1}}(u), \sigma_{k_{2}}(u) \ldots, \sigma_{k_{N}}(u) \quad\left(=\sigma_{n-1}(u)\right)
$$

a (!) run-to-completion computation of $u$ (from (local) configuration $\sigma_{0}(u)$ ).

## Divergence

We say, object $u$ can diverge on reception cons from (local) configuration $\sigma_{0}(u)$ if and only if there is an infinite, consecutive sequence

$$
\left(\sigma_{0}, \varepsilon_{0}\right) \xrightarrow{\left(\text { cons }_{0}, S n d_{0}\right)}\left(\sigma_{1}, \varepsilon_{1}\right) \xrightarrow{\left(\text { cons }_{1}, S n d_{1}\right)} \ldots
$$

such that $u$ doesn't become stable again.

- Note: disappearance of object not considered in the definitions. By the current definitions, it's neither divergence nor an RTC-step.



## Run-to-Completion Step: Discussion.

What people may dislike on our definition of RTC-step is that it takes a global and non-compositional view. That is:

- In the projection onto a single object we still see the effect of interaction with other objects.
- Adding classes (or even objects) may change the divergence behaviour of existing ones.
- Compositional would be: the behaviour of a set of objects is determined by the behaviour of each object "in isolation".
Our semantics and notion of RTC-step doesn't have this (often desired) property.
Can we give (syntactical) criteria such that any global run-to-completion step is an interleaving of local ones?

Maybe: Strict interfaces.
(Proof left as exercise...)

- (A): Refer to private features only via "self".
(Recall that other objects of the same class can modify private attributes.)
- (B): Let objects only communicate by events, i.e.
don't let them modify each other's local state via links at all.


## References

[Crane and Dingel, 2007] Crane, M. L. and Dingel, J. (2007). UML vs. classical vs. rhapsody statecharts: not all models are created equal. Software and Systems Modeling, 6(4):415-435.
[Damm et al., 2003] Damm, W., Josko, B., Votintseva, A., and Pnueli, A. (2003). A formal semantics for a UML kernel language 1.2. IST/33522/WP 1.1/D1.1.2-Part1, Version 1.2.
[Fecher and Schönborn, 2007] Fecher, H. and Schönborn, J. (2007). UML 2.0 state machines: Complete formal semantics via core state machines. In Brim, L., Haverkort, B. R., Leucker, M., and van de Pol, J., editors, FMICS/PDMC, volume 4346 of LNCS, pages 244-260. Springer.
[Harel and Kugler, 2004] Harel, D. and Kugler, H. (2004). The rhapsody semantics of statecharts. In Ehrig, H., Damm, W., Große-Rhode, M., Reif, W., Schnieder, E., and Westkämper, E., editors, Integration of Software Specification Techniques for Applications in Engineering, number 3147 in LNCS, pages 325-354. Springer-Verlag.
[OMG, 2007] OMG (2007). Unified modeling language: Superstructure, version 2.1.2. Technical Report formal/07-11-02.
[Störrle, 2005] Störrle, H. (2005). UML 2 für Studenten. Pearson Studium.

