Software Design, Modelling and Analysis in UML

Lecture 18: Hierarchical State Machines II

2015-01-22

Prof. Dr. Andreas Podelski, Dr. Bernd Westphal

Albert-Ludwigs-Universität Freiburg, Germany

Initial Pseudostate



- when entering a region without a specific destination state,
- then go to a state which is destination of an initiation transition,
- execute the action of the chosen initiation transitions between exit and entry actions.

Special case: the region of top.

- ullet If class C has a state-machine, then "create-C transformer" is the concatenation of
- ullet the transformer of the "constructor" of C (here not introduced explicitly) and
- a transformer corresponding to one initiation transition of the top region.

Contents & Goals

Last Lecture:

- Hierarchical State Machine Syntax

- Entry/Exit Actions

This Lecture:

Educational Objectives: Capabilities for following tasks/questions.

What does this State Machine mean? What happens if I inject this event?

- What does this hierarchical State Machine mean? What may happen if I inject this event? Can you please model the following behaviour.
- What is: AND-State, OR-State, pseudo-state, entry/exit/do, final state, ...

- Content: Initial and Final State
- Composite State Semantics
 The Rest

2/30

3/30

Initial Pseudostates and Final States

Final States

Towards Final States: Completion of States

ullet Dispatching (here: E) can then alternatively be viewed as

(iii) remove event from the ether, (ii) take an enabled transition (here: to s_2), (i) fetch event (here: E) from the ether, * Transitions without trigger can conceptionally be viewed as being sensitive for the "completion event".



- a step of object u moves u into a final state (s, fin), and
- all sibling regions are in a final state,
- is raised. then (conceptionally) a completion event for the current composite state \boldsymbol{s}
- If there is a transition of a parent state (i.e., inverse of chidt) of s enabled which is sensitive for the completion event,
- then take that transition,
- ullet otherwise kill u
- \leadsto adjust (2.) and (3.) in the semantics accordingly

(vi) if there is a transition enabled which is sensitive for the completion event, (v) raise a completion event — with strict priority over events from ether! (iv) after having finished entry and do action of current state (here: s_2) — the state is then called <code>completed</code> —,

then take it (here: (s₂, s₃)).
otherwise become stable.

5/30

• One consequence: $u \ {\rm never} \ "survives" \ {\rm reaching} \ {\rm a} \ {\rm state} \ (s,fin) \ {\rm with} \ s \in child(top).$

Composite States
(formalisation follows [Danun et al., 2003])

Composite States

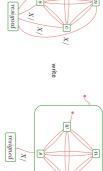
In a sense, composite states are about abbreviation, structuring, and avoiding redundancy.

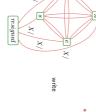
and instead of Composite States

fE.

write

Idea: in Tron, for the Player's Statemachine, instead of

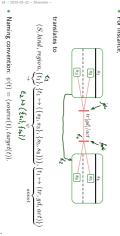




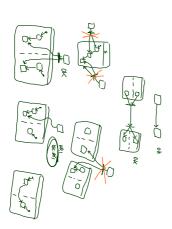
Syntax: Fork/Join

Recall: Syntax

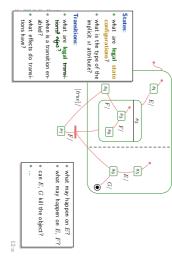
- For brevity, we always consider transitions with (possibly) multiple sources and targets, i.e.
- $\psi:(\rightarrow)\rightarrow(2^S\setminus\emptyset)\times(2^S\setminus\emptyset)$



 $\{top \mapsto \{\{s\}\}\} s \mapsto \{\{s_1, s_1'\}, \{s_2, s_2'\}, \{s_3, s_3'\}\}, s_1 \mapsto \emptyset, s_1' \mapsto \emptyset, \dots\}$ $\underbrace{\{(top,st),(s,st),(s_1,st)(s_1',st)(s_2',st)(s_2',st)(s_3,st)(s_3',st)\}}_{S,kind},$



Composite States: Blessing or Curse?



A Partial Order on States

Least Common Ancestor and Ting

 \bullet The least common ancestor is the function $\mathit{lca}: 2^S \setminus \{\emptyset\} \to S$ such that

• The states in S_1 are (transitive) children of $lca(S_1)$, i.e.

 $lca(S_1) \leq s$, for all $s \in S_1 \subseteq S$,

• Note: $laa(S_1)$ exists for all $S_1 \subseteq S$ (last candidate: top).

[ca ({s,'s;})=s'

 s_1 s_2

 s_3

S₁ S₁ S₂ S₂ S₂ S₂

\$\frac{1}{2}\frac{1}{2

• $lca(S_1)$ is minimal, i.e. if $\hat{s} \leq s$ for all $s \in S_1$, then $\hat{s} \leq lca(S_1)$

The substate- (or child-) relation induces a partial order on states:

- $\bullet \ top \leq s, \ \text{for all} \ s \in S,$

- $$\begin{split} & \quad s \leq s', \text{ for all } s' \in child(s), \\ & \quad \text{e transitive, reflexive, antisymmetric,} \\ & \quad \text{e } s' \leq s \text{ and } s'' \leq s \text{ implies } s' \leq s'' \text{ or } s'' \leq s'. \end{split}$$

[3/

14/30

State Configuration

State Configuration

• A set $S_1 \subseteq S$ is called (legal) state configurations if and only if \bullet The type of st is from now on a set of states, i.e. $st:2^S$

* $top \in S_1$, and * for each non-empty region $\emptyset \neq R \in region(s)$, * for each state $s \in S_1$, for each non-empty region $\emptyset \neq R \in region(s)$, exactly one (non pseudo-state) child of s (from R) is in S_1 , i.e.

 $|\{s_0 \in R \mid kind(s_0) \in \{\textit{st}, \textit{fin}\}\} \cap S_1| = 1.$

- \bullet The type of st is from now on a set of states, i.e. $st:2^S$
- A set $S_1 \subseteq S$ is called (legal) state configurations if and only if
- $top \in S_1$, and
- for each state $s \in S_1$, for each non-empty region $\emptyset \neq R \in region(s)$, exactly one (non pseudo-state) child of s (from R) is in S_1 , i.e.

 $|\{s_0 \in R \mid kind(s_0) \in \{\mathit{st}, \mathit{fin}\}\} \cap S_1| = 1.$

 Examples: ³⁶ 2

 $S = \{s_2\} \times (bop \ missing)$ $S = \{s_2, bop\} \times (no \ child of \ both \ rghn)$ S={tq,s,s2} V

13/30

Examples:

S={40, 5, 51, 52, 53}

{ 5, 52, 53 }

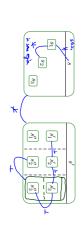
13/30

NOTE: S can be abbreviated as

Least Common Ancestor and Ting

- ullet Two states $s_1,s_2\in S$ are called **orthogonal**, denoted $s_1\perp s_2$, if and only if
- transitive child
- they are unordered, i.e. $s_1 \not \le s_2$ and $s_2 \not \le s_1$, and they "live" in different regions of an AND-state, i.e.

 $\exists s, region(s) = \{S_1, \dots, S_n\} \exists 1 \le i \ne j \le n : s_1 \in child^*(S_i) \land s_2 \in child^*(S_j).$



Least Common Ancestor and Ting

- A set of states $S_1\subseteq S$ is called **consistent**, denoted by $\downarrow S_1$, if and only if for each $s,s'\in S_1$,
- $s \leq s'$, or
- $s' \leq s$, or $s \perp s'$.
- 8,7 8" 13% S'' S''

17/30

[Crane and Dingel, 2007] Crane, M. L. and Dingel, J. (2007). UML vs. dassical vs. rhapsody statecharts: not all models are created equal. Software and Systems Modeling, 6(4):415–435.

[Damm et al., 2003] Damm, W., Josko, B., Votintseva, A., and Pnueli, A. (2003). A formal semantics for a UML kernel language 1.2. IST/33522/WP 1.1/D1.12-Part., Version 1.2.

[Feether and Schönborn, 2007] Feether, H. and Schönborn, J. (2007). UMI, 2.0 state machines. Complete formal semantics via core state machines. In Birin, L., Haverkort, B. R., Leudeer, M., and van de Pol. J., actions, PMICS/PDMC, volume 4346 of LVCS, pages 244–250. Springer.
[Harel and Kugler, 2004] Harel. D. and Kugler, H. (2004). The rhapsody semantics of statecharts. In Enrig, H., Damm, W., Groble-Rhoole, M., Reif, W., Schnieder, E., and Westkimper, E., editors, Integration of Software Specification in Techniques for Applications in Engineering, number 3147 in LNCS, pages 325–354.

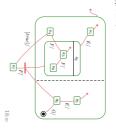
[OMG, 2007] OMG (2007). Unified modeling language: Superstructure, version 2.1.2. Technical Report formal/07-11-02.

30/30

Legal Transitions (fg.)

A hiearchical state-machine $(S,kind,region,\rightarrow,\psi,annot)$ is called **well-formed** if and only if for all transitions $t\in\rightarrow$,

- $\mathbb{Z}[i)$ source and destination are consistent, i.e. $\downarrow source(t)$ and $\downarrow target(t)$.] $\mathbb{Z}[i]$ source (and destination) states are pairwise orthogonal, i.e. • forall $s \neq s' \in source(t)$ ($\in target(t)$), $s \perp s'$,
- Example: Recall: final states are not sources of transitions. (iii) the top state is neither source nor destination, i.e. top ∉ source(t) ∪ source(t).



References