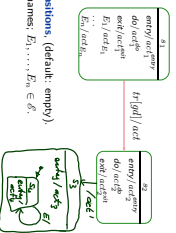


*Contents & Goals*

- Last Lecture:
  - Legal state configurations
  - Legal transitions
  - Rules (i) to (v) for hierarchical state machines
- This Lecture:
  - Educational Objectives: Capabilities for following tasks/questions:
    - How do entry / exit actions work? What about do-actions?
    - What is the effect of shallow / deep history pseudo-states?
    - What about junction, choice, terminate, etc.?
    - What is the idea of deferred events?
    - How are passive reactive objects treated in Rhapsody's UML semantics?
    - What about methods?
  - Content:
    - Entry / exit / do actions, internal transitions
    - Remaining pseudo-states, deferred events
    - Passive reactive objects
    - Behavioural features

*Entry/Do/Exit Actions*

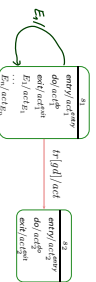
- In general, with each state  $s \in S$  there is associated
    - an entry, a do, and an exit action (default: skip)
    - a possibly empty set of trigger/action pairs called **Internal transitions**, (default: empty).
- Note: 'entry', 'do', 'exit' are reserved names;  $E_1, \dots, E_n \in \mathcal{E}$ .



- Recall: each action is supposed to have a transformer, assume  $f_{s_1, E_1}, f_{s_1, E_2}, \dots$
- Taking the transition above then amounts to applying
 
$$f_{s_1, E_1} \circ f_{a_1} \circ f_{s_2, E_3} \circ f_{s_2, E_4}$$
 instead of just
 
$$f_{a_1}$$
- adjust Rules (i), (ii), and (v) accordingly.

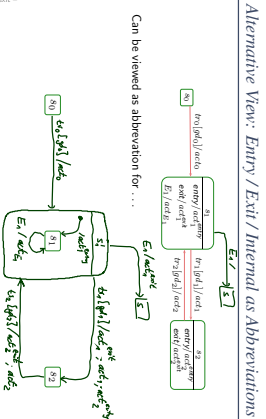
*Internal Transitions*

- Taking an **internal transition**, e.g. on  $E_1$ , only executes  $f_{s, E_1}$ .
  - **Intuition:** The state is neither left nor entered, so: no exit, no entry action.
  - Note: internal transitions also start a run-to-completion step.
- adjust Rules (i), (ii), and (v) accordingly.

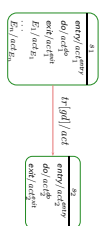


Note: the standard seems not to clarify whether internal transitions have **priority over** regular transitions with the same trigger at the same state  
 Some code generators assume that internal transitions have priority

*Entry and Exit Actions*

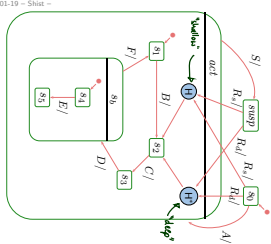


- **That is:** Entry / Internal / Exit don't add expressive power to Core State Machines. If internal actions should have priority,  $s_1$  can be embedded into an OR-state.
- Abbreviation view may avoid confusion in context of hierarchical states.



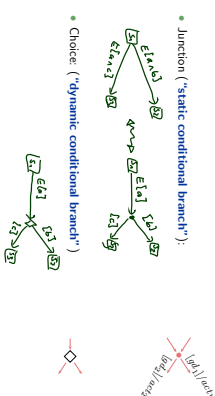
- **Intuition:** after entering a state, start its do-action.
  - If the do-action terminates, then the state is considered **completed** (the final state).
  - otherwise,
  - If the state is left before termination, the do-action is stopped.
- Recall the overall UML State Machine philosophy:
- "An object is either idle or doing a run-to-completion step."**
- Now, what is it exactly while the do action is executing...?

The Concept of History, and Other Pseudo-States

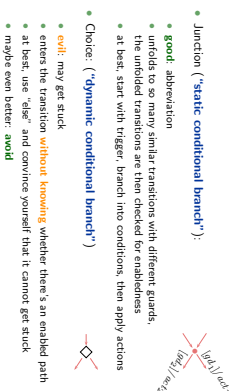


- What happens on...
- R1?
  - S1, S3
  - R2?
  - S2, S3
  - A, B, C, S, R, T
  - S1, S2, S3, S4, S5, S6, S7, S8, S9, S10
  - A, B, C, S, R, T
  - S1, S2, S3, S4, S5, S6, S7, S8, S9, S10
  - A, B, C, D, E, F, G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z
  - A, B, C, D, E, S, R, T
  - S1, S2, S3, S4, S5, S6, S7, S8, S9, S10

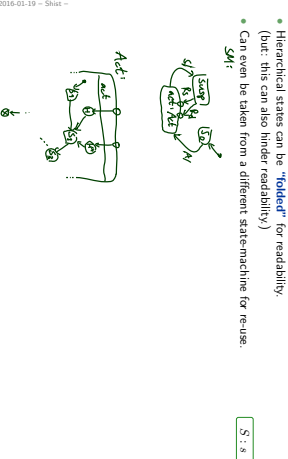
Junction and Choice



Junction and Choice



Entry and Exit Point, Submachine State, Terminate



- Hierarchical states can be “**folded**” for readability. (i.e., this can also hinder readability)
- Can even be taken from a different state-machine for re-use
- **Entry/exit points**
- Provide connection points for finer integration into the current level, than just via initial state
- Semantically a bit tricky:
  - First the exit action of the existing state,
  - then the actions of the transition,
  - then the entry actions of the entered state,
  - then action of the transition from the entry point to an internal state,
  - and then that internal state’s entry action
- **Terminate Pseudo-State**
- When a terminate pseudo-state is reached, the object taking the transition is immediately killed.

11/31



Deferred Events in State-Machines

14/31

Are We Done?

12/31

Deferred Events: Idea

- UML state machines comprise the feature of **deferred events**.
- The idea is as follows:
- Consider the following state machine:
    - $E: (a, e^*) \{a, e^* e\}, (a, a^* e)$
  - Assume we’re stable in  $s_1$ , and  $F$  is ready in the ether.
  - In the **framework of our course**,  $F$  is discarded!
  - But we may find it a pity to discard the poor event and we may want to remember it for later processing, e.g. in  $s_2$ , in other words: **defer** it.
- General options** to satisfy such needs:
- Provide a pattern how to “program” this (use self-loops and helper attributes)
  - Turn it into an original language concept. (← **OMG’s choice**)



15/31

UML distinguishes the following **kinds of states**:

	example		example
<b>simple state</b>		<b>pseudo-state</b>	
<b>final state</b>		<b>initial (shallow) history</b>	
<b>composite state</b>		<b>deep history</b>	
<b>OR</b>		<b>fork/join</b>	
<b>AND</b>		<b>junction, choice</b>	
		<b>entry point</b>	
		<b>exit point</b>	
		<b>terminate</b>	
		<b>submachine state</b>	

13/31

Deferred Events: Syntax and Semantics

- **Syntactically**,
  - Each state has (in addition to the name) a set of deferred events.
  - **Default**: the empty set.
  - The **semantics** is a bit intricate, something like
    - if Rule (i) (discard) would apply,
    - but  $E'$  is in the deferred set of the current state configuration,
    - then stuff  $E'$  into some “deferred events space” of the object, (e.g. into the ether (= extend  $\delta$ ) or into the local state of the object (= extend  $\sigma$ ))
    - and turn attention to the next event.
  - **Not so obvious**:
    - Is there a priority between deferred and regular events?
    - Is the order of deferred events preserved?
  - ...
- Fischer and Schöbhorn (2007), e.g. claim to provide semantics for the complete Hierarchical State Machine language, including deferred events.

16/31

Active and Passive Objects

What about non-Active Objects?

- Recall:**
- Were **not** working under the assumption that all classes in the class diagram (and thus all objects) are **active**.
  - That is, each object has its own thread of control and is (if stable) at any time ready to process an event from the ether.
  - steps of active objects can **interleave**.

But the world doesn't consist of only active objects.  
 For instance, ~~if the crossing controller from the exercise~~ we could wish to have the whole system live in one thread of control.

- So we have to address questions like:
- Can we send events to a non-active object?
  - And if so, when are these events processed?
  - etc.

Active and Passive Objects: Nomenclature

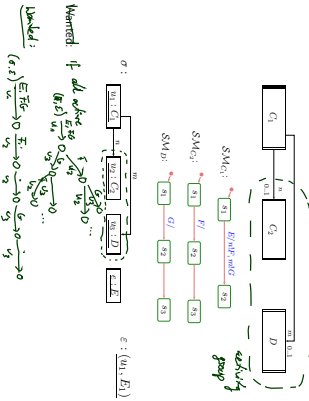
- Herl and Gery (1997) propose the following (orthogonal!) notions:
- A class (and thus the instances of this class) is either **active** or **passive** as defined by the class diagram.
  - An active object has (in the operating system sense) an own thread: an own program counter, an own stack, etc.
  - A **passive** object doesn't.

- A class is either **reactive** or **non-reactive**.
- A **reactive** class has a (non-trivial) state machine.
- A **non-reactive** one hasn't.

Which combinations do we (not) understand yet?

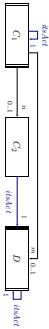
	active	passive
reactive	✓	?
non-reactive	(?)	(?)

Passive and Reactive / Rhapsody Style: Example



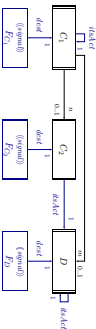
Passive Reactive / Rhapsody Style

- In each class, add (implicit) link  $link_{i,act}$  and use it to make each object  $u_i$  know the active object  $u_a$ , which is responsible for dispatching events to  $u_i$ .
- If  $u_i$  is an instance of an active class, then  $u_a = u_i$ .

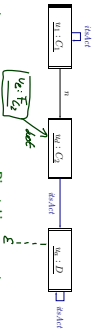


Passive Reactive / Rhapsody Style

- In each class, add (implicit) link  $link_{i,act}$  and use it to make each object  $u_i$  know the active object  $u_a$ , which is responsible for dispatching events to  $u_i$ .
- If  $u_i$  is an instance of an active class, then  $u_a = u_i$ .
- Equip all signals with (implicit) association  $dest$  and use it, to point to the destination object.
- For each signal  $F_i$ , have a version  $F_{i,0}$  with an association  $dest : C_{a1}, C \in \mathcal{C}$  (no inheritance yet).



- In each class, add (implicit) link  $link(d, d')$  and use it to make each object  $u$  know the active object  $u_a$  which is responsible for dispatching events to  $u$ .
  - If  $u$  is an instance of an active class, then  $u_a = u$ .
  - Equip all signals with (implicit) association  $dest$  and use it to point to the destination object.
- For each signal  $F_i$ , have a version  $F_i'$  with an association  $dest' : C_0, 1, C \in \mathcal{C}$  (no inheritance yet)



- Sending an event:**
- $n!F$  in  $u_1 : C_1$  becomes:
  - Create an instance  $u_2$  of  $F_2$  and set  $u_2$ 's  $dest$  to  $u_1 := a'(u_1)(a)$ .
  - Send to  $u_2 := a'(u_1)(a)(d, d')$ .
  - i.e.,  $e' = e @ (u_1, u_2)$ .
- Dispatching an event:**
- Observation: the other only has events for active objects.
  - Say  $u_2$  is ready in the other for  $u_1$ .
  - Then  $u_1$  asks  $a'(u_1)(d, d') = u_2$  to process  $u_2$  — and waits until completion of corresponding RT C.
  - $u_2$  may in particular discard event.

Discussion

- Prescriptive view:** They are legion...
- For instance,
  - allow absence of initial pseudo-states
  - object may then "be" in ending state without being in any substate;
  - or assume one of the children states non-deterministically
  - (e.g. in the case of a state machine with a signal  $sig$  for considering the order in which sigs have been added to the CASE tool's repository,
  - or some graphical order (left to right, top to bottom))
  - allow true concurrency
  - etc. etc.
- Exercise: Search the standard for "semantical variation point".

- Optimistic view:** tools exist with complete and consistent code generation.
- Crane and Digne (2007), e.g., provide an in-depth comparison of Statemate, UML, and Rhapsody state machines — the bottom line is:
  - the interaction is not empty
  - (i.e. there are pictures that mean the same thing to all three communities)
  - more is the state of another
  - (i.e. for each pair of semanticses exist pictures meaning different things)

And What About Methods?

And What About Methods?

- In the current setting, the (local) state of objects is **only modified** by actions or transitions, which we abstract to transformers.
- In general, there are also **methods**.
- UML follows an approach to separate
  - the **interface declaration** from
  - the **implementation**.
- In C++-lingo: distinguish **declaration** and **definition** of method.
- In UML, the former is called **behavioural feature** and can (roughly) be
  - a **call interface**  $(T_1, \dots, T_n) : T_i$
  - a **signal name**  $E$



Behavioural Features

- Semantics:**
- The **implementation** of a behavioural feature can be provided by:
    - An **operation**
- In our setting, we simply assume a transformer like  $T_i$ . It is then, e.g. clear how to admit method calls as actions on transitions: function composition of transformers (clear but tedious: non-termination).
- In a setting with Java as action language: operation is a method body.
- The class **state-machine** ("triggered operation")
    - Calling  $F_i$  with no parameters for a stable instance of  $C$
    - Calling  $F_i$  with parameters for a stable instance of  $C$
    - Transition actions may fill in the return value.
    - On completion of the RT C step, the call returns.
    - For a non-stable instance: the caller blocks until stability is reached again.



$C$
$S_1, A_1(\dots, T_{n_1}), T_{n_1}, T_{n_1}$
$S_2, A_2(\dots, T_{n_2}), T_{n_2}, T_{n_2}$
$\dots$
$S_n, A_n(\dots, T_{n_n}), T_{n_n}, T_{n_n}$

- **Visibility:**
  - Extend typing rules to sequences of actions such that a well-typed action sequence only calls visible methods.
- **Useful properties:**
  - **concurrency** — is thread safe
  - **quantified** — some mechanism ensures/should ensure mutual exclusion
  - **sequential** — is not thread safe, users have to ensure mutual exclusion
  - **isQuery** — doesn't modify the state space (thus thread safe)
- For simplicity, we leave the notion of steps untouched, we construct our semantics around state machines. Yet we could explain *pre/post* in OCL (if we wanted to).

### References

Cann, M. L. and Dreyel, J. (2007). UML vs. classical vs. rhapsody statecharts: not all models are created equal. *Software and Systems Modeling*, 6(4):415–435.

Fischer, H. and Schabhorn, J. (2007). UML 2.0 state machines: Complete formal semantics via core state machines. In Brim, L., Haverkort, B. R., Leucker, M., and van de Pol, J., editors, *FMICS/PDMC*, volume 4346 of *LNCS*, pages 284–260. Springer, 30(7):31–42.

Harel, D. and Gery, E. (1997). Executable object modeling with statecharts. *IEEE Computer*, OMC (2011a). Unified modeling language: Infrastructure, version 2.4.1. Technical Report formal/2011-08-05.

OMG (2011b). Unified modeling language: Superstructure, version 2.4.1. Technical Report formal/2011-08-06.