

*Real-Time Systems*

*Lecture 19: Quasi-Equal Clocks*

2018-01-25

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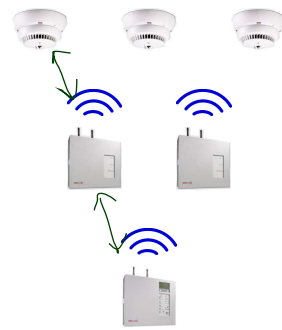
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*Motivation*

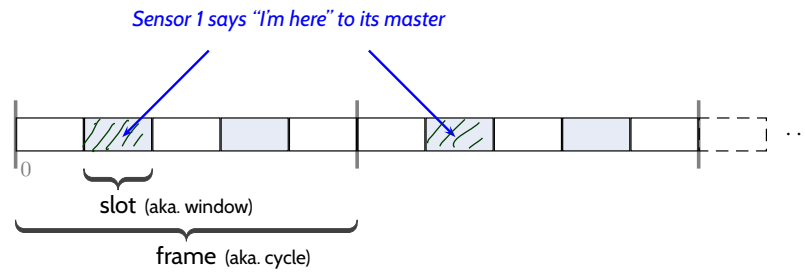
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## WFAS Self-Monitoring

- Periodically, **each sensor** sends a “**hi master, I’m still here**” message to its master.
- If a master misses that message from one of its sensors: report incidence.
- To avoid **message collision**, employ a TDMA (time division multiple access) scheme.



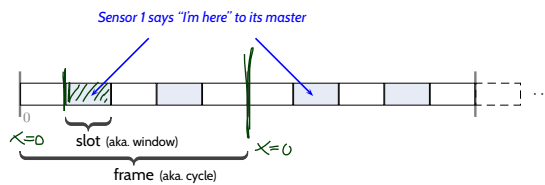
(Arenis et al., 2016)



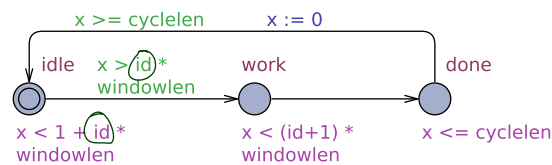
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## WFAS Self-Monitoring



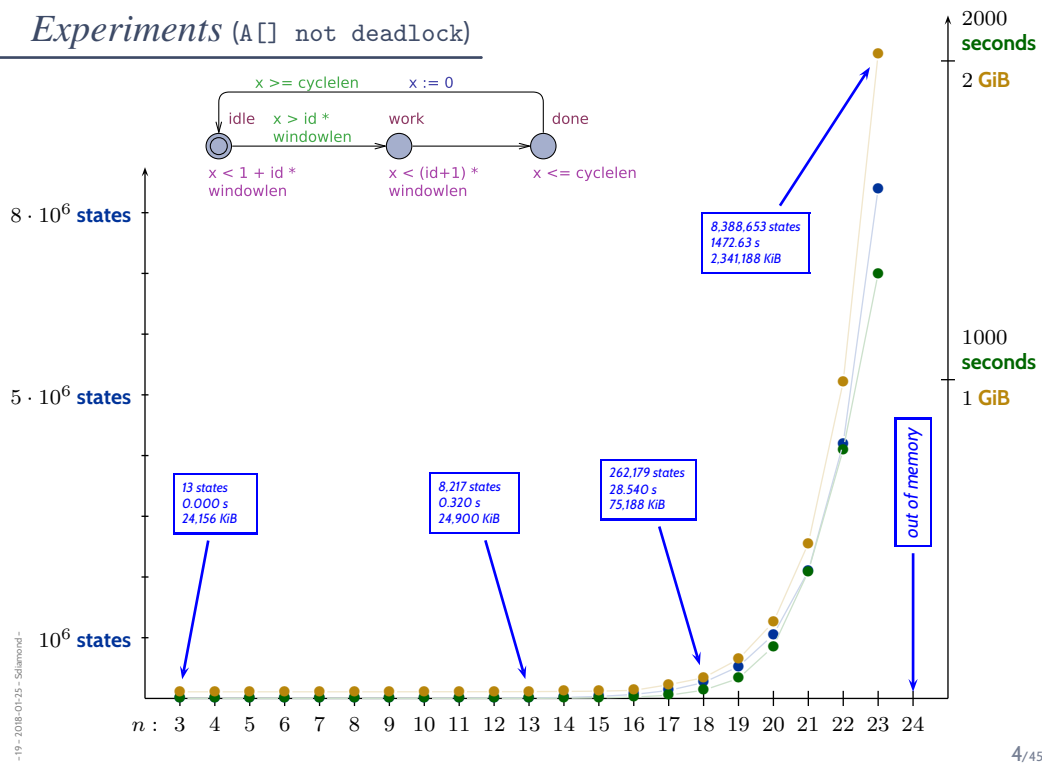
(Arenis et al., 2016)



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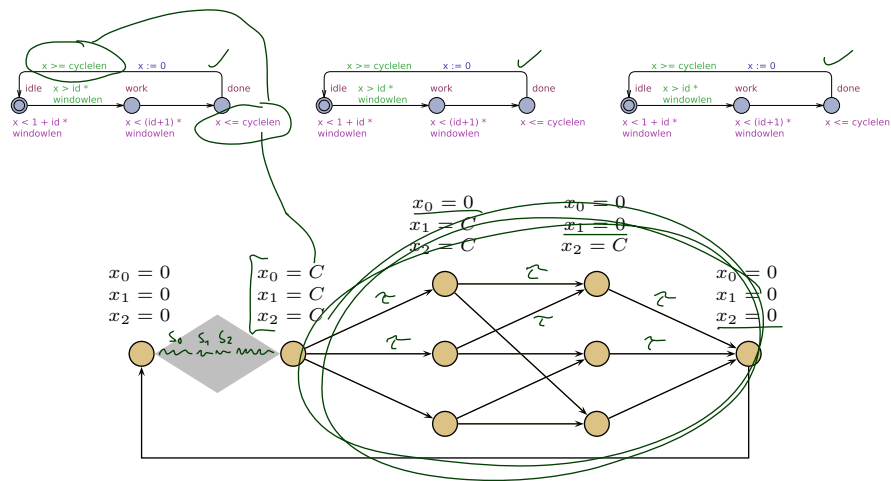
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## Experiments (A[] not deadlock)



## A Closer Look

- Option 1: well, that's exponential space complexity, we need to accept that.
- Option 2: take a closer look.



- Quasi-Equal Clocks
  - Definition, Properties
- QE Clock Reduction
  - The simple, and wrong approach
  - Transformation example
  - Experiments
  - Simple and Complex Edges
  - Transformation schemes
- Correctness of the Transformation
- Excursion: Bisimulation Proofs
- Proof of QE-Correctness
  - a particular weak bisimulation relation
- More Experiments

## Quasi-Equal Clocks

**Definition.** Let  $\mathcal{N}$  be a network of timed automata with clocks  $X$ . Two clocks  $x, y \in X$  are called **quasi equal**, denoted by  $x \simeq y$ , if and only if, for all reachable configurations of  $\mathcal{N}$ ,  $x$  and  $y$  are equal or at least one has value 0, i.e.

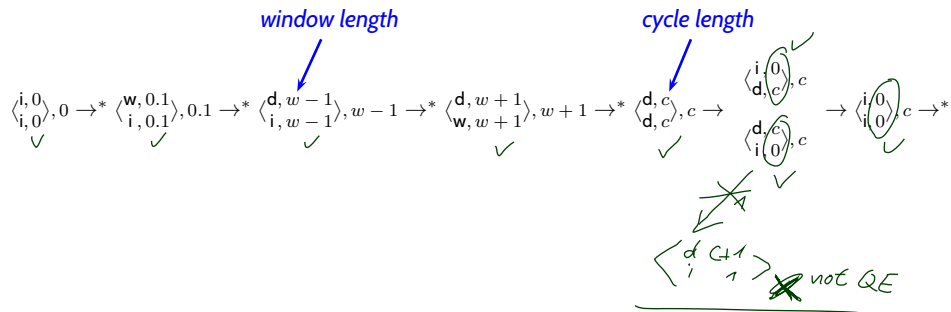
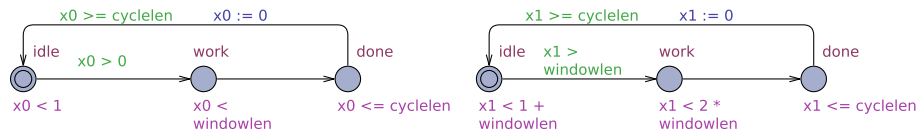
$$\forall \langle \vec{\ell}_0, \nu_0 \rangle, t_0 \xrightarrow{\lambda_1} \langle \vec{\ell}_1, \nu_1 \rangle, t_1 \dots \in \text{Paths}(\mathcal{N}) \forall i \in \mathbb{N}_0 \bullet \\ \nu_i \models (x = y \vee x = 0 \vee y = 0).$$

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## Example

$$\forall \langle \vec{\ell}_0, \nu_0 \rangle, t_0 \dots \in \text{Paths}(\mathcal{N}) \forall i \in \mathbb{N}_0 \bullet \nu_i \models (x = y \vee x = 0 \vee y = 0).$$



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$$\forall \langle \vec{\ell}_0, \nu_0 \rangle, t_0 \dots \in \text{Paths}(\mathcal{N}) \forall i \in \mathbb{N}_0 \bullet \nu_i \models (x = y \vee x = 0 \vee y = 0).$$

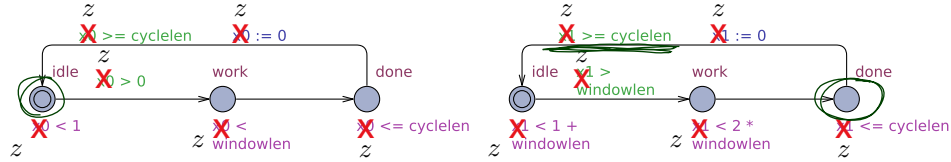
**Lemma.** Quasi-Equality is an equivalence relation.

**Proof:**

- **reflexive:** obvious.
- **symmetric:** obvious.
- **transitive:** a bit tricky  
(induction over a stronger property).

## Quasi-Equal Clock Reduction

## Idea: Use Just One Clock



### Behaviour:

$$\langle i, 0 \rangle, 0 \rightarrow^* \langle w, 0.1 \rangle, 0.1 \rightarrow^* \langle d, c \rangle, c \rightarrow \begin{cases} \langle i, 0 \rangle, c \\ \langle d, 0 \rangle, c \end{cases}$$

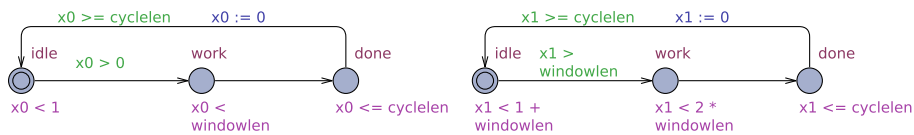
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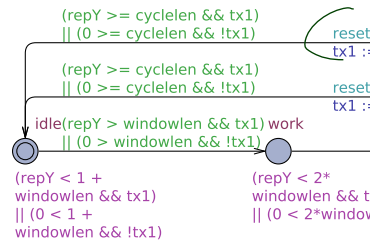
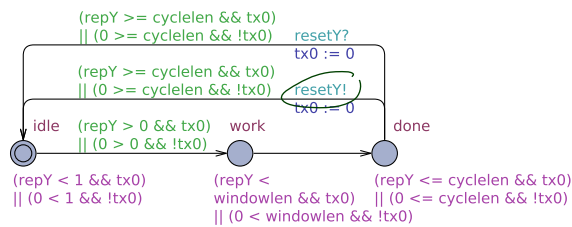
## A More Elaborate Transformation

$$\mathcal{V} = \{x, y\}, x \approx y$$

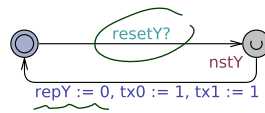
$$\in \Diamond x0 \neq x1$$



broadcast channel  $\text{resetY}$ ;



clock  $\text{repY}$ ;  
bool  $\text{tx} = 1$ ;  
bool  $\text{tx} = 1$ ;



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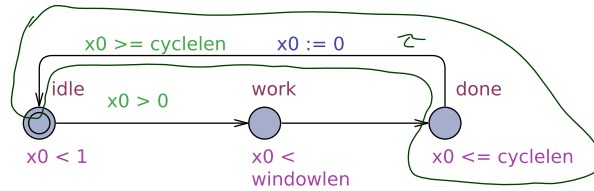




**Definition.** An edge  $e = (\ell, \alpha, \varphi, \vec{r}, \ell')$  resetting at least one quasi-equal clock is called simple edge if and only if the following conditions are satisfied:

- (i)  $\alpha = \tau$ ,  $\varphi \equiv x \geq c$ ,  $\vec{r} = \langle x := 0 \rangle$ ,  
for some constant  $c$  and **local clock**  $x$ ,
- (ii)  $I(\ell) = x \leq c$ ,
- (iii)  $e$  is **pre-** and **post-delayed**, and
- (iv)  $e$  is the only edge with source  $\ell$ .

Otherwise  $e$  is called complex edge.



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## Transformation Scheme: Variables and Channels

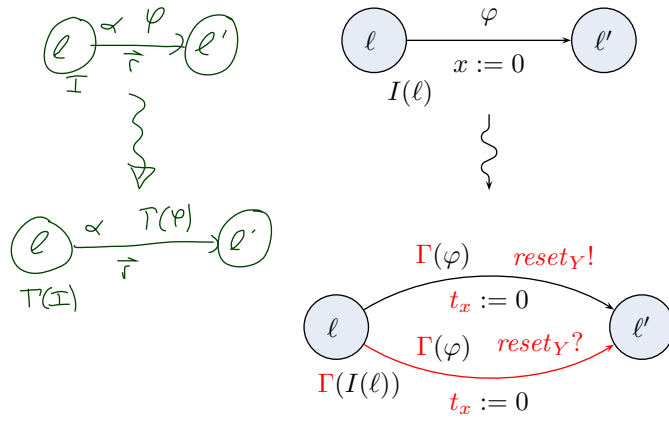
Given a network  $\mathcal{N}$  of timed automata, the **variables and channels** of **QE-transformation** of  $\mathcal{N}'$  are obtained by the following procedure:

- **remove** all quasi-equal clocks from  $\mathcal{N}$ ,
- for each **equivalence class** of quasi-equal clocks  $Y$ ,  
    **add** a fresh clock  $x_Y$  to  $\mathcal{N}'$  *~ reply*
- **add** a fresh boolean variable  $t_x$  to  $\mathcal{N}'$   
    for each quasi-equal clock  $x$  in  $\mathcal{N}$ ,  
    initial value:  $t_x := 1$ ,
- **add** a fresh channel  $reset_Y$  to  $\mathcal{N}'$ .  
    *broadcast*

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## Transformation Scheme (for Simple Edges)



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## Constraint Transformation $\Gamma$

**Definition.** Let  $\mathcal{N}$  be a network. Let  $Y, W \in \mathcal{EC}_{\mathcal{N}}$  be sets of quasi-equal clocks of  $\mathcal{N}$ ,  $x \in Y$  and  $y \in W$  clocks.

Given a clock constraint  $\varphi_{clk}$ , we define:

$$\Gamma_0(\varphi_{clk}) := \begin{cases} ((x_Y \sim c \wedge t_x) \vee (0 \sim c \wedge \neg t_x)) & , \text{ if } \varphi_{clk} = x \sim c, \\ ((x_Y - x_W \sim c \wedge t_x \wedge t_y) \vee (0 - x_W \sim c \wedge \neg t_x \wedge t_y) \vee (x_Y - 0 \sim c \wedge t_x \wedge \neg t_y) \vee (0 \sim c \wedge \neg t_x \wedge \neg t_y)) & , \text{ if } \varphi_{clk} = x - y \sim c, \\ \Gamma_0(\varphi_1) \wedge \Gamma_0(\varphi_2) & , \text{ if } \varphi_{clk} = \varphi_1 \wedge \varphi_2. \end{cases}$$

Then  $\Gamma(\varphi_{clk} \wedge \psi_{int}) := \Gamma_0(\varphi_{clk}) \wedge \psi_{int}$ .

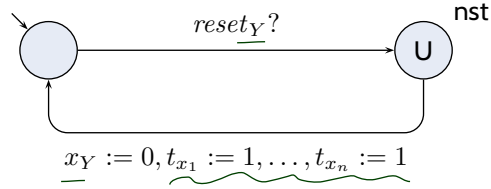
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Here,  $\mathcal{EC}_{\mathcal{N}}$  is the set of **equivalence classes** of **quasi-equal clocks** in  $\mathcal{N}$ .

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## Resetter Construction (for Simple Edges)

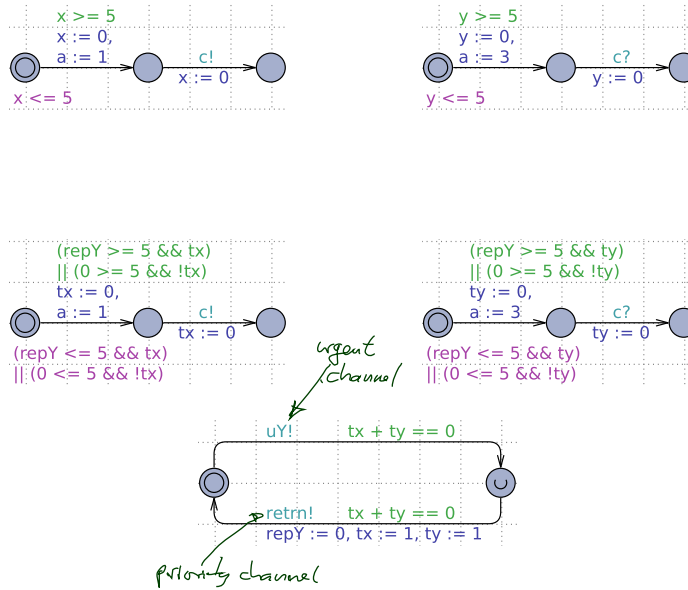
- For each equivalence class  $\underline{Y} = \{x_1, \dots, x_n\} \in \mathcal{EC}_{\mathcal{N}}$  add a **resetter**  $\mathcal{R}_Y$  to  $\mathcal{N}'$ :



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## Transformation Example (for Complex Edges)



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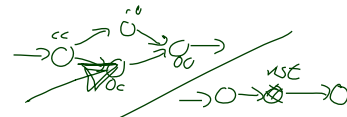
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## Correctness of the Transformation

### QE-Transformation Correctness

**Theorem.** Let  $\mathcal{N}$  be a network of timed automata and  $CF$  a configuration formula over  $\mathcal{N}$ . Then

$$\underbrace{\mathcal{N} \models \exists \Diamond CF} \iff \underbrace{\mathcal{N}' \models \exists \Diamond \Omega(CF)}.$$



**Definition.** Let  $\mathcal{N} = \{\mathcal{A}_1, \dots, \mathcal{A}_n\}$  be a network with equivalence classes of quasi-equal clocks  $\mathcal{EC}_{\mathcal{N}} = \{Y_1, \dots, Y_m\}$  and  $\beta$  a basic formula over  $\mathcal{N}$ .

$$\Omega_0(\beta) = \begin{cases} \ell \vee (\ell' \wedge \tilde{x}) & , \text{ if } \beta = \mathcal{A}_i.\ell, \quad (\ell, \alpha, \varphi, \langle x := 0 \rangle, \ell') \text{ simple.} \\ (\ell' \wedge \neg \tilde{x}) & , \text{ if } \beta = \mathcal{A}_i.\ell', \quad (\ell, \alpha, \varphi, \langle x := 0 \rangle, \ell') \text{ simple.} \\ \beta & , \text{ if } \beta \in \{\mathcal{A}_i.\ell, \mathcal{A}_i.\ell'\}, \quad (\ell, \alpha, \varphi, \vec{r}, \ell') \text{ not simple.} \\ \Gamma(\beta)[t_x / (t_x \vee \tilde{x}) \mid x \in Y, Y \in \mathcal{EC}_{\mathcal{N}}] & , \text{ if } \beta = \varphi_{clk} \wedge \varphi_{int}. \end{cases}$$

$\Omega(CF) = \exists \tilde{x}_1, \dots, \tilde{x}_{|X(\mathcal{N})|} \bullet \Omega_0(CF) \wedge \kappa_{\mathcal{N}}$ , where

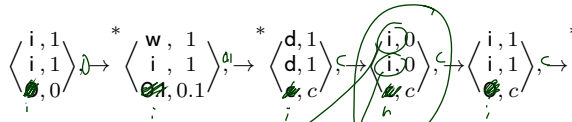
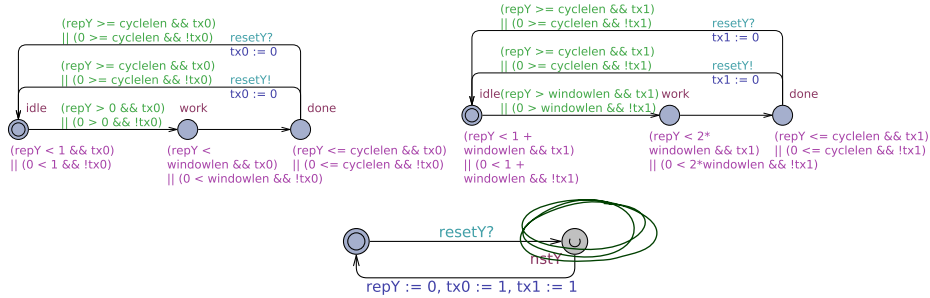
$$\kappa_{\mathcal{N}} := \bigwedge_{\substack{1 \leq i \leq n, \\ 1 \leq j \leq m, \\ (\ell, \alpha, \varphi, \langle x := 0 \rangle, \ell') \in \text{SimpEdges}_{Y_j}(\mathcal{A}_i)}} \kappa(x), \quad \kappa(x) : (\tilde{x} \implies \bigvee_{(\ell, \alpha, \varphi, \langle x := 0 \rangle, \ell') \in \text{SimpEdges}_{Y_j}(\mathcal{A}_i)} \ell' \wedge \ell_{nst\mathcal{R}_{Y_j}}).$$

By structural induction  $\Omega_0$  transforms configuration formulas  $CF$ .

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## Example

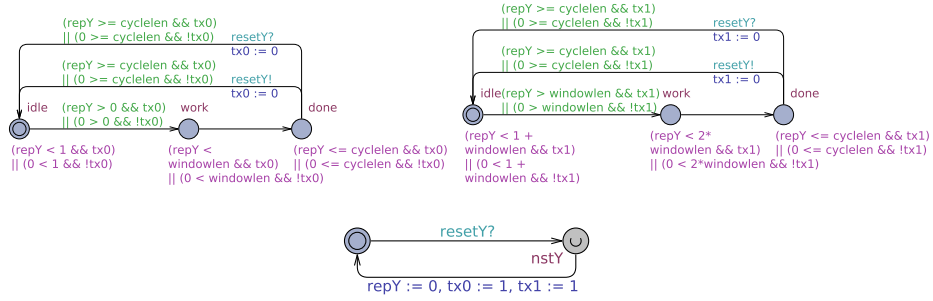


- $\mathcal{N} \models \exists \Diamond \text{S0.idle} \wedge \text{S1.done}$
- $\mathcal{N}' \models \exists \Diamond (\exists \tilde{x}_0, \tilde{x}_1 \bullet (\text{S0.idle} \wedge \neg \tilde{x}_0) \wedge (\text{S1.done} \vee (\text{S1.idle} \wedge \tilde{x}_1)) \wedge (\tilde{x}_0 \implies (\text{S0.idle} \wedge \text{nst})) \wedge (\tilde{x}_1 \implies (\text{S1.idle} \wedge \text{nst})))$

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## Example



$$\langle \begin{smallmatrix} i, 1 \\ 0, 0 \end{smallmatrix} \rangle \xrightarrow{*} \langle \begin{smallmatrix} w, 1 \\ 0.1, 0.1 \end{smallmatrix} \rangle \xrightarrow{*} \langle \begin{smallmatrix} d, 1 \\ c, c \end{smallmatrix} \rangle \xrightarrow{*} \langle \begin{smallmatrix} i, 0 \\ c, c \end{smallmatrix} \rangle \xrightarrow{*} \langle \begin{smallmatrix} i, 1 \\ 0, c \end{smallmatrix} \rangle \xrightarrow{*}$$

- $\mathcal{N} \models \exists \Diamond (x_0 = 0) \wedge x_1 > 0$
- $\mathcal{N}' \models \exists \Diamond (\exists \tilde{x}_0, \tilde{x}_1 \bullet ((x_Y = 0 \wedge (t_{x_0} \vee \tilde{x}_0)) \vee (0 = 0 \wedge \neg(t_{x_0} \vee \tilde{x}_0))) \wedge ((x_Y > 0 \wedge (t_{x_1} \vee \tilde{x}_1)) \vee (0 > 0 \wedge \neg(t_{x_1} \vee \tilde{x}_1)))) \wedge (\tilde{x}_0 \implies (S0.idle \wedge nst)) \wedge (\tilde{x}_1 \implies (S1.idle \wedge nst)))$

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## Bisimulation Proofs

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## Proof Sketch

- Use a **weak bisimulation relation** – the basic idea:
  - Let  $\mathcal{T}_i = (Conf_i, \Lambda_i, \{\xrightarrow{\lambda} \mid \lambda \in \Lambda_i\}, C_{ini,i}), i = 1, 2$ ,  
be labelled transition systems with (for simplicity)  $C_{ini,i} = \{c_{ini,i}\}$ .
  - A relation  $R \subseteq Conf_1 \times Conf_2$  is called **weak bisimulation** if and only if
    - (i) the **initial configurations** are related, i.e.  $(c_{ini,1}, c_{ini,2}) \in R$ ,
    - (ii) two related configurations **satisfy the same terms**, i.e.

$$\forall c_1, c_2, term \bullet (c_1, c_2) \in R \implies (c_1 \models term \iff c_2 \models term)$$

- (iii) given two related configurations  $(c_1, c_2) \in R$ ,
  - a) if  $\mathcal{T}_1$  has a  $\lambda$ -transition from  $c_1$  to some  $c'_1$ ,  
then  $\mathcal{T}_2$  has  $\tau$ - and  $\lambda$ -transitions from  $c_2$  to a related  $c'_2$ , i.e.

$$\forall c'_1 \bullet c_1 \xrightarrow{\lambda} c'_1 \implies \exists c'_2 \bullet c_2 \xrightarrow{\lambda^*} c'_2 \wedge (c'_1, c'_2) \in R$$

- b) similarly for  $\mathcal{T}_2$  to  $\mathcal{T}_1$ , i.e.

$$\forall c'_2 \bullet c_2 \xrightarrow{\lambda} c'_2 \implies \exists c'_1 \bullet c_1 \xrightarrow{\lambda^*} c'_1 \wedge (c'_1, c'_2) \in R$$

- $\mathcal{T}_1$  and  $\mathcal{T}_2$  are called **weakly bisimilar** iff there exists a weak bisimulation for  $\mathcal{T}_1, \mathcal{T}_2$ .

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## Once Again

- (i)  $(c_{ini,1}, c_{ini,2}) \in R$ ,
- (ii)  $\forall c_1, c_2, term \bullet (c_1, c_2) \in R \implies (c_1 \models term \iff c_2 \models term)$
- (iii) for all  $(c_1, c_2) \in R$ ,
  - a) " $\mathcal{T}_2$  can simulate transitions of  $\mathcal{T}_1$ ":

$$\begin{array}{ccc} c_1 & \xrightarrow{\lambda} & c'_1 \\ R \downarrow & \implies & \downarrow R \\ c_2 & & c_2 \xrightarrow{\lambda^*} c'_2 \end{array}$$

(using any finite number of  $\tau$ -transitions in between)

- b) " $\mathcal{T}_1$  can simulate transitions of  $\mathcal{T}_2$ ":

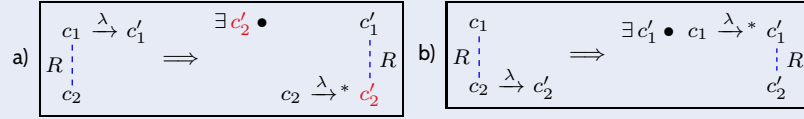
$$\begin{array}{ccc} c_1 & & c'_1 \\ R \downarrow & \implies & \downarrow R \\ c_2 & \xrightarrow{\lambda} & c'_2 \end{array}$$

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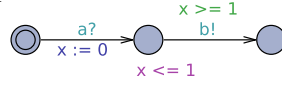
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## Example

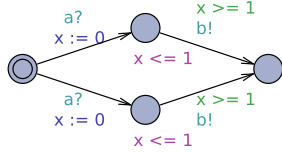
- (i)  $(c_{ini,1}, c_{ini,2}) \in R$ , (ii)  $\forall c_1, c_2, term \bullet (c_1, c_2) \in R \implies (c_1 \models term \iff c_2 \models term)$   
 (iii) for all  $(c_1, c_2) \in R$ ,



$\mathcal{A}_1$ :

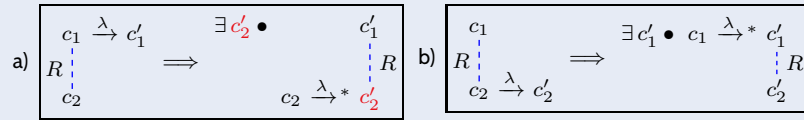


$\mathcal{A}_2$ :

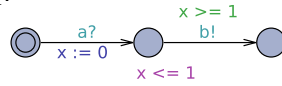


## Example

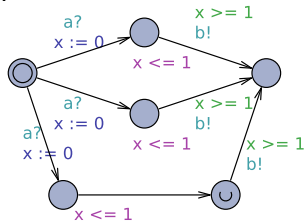
- (i)  $(c_{ini,1}, c_{ini,2}) \in R$ , (ii)  $\forall c_1, c_2, term \bullet (c_1, c_2) \in R \implies (c_1 \models term \iff c_2 \models term)$   
 (iii) for all  $(c_1, c_2) \in R$ ,



$\mathcal{A}_1$ :

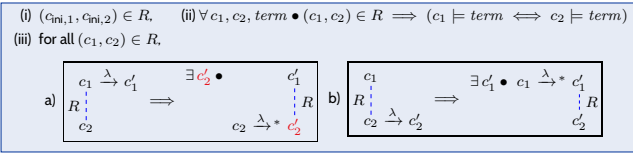


$\mathcal{A}_3$ :





## What is It Good For?



- Let  $term$  be a term over two weakly bisimilar networks  $\mathcal{N}$  and  $\mathcal{N}'$ .
- Claim:**  $\mathcal{N} \models \exists \Diamond term \iff \mathcal{N}' \models \exists \Diamond term$ .
- Proof:**
  - Because  $\mathcal{N}$  and  $\mathcal{N}'$  are weakly bisimilar, there is a **simulation relation**  $R$ .
  - Direction " $\implies$ ": Let  $\mathcal{N} \models \exists \Diamond term$ .
    - Thus there is a **computation path**  $c_{1,0} \xrightarrow{\lambda_1} c_{1,1} \xrightarrow{\lambda_2} \dots \xrightarrow{\lambda_n} c_{1,n}$  with  $c_{1,n} \models term$ .
    - Induction over length of path:**
      - Case  $n = 0$ :**  
Then  $c_{1,0} \models term$  and  $c_{0,1}$  is an initial configuration, thus  $c_{2,0}$  is  $R$ -related (by (i)) and thus  $c_{2,0} \models term$  (by (ii)).
      - Case  $n \rightarrow n + 1$ :**  
For the path  $c_{1,0} \xrightarrow{\lambda_1} \dots \xrightarrow{\lambda_n} c_{1,n} \xrightarrow{\lambda_{n+1}} c_{1,n+1}$ , there is (by **induction hypothesis**) an  $R$ -related configuration  $c_{2,m}$ ,  $m \geq n$ , **reachable** in  $\mathcal{N}'$ .  
By (iii).a), there is a configuration  $c'_{2,m}$ , which is  $R$ -related to  $c_{1,n+1}$ , and **reachable** from  $c_{2,m}$ , thus, by (ii),  $c_{1,n+1} \models term$ .
  - Direction " $\impliedby$ ": similar.

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## Proof of QE-Correctness

**Definition. [Weak Bisimulation]**

Networks  $\mathcal{N}, \mathcal{N}'$  are called **weakly bisimilar** if and only if there is a **weak bisimulation relation**  $QE \subseteq \text{Conf}(\mathcal{N}) \times \text{Conf}(\mathcal{N}')$  such that:

- (i)  $\forall s \in \underline{C_{ini}}(\mathcal{N}) \exists r \in \underline{C_{ini}}(\mathcal{N}') \bullet (s, r) \in QE,$   
 $\forall r \in \underline{C_{ini}}(\mathcal{N}') \exists s \in \underline{C_{ini}}(\mathcal{N}) \bullet (s, r) \in QE$
- (ii)  $\forall CF \in \mathcal{CF}_{\mathcal{N}} \forall (s, r) \in QE \bullet s \models_{\delta} CF \implies r \models_{\delta} \Omega(CF).$
- (iii)  $\forall CF \in \mathcal{CF}_{\mathcal{N}} \forall (s, r) \in QE \bullet$   
 $r \models_{\delta} \Omega(CF) \implies \exists \dot{s} \in \text{Conf}(\mathcal{N}) \bullet (\dot{s}, r) \in QE \wedge \dot{s} \models_{\delta} CF.$
- (iv)  $\forall (s, r) \in QE \forall \lambda, s' \bullet s \xrightarrow{\lambda} s' \implies \exists r' \bullet r \xrightarrow{\lambda^*} r' \wedge (s', r') \in QE$
- (v)  $\forall CF \in \mathcal{CF}_{\mathcal{N}} \forall (s, r) \in QE \forall \lambda, r' \bullet$   
 $r \xrightarrow{\lambda} r' \wedge r' \models_{\delta'} \Omega_0(CF) \implies \exists s' \bullet s \xrightarrow{\lambda^*} s' \wedge (s', r') \in QE.$

Here,  $r \xrightarrow{\tau^*} r'$  denotes zero or more successive  $\tau$ -transitions from  $r$  to  $r'$ .

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*A Weak Bisimulation Relation for QE-Transformation*

- Let  $\mathcal{N}$  be a network of timed automata and  $\mathcal{N}'$  the network obtained by QE-transformation of  $\mathcal{N}$ . Then  $QE : \text{Conf}(\mathcal{N}) \rightarrow 2^{\text{Conf}(\mathcal{N}'})$  defined as follows is a **weak bisimulation relation**.

$$QE((\vec{\ell}_{\dot{s}}, \nu_{\dot{s}})) = \left\{ r = \langle (\ell_{r,1}, \dots, \ell_{r,n}, \ell_{\mathcal{R}_{Y_1}}, \dots, \ell_{\mathcal{R}_{Y_m}}), \nu_r \rangle \mid \right.$$

$$(\forall x \in V(\mathcal{N}) \bullet \underline{\nu_r(x) = \nu_{\dot{s}}(x)}) \quad (6.2.1)$$

$$\wedge \forall 1 \leq i \leq n \bullet \quad (6.2.2)$$

$$\left( \left( \underline{\ell_{r,i} = \ell_{\dot{s},i}} \wedge \forall x \in X(\mathcal{A}_i) \bullet \underline{\nu_{\dot{s}}(x) = \nu_r(x_x) \cdot \nu_r(t_x)} \right) \right. \quad (6.2.2a)$$

$$\left. \left( \bigvee \left( \exists (\ell, \alpha, \varphi, \langle x := 0 \rangle, \ell') \in \text{SimpEdges}_Y(\mathcal{A}_i) \bullet \underline{\ell_{\mathcal{R}_Y} \neq \ell_{ini\mathcal{R}_Y}} \wedge \right. \right. \right.$$

$$\left. \left. \underline{\ell_{\dot{s},i} = \ell} \wedge \underline{\ell_{r,i} = \ell'} \wedge \nu_{\dot{s}}(x) = \nu_r(x_x) \wedge \nu_r(t_x) = 0 \wedge \right. \right.$$

$$\left. \left. \forall y \in X(\mathcal{A}_i) \setminus \{x\} \bullet \nu_{\dot{s}}(y) = \nu_r(x_y) \cdot \nu_r(t_y) \right) \right) \quad (6.2.2b)$$

$$\wedge \forall Y \in \mathcal{EC}_{\mathcal{N}} \bullet$$

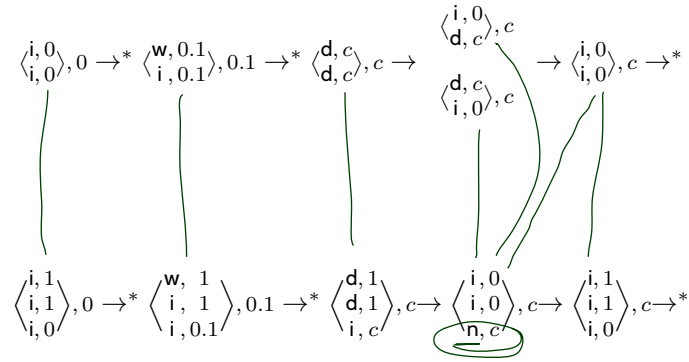
$$\left( \nu_r(s_Y^{A_i}) = 1 \iff \exists (\ell, \alpha, \varphi, \vec{r}, \ell') \in \text{SimpEdges}_Y(\mathcal{A}_i) \bullet \ell_{r,i} = \ell \right) \quad (6.2.3)$$

$$\wedge \nu_r(\text{prio}_Y) = 1 \iff (\ell_{r,\mathcal{R}_Y} = \ell_{nst\mathcal{R}_Y}) \Big\} \quad (6.2.4)$$

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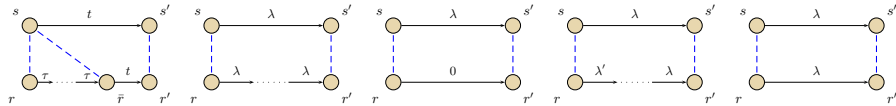
## Example



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## Proof of Having Indeed a Bisimulation

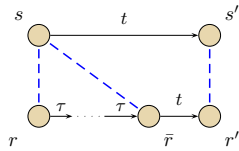


- $s \xrightarrow{\lambda} s'$  to  $r \xrightarrow{\lambda'} r'$ :

**Cases:**

- delay  $d > 0$ :

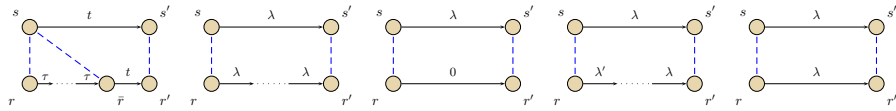
resetter may need to go back to idle, then do same delay.



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## Proof of Having Indeed a Bisimulation

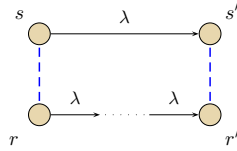


- $s \xrightarrow{\lambda} s'$  to  $r \xrightarrow{\lambda^*} r'$ :

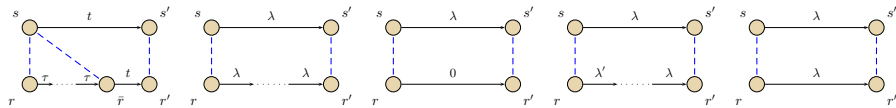
### Cases:

- delay  $d > 0$
- first simple edge:

first simple edges  
pushes resetter and  
all other simples.



## Proof of Having Indeed a Bisimulation

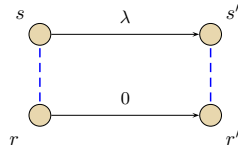


- $s \xrightarrow{\lambda} s'$  to  $r \xrightarrow{\lambda^*} r'$ :

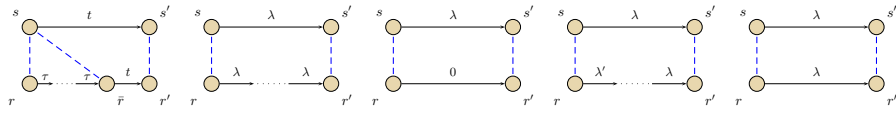
### Cases:

- delay  $d > 0$
- first simple edge
- other simple edge:

resetter is in  
nst, do nothing  
in  $\mathcal{N}'$ .



## Proof of Having Indeed a Bisimulation

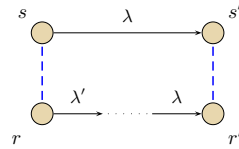


- $s \xrightarrow{\lambda} s'$  to  $r \xrightarrow{\lambda^*} r'$ :

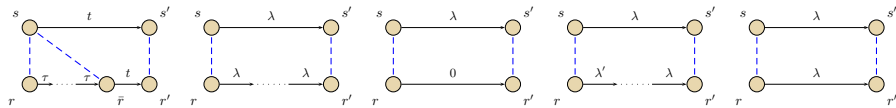
### Cases:

- delay  $d > 0$
- first simple edge
- other simple edge
- non-reset, or at least one complex:

resetter may need  
to push simples  
first, then take  
same edge in  $\mathcal{N}'$ .



## Proof of Having Indeed a Bisimulation

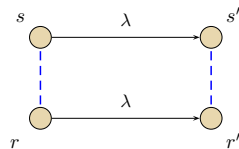


- $s \xrightarrow{\lambda} s'$  to  $r \xrightarrow{\lambda^*} r'$ :

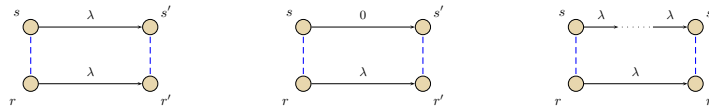
### Cases:

- delay  $d > 0$
- first simple edge
- other simple edge
- non-reset, or at least one complex
- delay  $d = 0$ :

do same  
delay in  $\mathcal{N}'$ .



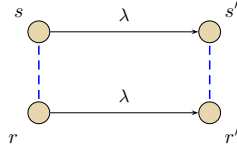
## Proof of Having Indeed a Bisimulation



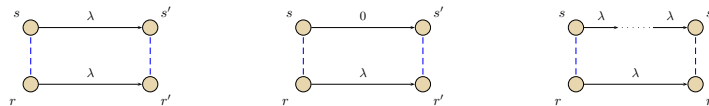
- $r \xrightarrow{\lambda} r'$  to  $s \xrightarrow{\lambda^*} s'$ :

**Cases:**

- delay  $d > 0$ :  
do same delay  
in  $\mathcal{N}$ .



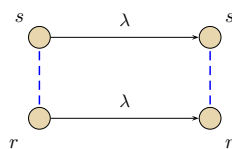
## Proof of Having Indeed a Bisimulation



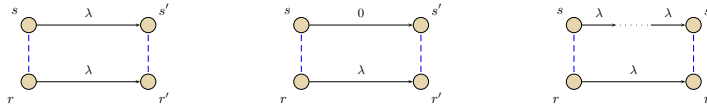
- $r \xrightarrow{\lambda} r'$  to  $s \xrightarrow{\lambda^*} s'$ :

**Cases:**

- delay  $d > 0$
- complex, or non-resetting:  
take same edge  
in  $\mathcal{N}$ .



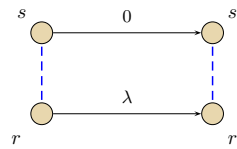
## Proof of Having Indeed a Bisimulation



- $r \xrightarrow{\lambda} r'$  to  $s \xrightarrow{\lambda^*} s'$ :

### Cases:

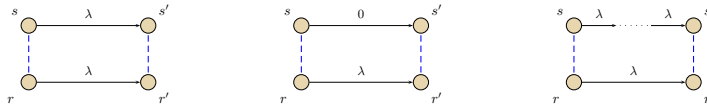
- delay  $d > 0$
- complex, or non-resetting
- resetter to nst,  
or returns (no simples enab. in  $\mathcal{N}$ ):  
do nothing  
in  $\mathcal{N}$ .



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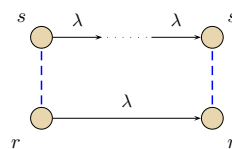
## Proof of Having Indeed a Bisimulation



- $r \xrightarrow{\lambda} r'$  to  $s \xrightarrow{\lambda^*} s'$ :

### Cases:

- delay  $d > 0$
- complex, or non-resetting
- resetter to nst,  
or returns (no simples enab. in  $\mathcal{N}$ )
- resetter returns (some simples enab. in  $\mathcal{N}$ ):  
take all enabled  
simple edges in  $\mathcal{N}$ .



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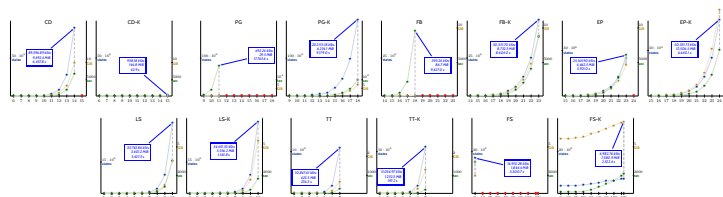
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## More Experiments

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## Case Studies

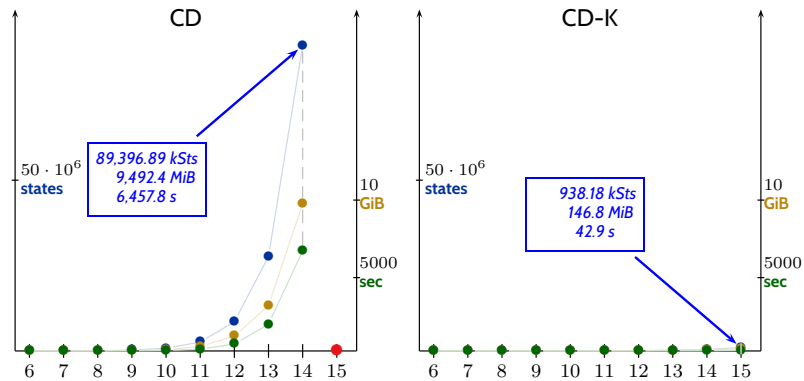
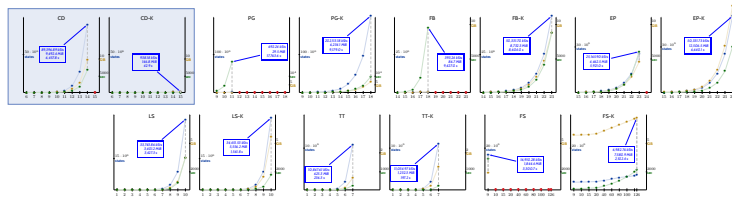


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## Case Studies

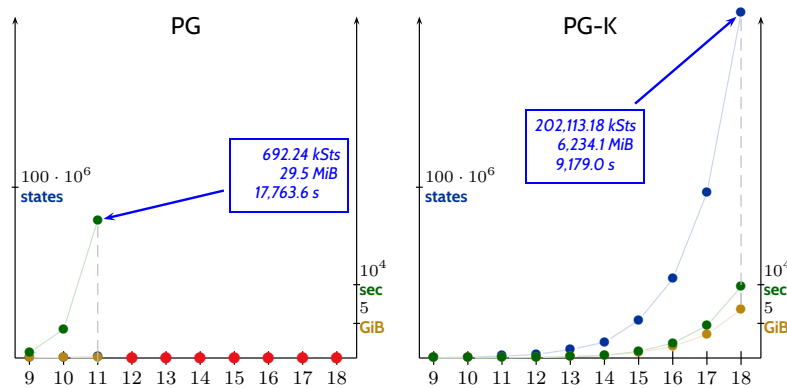
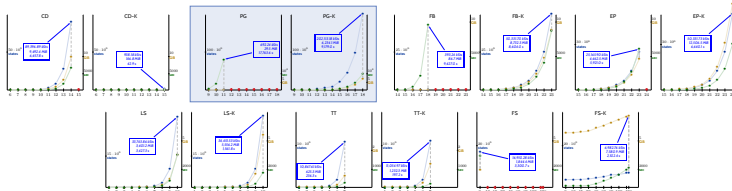


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## Case Studies

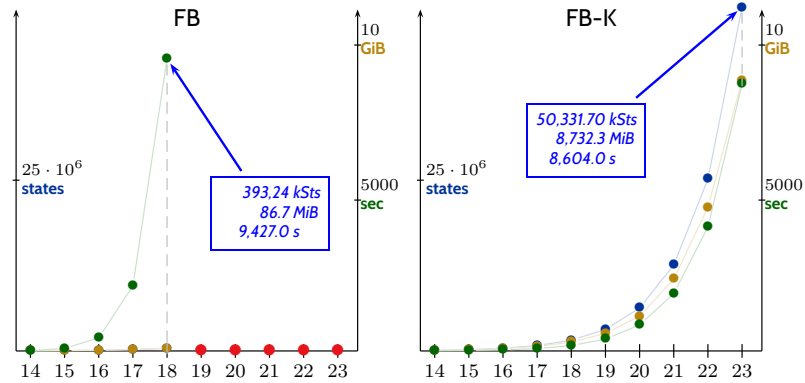
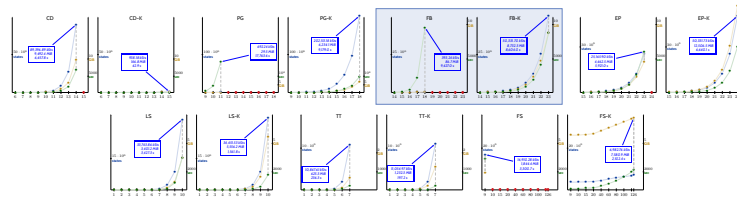


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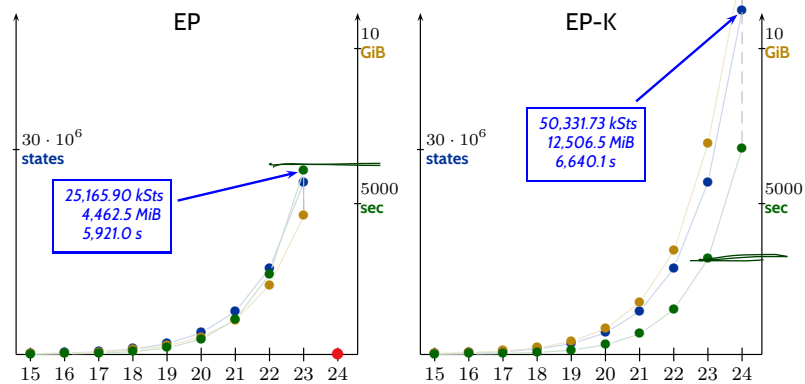
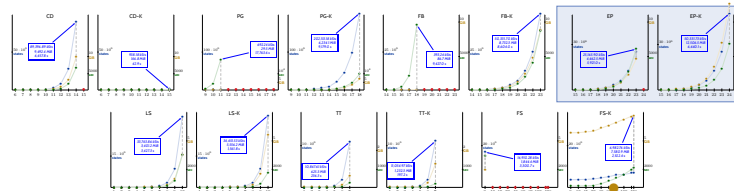
## Case Studies



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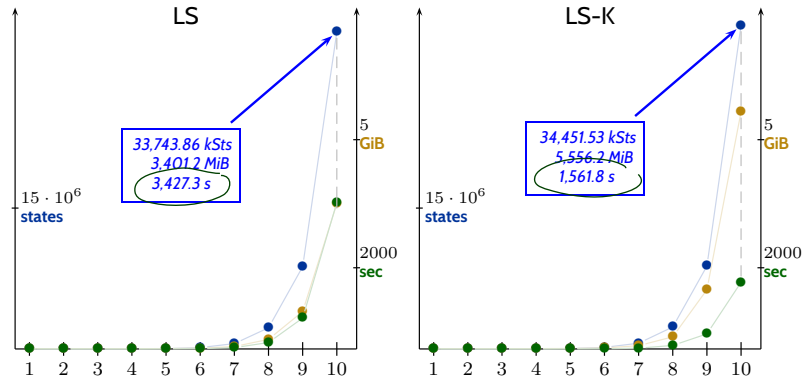
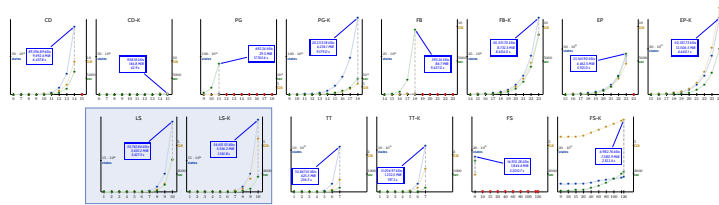
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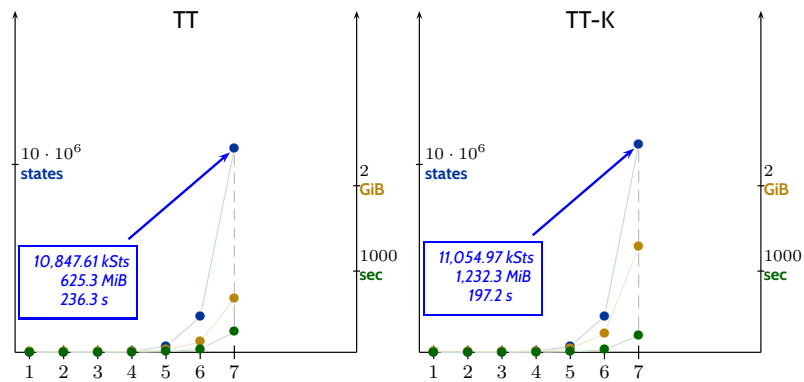
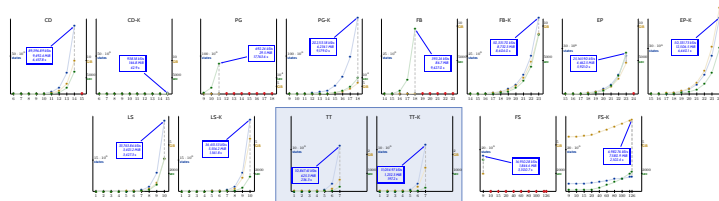
## Case Studies



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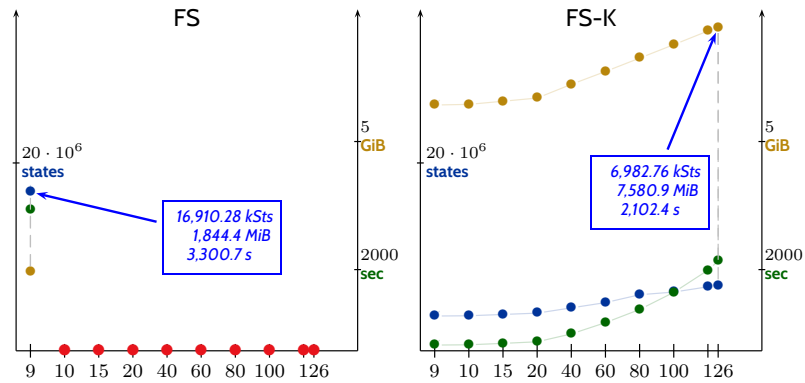
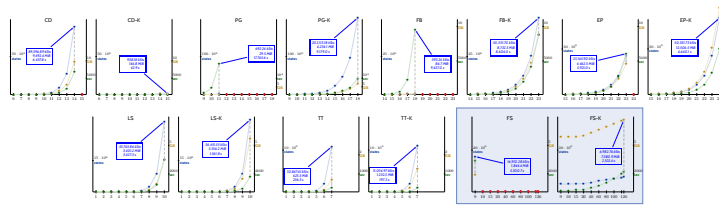
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## Savings

## Upper Bound on Number of Configurations

**Theorem.** Let  $\mathcal{N}$  be a network of timed automata with equivalence classes of quasi-equal clocks  $\mathcal{EC}_{\mathcal{N}} = \{Y_1, \dots, Y_m\}$ .

Then the number of configurations of  $\mathcal{N}'$  is bounded above by:

$$|L(\mathcal{A}_1) \times \dots \times L(\mathcal{A}_n) \times L(\mathcal{R}_{Y_1}) \times \dots \times L(\mathcal{R}_{Y_m})| \\ \cdot (2c+2)^{|\mathcal{EC}_{\mathcal{N}}|} \cdot (4c+3)^{\frac{1}{2}|\mathcal{EC}_{\mathcal{N}}| \cdot (|\mathcal{EC}_{\mathcal{N}}|-1)} \\ \cdot 2^{|Y_1 \cup \dots \cup Y_m|},$$

where  $c = \max\{c_x \mid x \in \mathcal{X}(\mathcal{N})\}$ .

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## Only Simple Edges

**Lemma.** Let  $\mathcal{N}$  be a network of timed automata with a set of equivalence classes of quasi-equal clocks  $\mathcal{EC}_{\mathcal{N}}$ , where

- $|Y| \geq 2, Y \in \mathcal{EC}_{\mathcal{N}}$ , and
- each clock  $x \in Y, Y \in \mathcal{EC}_{\mathcal{N}}$ , is **exclusively reset by simple edges**.

Then  $|Reach_{\mathcal{N}'}| < |Reach_{\mathcal{N}}|$ .

(Here,  $Reach_{\mathcal{N}}$  denotes the set of all reachable (zone graph-)configurations of  $\mathcal{N}$ .)

**Proof:** Use the following lemma.

**Lemma.** Let  $\mathcal{N}$  be a network where all quasi-equal clocks are exclusively reset by simple edges. Then

$$|Reach_{\mathcal{N}'}| = |Reach_{\mathcal{N}}| - \left( \sum_{s \in RC} 2^{|cls(s)|} \right) + \sum_{s \in RC} \left[ |class(s)| + 2 \right].$$

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- “it’s (a bit more) complicated”

## Content

- Quasi-Equal Clocks
  - Definition, Properties
- QE Clock Reduction
  - The simple, and wrong approach
  - Transformation example
  - Experiments
  - Simple and Complex Edges
  - Transformation schemes
- Correctness of the Transformation
- Excursion: Bisimulation Proofs
- Proof of QE-Correctness
  - a particular weak bisimulation relation
- More Experiments
- Savings

- The **space complexity** of Pure-TA reachability-checking is

$$L_1 \times \cdots \times L_n \times \text{Regions}(X),$$

i.e., exponential in number of clocks, and **of TA**.

- If a model is **expensive to check**,
  - it may necessarily be that expensive,
  - or artificially / non-necessarily.→ take a closer look (→ exercises).
- One example: **Quasi-equal clocks**
  - advantage: **can be good for validation**,
  - dis-advantage: **expensive to check**.
- The **QE transformation** (source-to-source)
  - **eliminates interleavings of simple edges**,
  - reduces DBM size to **(number of equiv. classes)<sup>2</sup>**,
  - **reflects** all queries.

## *References*

## References

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