Interpolation-based Software Verification with WOLVERINE*

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Abstract. Wolverine is a software verification tool using Craig interpolation to compute invariants of ANSI-C and C++ programs. The tool is an implementation of the *lazy abstraction* approach, generating a reachability tree by unwinding the transition relation of the input program and annotating its nodes with interpolants representing safe states. Wolverine features a built-in interpolating decision procedure for equality logic with uninterpreted functions which provides limited support for bit-vector operations. In addition, it provides an API enabling the integration of other interpolating decision procedures, making it a valuable source of benchmarks and allowing it to take advantage of the continuous performance improvements of SMT solvers. We evaluate the performance of Wolverine by comparing it to the predicate abstraction-based verifier Satabs on a number of verification conditions of Linux device drivers.

1 Introduction

The last decade has seen significant progress in the area of automated software verification, manifesting itself in a number of impressive verification tools. A recent and comprehensive survey of software verification techniques is provided in [1] and a comparison of verification tools can be found in [2]. One approach that received particular attention is predicate abstraction [3], a technique that constructs a conservative abstraction of the original program using a finite set of first-order-logic predicates to track relevant facts about the program variables.

The performance of such predicate abstraction-based software model checkers is contingent on suitable predicates. Contemporary verification tools (e.g., Microsoft's Slam [4]) derive these predicates from spurious counterexamples in an iterative manner [5,6]. Recent incarnations of this technique (such as Blast [7]) rely on Craig interpolation to derive predicates, taking advantage of the inherent properties of interpolants which enable concise abstractions.

Some flavours of this interpolation-based abstraction mechanism avoid the use of predicate abstraction to construct an abstract transition relation altogether [8–10]. This omission has several advantages:

^{*} Supported by the EU FP7 STREP MOGENTES and a gift from the Intel Labs Academic Research Office.

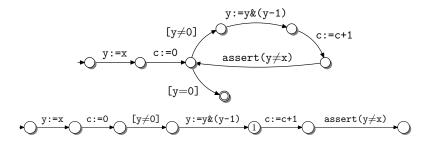


Fig. 1. A program and one of its execution traces

- It eliminates computationally expensive calls to a theorem prover that predicate abstraction-based verifiers require to construct an abstraction, and
- it decreases the size and the complexity of the implementation of the verification tool significantly (by about two thirds in our experience).

The algorithm presented in [8] (and our implementation WOLVERINE) essentially follows the *lazy abstraction* paradigm [11], but retains the abstract transition function at the coarsest level (determined by the control-flow-graph of the program under scrutiny). The reachability tree obtained by unwinding this transition function is annotated with interpolants representing an overapproximation of the reachable states. A fixed point of these annotations constitutes an invariant establishing the correctness of the program with respect to a given safety property. Section 2 provides more details.

Contributions. WOLVERINE is a software verification tool checking reachability properties stated in terms of assertions and implements the algorithm described in [8]. It is, to the best of our knowledge, the first freely available³ software model checker for C/C++ programs based on this algorithm.

Wolverine features a built-in interpolating decision procedure for equality logic with uninterpreted functions which provides limited support for bit-vector operations, while many comparable verification tools use linear arithmetic to approximate semantics of the program. In addition, it provides a programming interface (described in Section 3) enabling the integration of other interpolating decision procedures, allowing it to take advantage of the continuous performance improvements of interpolating SMT solvers (see, for instance, [12–14]).

We present an evaluation of our implementation in Section 4 and provide a tool to generate additional benchmarks on the website of WOLVERINE.

2 Implementation

The implementation of WOLVERINE is based on the CPROVER framework (written in C++), which also forms the foundation of the verification tools CBMC [15],

³ Source available under a BSD-style license on http://www.cprover.org/wolverine.

SATABS [16], and IMPACT [8]. We describe the implementation of WOLVER-INE using the example program in Figure 1. The program implements Wegner's algorithm, determining how many bits of \mathbf{x} are set to one. The assertion assert($\mathbf{y}\neq\mathbf{x}$) is a naïve safeguard against non-termination. WOLVERINE uses symbolic simulation to construct a reachability tree. To this end, it unwinds the control flow graph of the program until an assertion is reached. The shortest execution trace of our example program reaching the assertion is shown at the bottom of Figure 1. In order to check whether the trace violates the assertion, WOLVERINE transforms it into static single assignment form:

$$\overbrace{(y_1 = x_0) \land (y_1 \neq 0)}^{\mathrm{prefix}} \land \underbrace{(y_2 = y_1 \& (y_1 - 1))}^{\oplus} \land \underbrace{(y_2 = x_0)}^{\neg \ assertion}$$
 (1)

Using slicing, we eliminate the assignments to c, since they are not relevant to the correctness of the program. The effect of negating the asserted condition is that every satisfying assignment of Formula (1) represents a witness for an assertion violation. Note, however, that the formula is unsatisfiable and therefore this execution cannot violate the assertion. Accordingly, the sub-formula tagged "prefix" in Formula (1) represents a set of reachable states (at location 1 in the trace) that is safe with respect to the assertion.

WOLVERINE splits the symbolic representation of the execution trace into n partitions A_1, \ldots, A_n , one for each basic block traversed by the trace. It passes these n formulas on to an *interpolating* decision procedure (by default the builtin algorithm described in [17, 18]), which returns n-1 interpolants I_1, \ldots, I_{n-1} that satisfy the following conditions [8]:

For all 1 ≤ j ≤ n, (I_{j-1} ∧ A_j) implies I_j (with I₀ = true and I_n = false), and
for all 1 ≤ j < n, I_j refers only to SSA variables that occur in A₁,..., A_j as well as in A_{j+1},..., A_n.

The first condition guarantees that each interpolant I_j represents a set of safe states at the respective program location from which no state violating the assertion is reachable via the given trace (i.e., the interpolants and the program statements in the trace form Hoare triples). The second condition above guarantees that the interpolants refer only to SSA variables that are *live* at the corresponding location in the trace. For instance, a valid sequence of interpolants for the formulas $(y_1 = x_0)$, $(y_1 \neq 0)$, $(y_2 = y_1 & (y_1 - 1))$, and $(y_2 = x_0)$ would be $y_1 = x_0$, $(y_1 = x_0) \land (y_1 \neq 0)$, and $(x_0 \neq 0) \land (y_2 \leq x_0 - 1)$.

After mapping the SSA variables back into the original program context, Wolverine annotates the corresponding path in the reachability tree accordingly, e.g., the node ① is labelled $(x \neq 0) \land (y \leq x - 1)$.

Wolverine continues expanding the reachability tree until each leaf is either fully expanded or *covered* by a previously discovered node. A node is covered if its (or one of its predecessors') annotation implies the annotation of a previously discovered node associated with the same program location.⁴ For instance, if

⁴ The fact that interpolation is non-monotonic imposes some restrictions on the covering relation, which are described in more detail in [8, 19].

WOLVERINE annotates a node ② (which succeeds ① in the reachability tree and also corresponds to the program location following the assignment y:=y&(y-1)) with $(x \neq 0) \land (y \leq x-1)$, then ② is covered by ①.

The built-in decision procedure supports bit-vector operations using a limited set of inference rules (such as $(t_1 = t_2 \& t_3) \vdash (t_1 \le t_2)$ and $(1 > t_1) \vdash (t_1 = 0)$ for terms t_i of type unsigned integer). Details are provided in [19, 17]. Moreover, it uses eager bit-blasting and a SAT solver to identify an unsatisfiable core before invoking the "word-level" interpolating decision procedure [19, 18]. If the decision procedure fails to provide an interpolant, WOLVERINE falls back on using the weakest precondition. Finally, using the option --interpolator smt-out, WOLVERINE is able to print the SSA instances in the SMT-LIB format, enabling the generation of benchmarks.

3 Interface for Interpolating Decision Procedures

Wolverine provides a C++ interface for calling external interpolating decision procedures. In order to integrate an external solver into Wolverine, the programmer has to implement a class inheriting from external_interpolatort:

```
class external_interpolatort: public wolver_interpolatort {
...
virtual bool initialise();
virtual bool process_options(const optionst&);
protected:
    virtual bool translate(const expr_listt&)=0;
    virtual decision_proceduret::resultt solve()=0;
    virtual bool read_interpolants(expr_listt&)=0;
};
```

The public methods initialise and process_options provide an opportunity to initialise the external tool and to deal with command line parameters. Wolverine provides the class external_processt, which supports the execution of and communication with command line tools.

The methods translate and read_interpolants are required to convert formulas between the representation used by the external interpolator and expressions in the CPROVER format. CPROVER expressions (represented by the class exprt) are annotated syntax trees with typing information. The class typet is used to store types. WOLVERINE expects the interpolants returned to be typed. Accordingly, an interpolator which discards the typing information needs to restore it before returning a result to WOLVERINE. The CPROVER framework provides support for this task in form of the methods c[pp]_typecheck.

The method solve returns D_UNSATISFIABLE, D_SATISFIABLE, or D_ERROR. In the latter case, WOLVERINE provides the option to fall back on computing interpolants using the weakest precondition. If the instance is satisfiable, the trace represents a valid counterexample and is reported. Otherwise, the method read_interpolants is expected to return in its parameter a sequence of typed expressions which satisfy the conditions stated in Section 2.

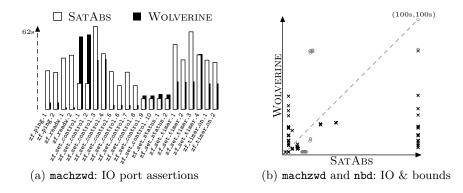


Fig. 2. Performance results Wolverine vs Satabs on DDVerify drivers.

4 Checking Linux Device Drivers

Figure 2 provides a comparison of Wolverine with the predicate-abstraction based verifier Satabs on a number of sequential device driver benchmarks (generated with DDVerify [20], which provides a harness and an OS model annotated with assertions) on a 3GHz Intel Core if CPU with 4GB RAM. Figure 2(a) shows the run-time of Wolverine and Satabs on 22 assertions related to the usage of IO ports for the machzwd device driver [20]. Wolverine performs better than Satabs in all but 6 cases. The performance gain is particularly impressive for the assertions zf_readw.[1,2], for which both tools report a counterexample. We attribute this to the lower overhead of the search algorithm of Wolverine. In the case of the claims zf_set_control.[1,2], we observe a large number of coverage checks in Wolverine for different branches of the reachability tree, and the eager abstraction approach of Satabs prevails.

The scatter-plot in Figure 2(b) shows the run-time for 73 array bound checks for the machzwd driver (displayed using ×) and 78 array bound and IO properties for the driver nbd (indicated by \circ). SATABS exceeded the time-out of 100 seconds for 23 properties of machzwd, and SATABS as well as WOLVERINE timed out in 22 cases for nbd. Our results suggest that, while SATABS is significantly faster when few predicates are sufficient to prove an assertion correct, WOLVERINE's lazy approach is more robust as the number of predicates increases.

5 Conclusion

Wolverine is a freely available implementation of the interpolation-based lazy abstraction algorithm presented in [8]. Its modular design enables the integration of modern interpolating SMT solvers, making it future-proof and (when combined with DDVerify [20]) a valuable source for benchmarks. Our experimental evaluation shows that our implementation is competitive when compared

⁵ Performance results for the device drivers presented in [7] are reported in [19].

to existing predicate-abstraction based verification tools. As future work, we intend to integrate and study the performance impact of different interpolation techniques.

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