# Software Design, Modelling and Analysis in UML

# Lecture 10: Core State Machines II

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#### Contents & Goals

### Last Lecture:

- Core State Machines
- UML State Machine syntax
- State machines belong to classes.

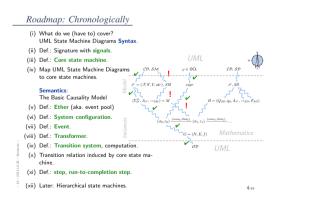
### This Lecture:

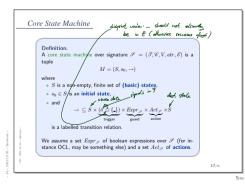
- Educational Objectives: Capabilities for following tasks/questions.
- What does this State Machine mean? What happens if I inject this event?
- Can you please model the following behaviour.
  What is: Signal, Event, Ether, Transformer, Step, RTC.

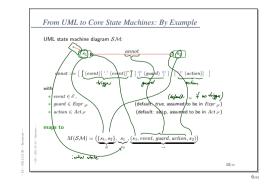
### • Content:

- Ether, System Configuration, Transformer
- Run-to-completion StepPutting It All Together

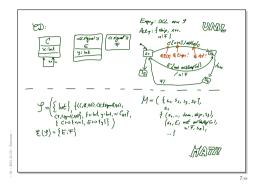
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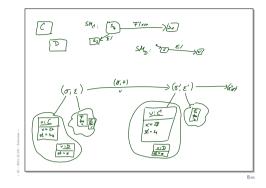






Recall: UML State Machines





The Basic Causality Model

### 6.2.3 The Basic Causality Model [OMG, 2007b, 12]

"Causality model' is a specification of how things happen at run time [...].

The causality model is quite straightforward:

- Objects respond to messages that are generated by objects executing communication actions.
- When these messages arrive, the receiving objects eventually respond by executing the behavior that is matched to that message.
- The dispatching method by which a particular behavior is associated with a given message depends on the higher-level formalism used and

is not defined in the UML specification (i.e., it is a semantic variation point).

The causality model also subsumes behaviors invoking each other and passing information to each other through arguments to parameters of the invoked behavior, [...].

This purely 'procedural' or 'process' model can be used by itself or in conjunction with the object-oriented model of the previous example."

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15.3.12 StateMachine [ОМG, 2007ь, 563]

 Event occurrences are detected, dispatched, and then processed by the state machine, one at a time.
 The semantics of event occurrence

processing is based on the run-tocompletion assumption, interpreted as run-to-completion processing.

 Run-to-completion processing means that an event [...] can only be taken from the pool and dispatched if the processing

of the previous [...] is fully completed.
The processing of a single event occurrence by a state machine is known

as a run-to-completion step. • Before commencing on a run-to-

completion step, a state machine is in a stable state configuration with all entry/exit/internal-activities (but not necessarily do-activities) completed.

# (b, 303) The same conditions apply after the run-to-completion step is completed. Thus, an event occurrence will never be processed [...] in some intermediate and

inconsistent situation. • [IOW,] The run-to-completion step is the passage between two stable configurations of the state machine.

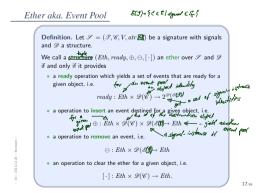
 The run-to-completion assumption simplifies the transition function of the StM, since concurrency conflicts are avoided during the processing of event, allowing the StM to safely complete its run-to-

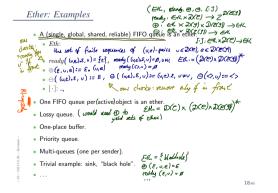
completion step.

## 15.3.12 StateMachine [ОМG, 2007ь, 563]

 The order of dequeuing is not defined, leaving open the possibility of modeling different priority-based schemes.

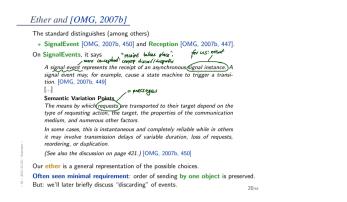


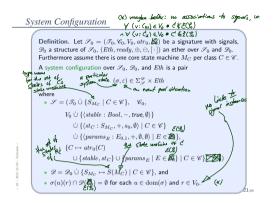




15.3.12 StateMachine [ОМG, 2007ь, 563]

 The order of dequeuing is not defined,
 Run-to-completion may be implemented leaving open the possibility of modeling different priority-based schemes.





System Configuration Step-by-Step

- We start with some signature with signals  $\mathscr{S}_0 = (\mathscr{T}_0, \mathscr{C}_0, V_0, atr_0 / \mathscr{I}_{\bullet})$ .
- A system configuration is a pair (σ, ε) which comprises a system state σ wrt. S (not wrt. S<sub>0</sub>).
- Such a system state  $\sigma$  wrt.  $\mathscr{S}$  provides, for each object  $u \in dom(\sigma)$ ,

values for the explicit attributes in V<sub>0</sub>,

- values for a number of implicit attributes, namely
- a stability flag, i.e.  $\sigma(u)(stable)$  is a boolean value,
- a current (state machine) state, i.e.  $\sigma(u)(st)$  denotes one of the states of core state machine  $M_C$  ,
- a temporary association to access event parameters for each class,
   i.e. σ(u)(params<sub>E</sub>) is defined for each E ∈ 𝔅.
- For convenience require: there is no link to an event except for params<sub>E</sub>.

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#### Stability

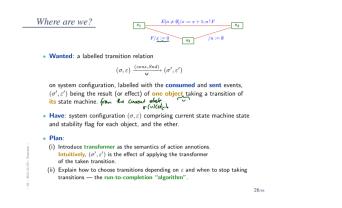
Definition. Let  $(\sigma, \varepsilon)$  be a system configuration over some  $\mathscr{S}_0, \mathscr{D}_0, Eth$ . We call an object  $u \in \operatorname{dom}(\sigma) \cap \mathscr{D}(\mathscr{C}_0)$  stable in  $\sigma$  if and only if  $\sigma(u)(\operatorname{stable}) = true.$ 

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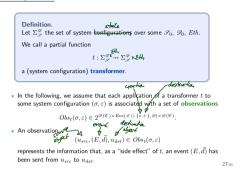
Events Are Instances of Signals	-( <i>T</i> )
Definition. Let $\mathscr{P}_0$ be a structure of the signature with signals $\mathscr{P}_0 = (\mathscr{P}_0, \mathscr{P}_0, V_0, dr_0 \bigotimes)$ and let $E \in \bigotimes_{i=1}^{n} be a signal.$ Let $atr(E) = \{v_1, \dots, v_n\}$ . We call	
$e = (E, \{v_1 \mapsto d_1, \dots, v_n \mapsto d_n\}), \in \mathcal{EG}$ ) × (V_0 $\mapsto \mathcal{D}$ (V	) Wa)
or shorter (if mapping is clear from context) $(E, (d_1, \dots, d_n)) \text{ or } (E, \vec{d}),$ an event (or an instance) of signal $E$ (if type-consistent). We use $Eus(\mathcal{E}_0, \mathcal{R}_0)$ to denote the set of all events of all signals in $\mathscr{S}_0$ wrt. $\mathscr{B}_0$ .	-
As we always try to <u>maximize confusion</u> : • By our existing naming convention, u ∈ 𝒫(E) is also called instance of the (signal) class E in system configuration (σ, ε) if u ∈ dom(σ). • The corresponding event is then (E, σ(u)).	24/65

The idea is the following:
Signals are types (classes).
Instances of signals (in the standard sense) are kept in the system state component of system configurations. (*q*; *l*.).
Identities of signal instances are kept in the ether *E*.
Each signal instance is in particular an event — somehow "a recording that this signal occurred" (*witherd are for its identity*).
The main difference between signal instance and event: Events don't have an identity.
Why is this useful? In particular for reflective descriptions of behaviour, we are typically not interested in the identity of a signal instance, but only whether it is an "*E*" or "*F*", and which parameters it carries.

Signals? Events...? Ether...?!







#### Why Transformers?

 Recall the (simplified) syntax of transition annotations: annot ::= [ (event) ['[' (guard) ']' ] ['' (action)] ]

 Clear: (event) is from & of the corresponding signature.

 But: What are (guard) and (action)?

• UML can be viewed as being parameterized in expression language (providing  $\langle guard \rangle$ ) and action language (providing  $\langle action \rangle$ ).

# • Examples:

Expression Language:
 OCI

Java, C++, ... expressions

Action Language:

· UML Action Semantics, "Executable UML"

Java, C++,  $\ldots$  statements (plus some event send action) .

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# Transformers as Abstract Actions!

In the following, we assume that we're given

- an expression language Expr for guards, and
- an action language Act for actions,

and that we're given

· a semantics for boolean expressions in form of a partial function

#### $I[\cdot](\cdot): Expr \rightarrow (\Sigma_{\mathscr{S}}^{\mathscr{D}} \twoheadrightarrow \mathbb{B})$

#### which evaluates expressions in a given system configuration,

Assuming I to be partial is a way to treat "undefined" during runtime. If I is not defined (for instance because of dangling-reference navigation or division-by-zero), we want to go to a designated "error" system configuration.

• a transformer for each action.

Expression/Action Language Examples

We can make the assumptions from the previous slide because instances exist:

- for OCL, we have the OCL semantics from Lecture 03. Simply remove the pre-images which map to "⊥".
- for Java, the operational semantics of the SWT lecture uniquely defines transformers for sequences of Java statements.
- We distinguish the following kinds of transformers:
- skip: do nothing recall: this is the default action
- send: modifies ε interesting, because state machines are built around sending/consuming events
- create/destroy: modify domain of σ not specific to state machines, but let's discuss them here as we're at it

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update: modify own or other objects' local state — boring

## References

[Harel and Gery, 1997] Harel, D. and Gery, E. (1997). Executable object modeling with statecharts. *IEEE Computer*, 30(7):31–42.

[OMG, 2007a] OMG (2007a). Unified modeling language: Infrastructure, version 2.1.2. Technical Report formal/07-11-04.

[OMG, 2007b] OMG (2007b). Unified modeling language: Superstructure, version 2.1.2. Technical Report formal/07-11-02.